

# Volatile Organic Compound Detection Using Insect Odorant-Receptor Functionalised Field-Effect Transistors

by

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# **Abstract**

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# 1. Introduction

This is a book created from markdown and executable code.

See for additional discussion of literate programming.

[1] 2



## **2. Carbon Nanotube and Graphene Field-Effect Transistors**

### **2.1. Ambipolar Field-Effect Transistors**

#### **2.1.1. Gating**

Back-gating

Liquid-gating

#### **2.1.2. Electrical Properties**



### **3. Carbon Nanotube and Graphene Field-Effect Transistors as Biosensor Platforms**

#### **3.1. Carbon Nanotube Networks & Graphene as Biosensing Transducers**

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##### **3.2.1. Sensor Types**

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##### **3.2.3. Sensing Methods**

Aqueous Environment

Vapour Environment

#### **3.3. Insect Odorant Receptors**

#### **3.4. Non-Specific Binding**

##### **3.4.1. Non-Specific Binding to Channels**

##### **3.4.2. Non-Specific Binding to Apparatus**

Encapsulation

PDMS Well

Reference Electrode



## 4. Fabrication of Carbon Nanotube Network and Graphene Field-Effect Transistors

This chapter discusses the fabrication processes for both the carbon nanotube network and graphene transistors. Experimental optimisation of the transducer element is critical for biosensor work, and large numbers of transducers were required for testing various biosensor functionalisation processes. Therefore, these processes were developed to rapidly fabricate devices with reproducible device characteristics appropriate for biosensing work. Also outlined in this chapter are the characterisation techniques taken to test the quality and reproducibility of these fabrication processes.

The nitrogen ( $\geq 99.99\%$ ) and oxygen (99.7%) used in fabrication work was supplied by BOC Limited New Zealand. Deionised (DI) water was taken from a Synergy® UV Water Purification System. The DI water had a measured conductivity of  $(1.4 \pm 0.1) \mu\text{S cm}^{-1}$ , compared to tap water with a measured conductivity of  $(7.8 \pm 0.2) \mu\text{S cm}^{-1}$ .

### 4.1. Deposition of Carbon Nanotubes

4-inch *p*-type (B-doped) silicon wafers with either a 100 nm or 300 nm SiO<sub>2</sub> layer (Wafer-Pro LLC) were used as the substrate for carbon nanotube network deposition. A 100 nm SiO<sub>2</sub> layer was the preferred option for the devices intended for backgated measurements. Before deposition of carbon nanotubes, the wafers were spin-coated with AZ® 1518 photoresist, placed photoresist-side down onto a cleanroom wipe, fixed in place using vacuum suction, then cleaved into quarters using a diamond-tipped scribe tool. For fabrication performed before June 2023, the protective photoresist layer was then removed by soaking the quarter-wafers in acetone for 15 minutes, then rinsed with isopropyl alcohol (IPA) and dried with N<sub>2</sub> gas. However, for complete removal of photoresist, we found it was necessary to flood expose the wafer with the Karl Suss Aligner for 1 min and then place it in AZ326 developer for 3 min, as discussed further in Section 6.2.2.

Carbon nanotubes were deposited before alignment markers photolithography on all wafers fabricated between Aug 2021-Feb 2023, while devices fabricated before Aug 2021 and after Feb 2023 had alignment markers photolithography performed before the deposition of carbon nanotubes. The process order was first switched in Aug 2021 as this

#### *4. Fabrication of Carbon Nanotube Network and Graphene Field-Effect Transistors*

order led to faster processing times. However, the order was switched back in Feb 2023 to minimise the exposure of carbon nanotubes to photolithographic chemical processes.

##### **4.1.1. Solvent-Based**

The solvent-based deposition process for the carbon nanotube network in the second fabrication protocol is as follows. 10 mg of 2-mercaptopurine (99%, Sigma-Aldrich) was dissolved in 1 ml ethanol by sonication until clear. Quarter wafers were sonicated in acetone for 3 min, then exposed to O<sub>2</sub> plasma at 100 W for at least 2 min in a small plasma cleaner (Plasma Etch, Inc., PE-50 Compact Benchtop Plasma Cleaning System) or reactive ion etcher (Oxford Instruments, Plasmalab® 80 Plus) under 300 mTorr pressure. The cleaned SiO<sub>2</sub>/Si surface was then coated with 2-mercaptopurine for 10 minutes, rinsed with ethanol to remove residual 2-mercaptopurine, and then nitrogen dried. Meanwhile, 5 µg of 99% semiconducting carbon nanotube bucky paper (NanoIntegris, IsoNanotubes S-99) was dispersed in 10 mL of anhydrous 1,2-dichlorobenzene (Sigma Aldrich) by ultrasonication until no particles were visible to the naked eye. During this time, the ultrasonic bath temperature was kept between 20 - 30 °C or the buckypaper would not disperse successfully. The substrates were then placed into a dish with CNT-DCB suspension and left covered for 1 hour, dipped into ethanol for 10 min to remove residual solvent and any unattached carbon nanotube bundles, and then dried with nitrogen.

##### **4.1.2. Surfactant-Based**

Two different approaches were used to attach the surfactant-dispersed CNTs. In both approaches, the quarter wafers were rinsed with ultrapure deionised water (DI water), acetone and IPA before being placed into a reactive ion etcher (Oxford Instruments, Plasmalab 80 Plus) and exposed to O<sub>2</sub> plasma at 100 W for at least 2 min in a small plasma cleaner (Plasma Etch, Inc, PE-50 Compact Benchtop Plasma Cleaning System) or reactive ion etcher (Oxford Instruments, Plasmalab 80 Plus) under 300 mTorr pressure to make the surface hydrophilic. 1 mL of poly-L-lysine (PLL) was immediately deposited onto each quarter wafer and left for 5 minutes. Then, the quarter wafers were rinsed for 30 s with DI water and dried with N<sub>2</sub> gas. This process allows for the surface adhesion of semiconducting single carbon nanotubes suspended in surfactant.

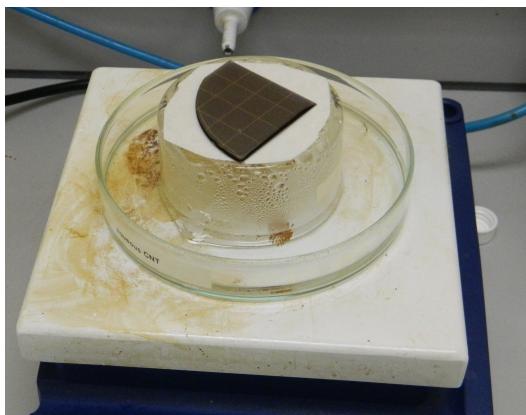
##### **Simple Dropcasting**

2 mL of IsoNanotubes-S 90% or 99% solution (NanoIntegris) was decanted into a small bottle and sonicated for 5 s to break up bundles of CNTs. An even spread of 400 µL CNT solution was placed in the centre of the PLL-functionalised quarter wafer, covered and left for 10 minutes. The CNT solution was then rinsed off with DI water and IPA, and then the quarter wafer was dried with N<sub>2</sub> gas. Next, the quarter wafer was

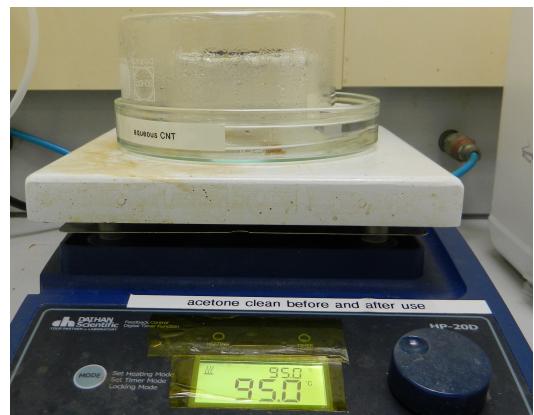
#### 4.2. Photolithography for Carbon Nanotube and Graphene Field-Effect Transistors

annealed in a vacuum oven at 150° C for 1 hour. This method would often lead to an inhomogeneous spread of CNTs across the quarter wafer surface, detailed further in section 6.3.1.

#### Steam-Assisted Method



(a) Steam method setup without steam cover



(b) Steam method setup with steam cover

Figure 4.1.: Photographs of steam-assisted method setup (top and side view).

2 mL of IsoNanotubes-S 90% or 99% solution (NanoIntegris) was decanted into a small bottle and burst-sonicated once (on then off again) to break up bundles of CNTs. 75 mL of 95° C water was then placed into a glass dish on a hotplate held at 95° C. After this, the PLL-functionalised quarter wafer was placed in the centre of an insulating surface on the same hotplate. The CNT dispersion was carefully spread across the surface of the wafer without spilling any over the wafer edges. The wafer on the insulating surface and glass dish were then left under the same cover for 2 minutes to expose the wafer to steam from the glass dish. The use of an insulating surface meant that the wafer and CNT dispersion were not heated from below while exposed to steam. The steam-assisted deposition setup is shown in Figure 4.1.

The CNT dispersion was then rinsed off the wafer with DI water, ethanol, acetone and IPA, and then the quarter wafer was dried with N<sub>2</sub> gas. As in the original method, the quarter wafer was then annealed in a vacuum oven at 150° C for 1 hour. This method gave an even spread of CNTs across the quarter wafer surface, leading to a greater consistency in performance between devices. Further details can be found in Section 6.3.1.

4. Fabrication of Carbon Nanotube Network and Graphene Field-Effect Transistors

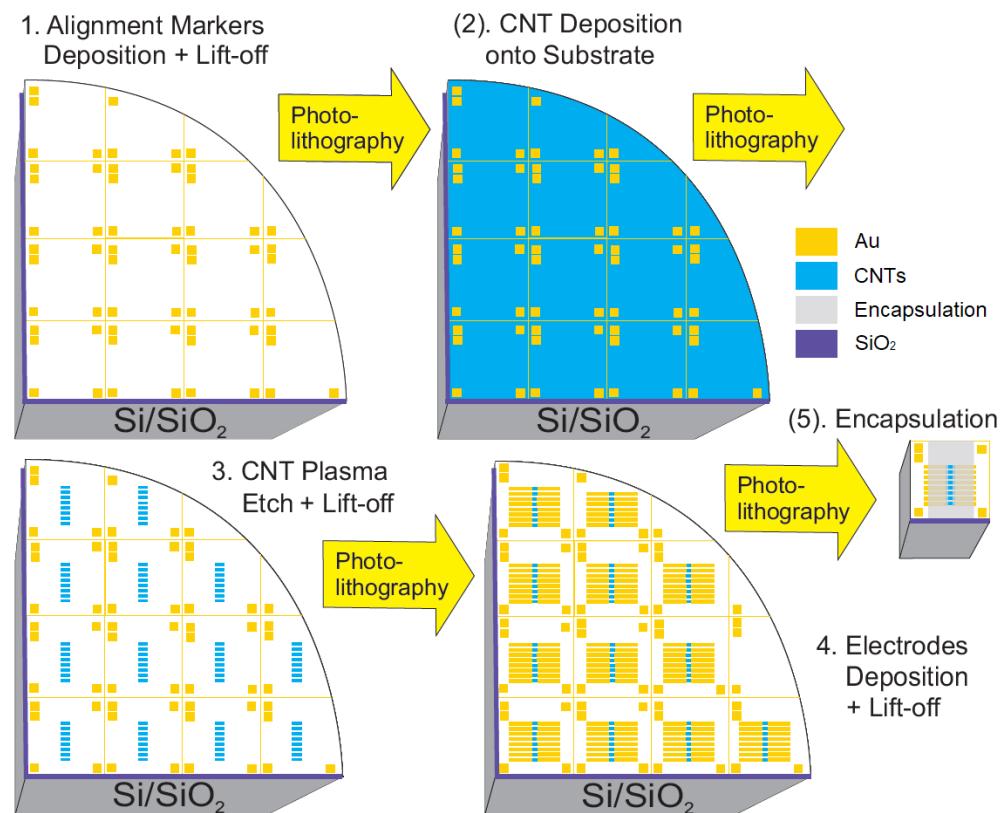


Figure 4.2.: The photolithographic processes used for fabrication of both carbon nanotube and graphene devices (graphene devices were fabricated individually for every step, step #2 skipped for graphene devices)

## 4.2. Photolithography for Carbon Nanotube and Graphene Field-Effect Transistors

Photolithography was used to define eight channel regions on each device and subsequently to define metal contacts for each of these channels. A schematic demonstrating these photolithography processes on a quarter wafer is shown in Figure 4.2.

Thermal evaporation was used when depositing chromium (Cr-plated tungsten rods, Kurt J. Lesker) and gold (Au wire, 99.99%, Regal Castings Ltd.), while electron beam evaporation was used when depositing titanium (Ti pieces, 99.99%, Kurt J. Lesker) and metal oxides (*e.g.* Al<sub>2</sub>O<sub>3</sub> pieces, 99.99%, Kurt J. Lesker). Metal and metal oxide deposition was performed using an Angstrom Engineering Nexdep 200 Vacuum Deposition System. Deposition thickness was controlled using an Inficon Deposition Controller and electron beam power was provided by a Telemark TT-6 power supply. For metals, the chamber was initially evacuated to a pressure  $5 \times 10^{-6}$  mTorr, while for metal oxides the chamber was initially evacuated to a pressure below  $1 \times 10^{-5}$  mTorr. After evaporation, the chamber was cooled and vented with nitrogen.

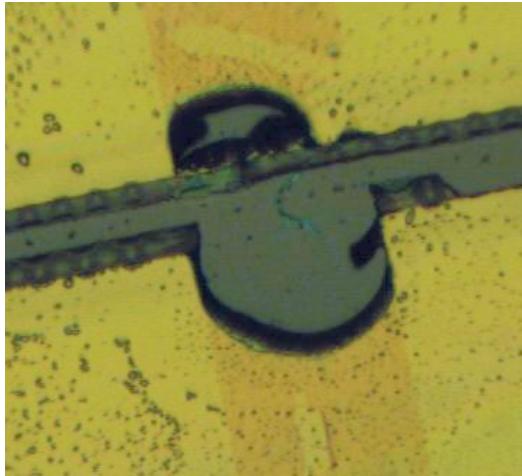
Carbon nanotube devices were fabricated using the quarter wafer substrates discussed in Section 4.1.

Graphene devices were fabricated using 300 nm SiO<sub>2</sub>/p-type Si substrates covered with a monolayer of mechanically transferred CVD graphene (Advanced Chemical Supplier). This substrate was cleaved into equal-sized square chips before photolithography, with side length between 11.6 - 11.7 mm, subject to variability in wafer size. The same cleaving process outlined in Section 4.1 was used for cleaving the chips, but the photoresist was not rinsed off after cleaving. Devices were exposed to a brief burst of N<sub>2</sub> gas to remove any dust from the cleaving process from the surface of devices. When not being used in photolithography, devices were stored in a vacuum desiccator to prevent the quality of the graphene deteriorating with exposure to air over time.

Dimethyl sulfoxide (DMSO) was sometimes used in lift-off processes instead of acetone between Jul 2021 and Feb 2023 because of its effectiveness as a photoresist stripping agent. However, it was abandoned due to some indications it was too aggressive for the devices being fabricated, as shown in Figure 4.3 and also as detailed in Section 6.3.1. It is possible that heat from the electrodes deposition sometimes crosslinked residual photoresist on the nanomaterial, and then during lift-off was removed aggressively together with any attached nanomaterial by the DMSO. However, it is also possible that prolonged exposure to DMSO alone was sufficient to detach nanomaterial from the substrate. Therefore, acetone was the preferred agent for lift-off despite being a less efficient stripping agent than DMSO.

From Jul 2023 onwards, after each photolithography step using negative resist, quarter wafers/chips placed in AZ326 or SU8 developer for 3 min to ensure complete removal of photoresist residue. For each step with positive resist, the same procedure was performed

#### 4. Fabrication of Carbon Nanotube Network and Graphene Field-Effect Transistors



(a) Damage to gold electrode in channel region after DMSO lift-off



(b) Damage to graphene (blue region) after DMSO lift-off

Figure 4.3.: Lift-off with dimethyl sulfoxide sometimes led to damage to regions where nanomaterials were present.

but with a flood exposure with UV light for 1 min before being placed in developer. The results of this process are discussed further in Section 6.2.2.

##### 4.2.1. Alignment Markers

Metal alignment markers were deposited in order to accurately align the device channels with device electrodes in subsequent photolithography steps. These alignment markers were asymmetric to indicate the orientation of the device for subsequent photolithography steps and electrical characterisation. In later discussion, channel 1 is defined as the channel placed closest to the large, double square alignment marker.

For carbon nanotube quarter wafers, alignment markers were deposited either directly before or after carbon nanotube deposition (see Section 4.1 for discussion). For graphene devices, alignment markers were deposited directly after cleaving using the protective photoresist layer spincoated prior to cleaving. AZ® 1518 was used for alignment marker photolithography.

For carbon nanotube devices made before Jun 2022, chromium was used as an adhesive layer for gold, while for all graphene devices and carbon nanotube devices made after Jun 2022, titanium was used as the adhesive layer. For chromium/gold depositions, a nominal 10 nm of chromium was deposited followed by a nominal 100 nm Au layer. For titanium/gold depositions, a nominal 10-20 nm of titanium was deposited followed by a nominal 50 nm Au layer (for independent measurements of metal layer thickness, see Section 4.2.3). Devices were then soaked in acetone for at least 2 hours for photoresist

## 4.2. Photolithography for Carbon Nanotube and Graphene Field-Effect Transistors

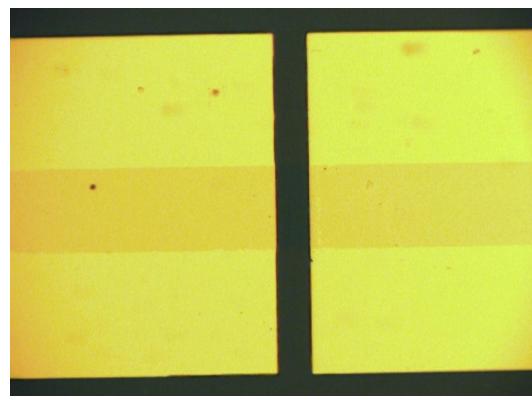
lift-off, washed in IPA and dried with nitrogen. The use of titanium gave rise to a cleaner lift-off and improved gold adhesion. Using a relatively thin gold layer (50 nm nominal instead of 100 nm) proved to still be clearly visible but to a cleaner lift-off.

### 4.2.2. Channel Etching

Eight channel features, each 1000  $\mu\text{m}$  in length and 100  $\mu\text{m}$  in width with a pitch of 1200  $\mu\text{m}$ , were patterned using AZ® 1518 photolithography on each carbon nanotube or graphene-covered substrate. Unwanted nanomaterial not covered with photoresist was then etched away with 200 W oxygen plasma at 600 mTorr using a reactive ion etcher or RIE (Plasmalab® 80 Plus, Oxford Instruments). The etch time was 3 minutes for carbon nanotube quarter wafers, and 1 minute for graphene chips. The protective photoresist was then removed by soaking in acetone for at least 5 minutes.



(a) Graphene channel after photolithographically defined plasma etch



(b) Graphene channel after photolithographically defined electrodes deposition and liftoff

Figure 4.4.: Microscope images of a graphene channel after plasma etch and electrodes photolithography steps.

### 4.2.3. Electrodes

The source and drain electrodes for each channel were patterned using photolithography with either AZ® 1518 photoresist (pre-Mar 2023) or AZ® nLOF 2020 photoresist (post-Mar 2023). Before metal deposition, the developed photoresist pattern was exposed to O<sub>2</sub> plasma at 50 W for up to 5 s or at 20 W for 20-25 s in a PE-50 plasma cleaner (Plasma Etch, Inc.) to remove residual photoresist on the developed regions and ensure a clean lift-off. After metal deposition, wafers/devices were soaked in acetone for at least 2 hours for photoresist lift-off, washed in IPA and dried with nitrogen.

#### 4. Fabrication of Carbon Nanotube Network and Graphene Field-Effect Transistors

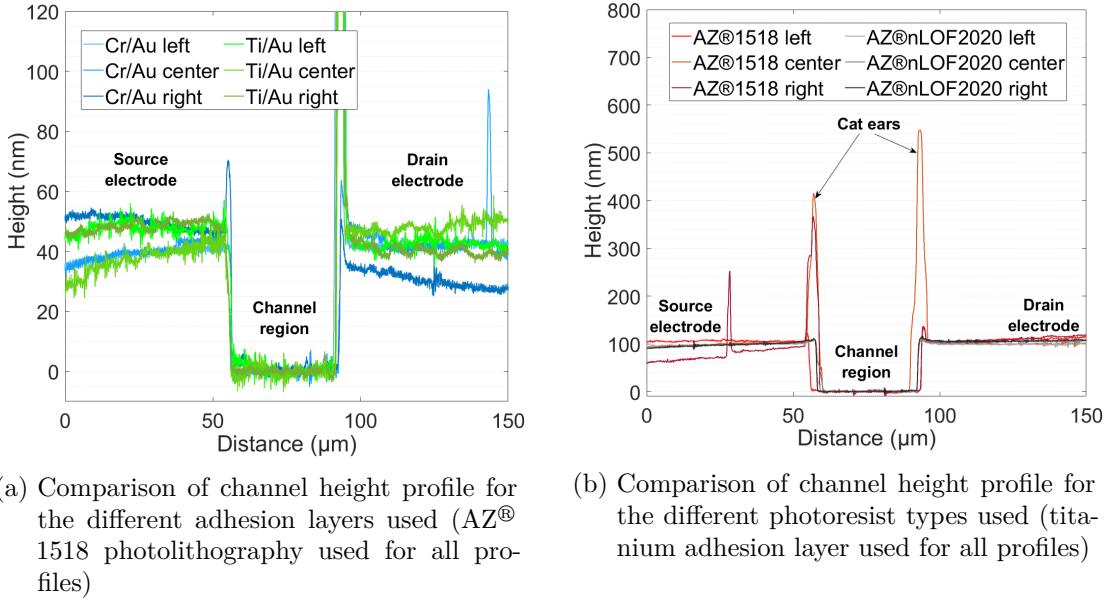


Figure 4.5.: Dektat height profiles taken between the source and drain electrodes across a channel from various quarter wafers with electrodes deposited using different approaches. For each quarter wafer, the profiles of three different channels are shown, selected from different locations across the quarter wafer surface.

As with the alignment markers deposition (see Section 4.2.1), before Jun 2022 chromium was used for the gold adhesion layer, and after Jun 2022 titanium was used. Adhesion layers are required to stick metals such as gold and platinum to silicon dioxide [1]. A 20 nm nominal titanium layer instead of 10 nm nominal was found to give better electrode adhesion, and devices after Feb 2023 were made using this thicker adhesion layer. Good electronic contact could be made with electrodes with a nominal gold layer thickness of 60-100 nm, and a Au layer nominally 100 nm thick was most commonly used.

Example height profiles of chromium layer channels and a titanium layer channels taken using a Veeco Dektat 150 profiler are shown in Figure 4.5a. AZ® 1518 photoresist was used here for photolithographic patterning. A 10 nm adhesion layer and 100 nm Au layer were used for both depositions to ensure a consistent comparison. From Figure 4.5a, we find an measured Cr/Au electrode height of  $42 \pm 1$  nm and an measured Ti/Au electrode height of  $48 \pm 2$  nm, slightly less than half the respective heights stated on the Inficon Deposition Controller.

Although using AZ® nLOF 2020 photolithography involves more processing steps, it gave rise to more cleanly-defined electrodes with a more consistent height profile. Often electrodes deposition using AZ® 1518 photoresist would lead to sharp vertical spikes along the edge of the electrode. These edge spikes or “cat ears” could partially or fully protrude through thin encapsulation materials such as SU8 and Al<sub>2</sub>O<sub>3</sub>, leading to significant leakage currents from the electrodes into the FET top gate. This effect is

## 4.2. Photolithography for Carbon Nanotube and Graphene Field-Effect Transistors

due to the profile of positive resists being suboptimal for lift-off processes, as discussed in Appendix A.

The height profiles corresponding to a wafer with electrodes fabricated using AZ® 1518 and to a wafer with electrodes fabricated using AZ® nLOF 2020 are shown in Figure 4.5b. A 20 nm titanium adhesion layer and 100 nm Au layer were used for both depositions to ensure a consistent comparison, resulting in a measured electrode height of  $103 \pm 2$  nm for both wafers. The wafer which used AZ® nLOF 2020 photoresist clearly has a more consistent electrode height profile across the wafer surface than the wafer which used AZ® 1518 resist. The measured edge features for the AZ® 1518 resist electrodes vary in size from 20 nm to 450 nm above the bulk electrode surface, whereas the edge features for the AZ® nLOF 2020 resist do not exceed 14 nm in height.

### 4.2.4. Encapsulation

Several different approaches were used for the encapsulation, or contact protection, of devices. The encapsulation of graphene and carbon-nanotube transistors for biosensing is essential to improve transistor characteristics, passivate the electrodes and ensure only the nanomaterial region is active during biosensing, as discussed in Section 3.2.3.

Before encapsulation photolithography the carbon-nanotube network quarter wafers were cleaved into individual 12 mm x 12 mm chips, using the cleaving process outlined in Section 4.1. Cleaving the devices at this step simplified mask alignment and ensured consistent thickness across photoresist encapsulated devices.

Two different photolithography masks were used for encapsulation photolithography in this work, with different exposed areas of active nanomaterial. The first mask was used for devices made before Jan 2023, and was designed to leave a region of  $500 \mu\text{m} \times 10 \mu\text{m}$  unencapsulated for each channel. The second mask was used exclusively after Jan 2023, and was designed to leave a region of  $200 \mu\text{m} \times 20 \mu\text{m}$  unencapsulated for each channel. This change was made to double the area of carbon nanotubes exposed to electrolyte while halving the area of SiO<sub>2</sub> dielectric exposed to electrolyte during aqueous sensing.

A side-by-side microscope comparison of hardbaked AZ® 1518 processed with each mask is given in Figure 4.6, while a Dektat profile comparison corresponding to Figure 4.6 is shown in Figure 4.7a. The profiles corresponding to the mask used after Jan 2023 clearly exhibit greater device-to-device consistency, partly due to the mask requiring a greater level of accuracy when aligning the encapsulation pattern with the electrode channel. The larger feature size also means development time has less of an impact on the quality of the encapsulation opening.

#### 4. Fabrication of Carbon Nanotube Network and Graphene Field-Effect Transistors

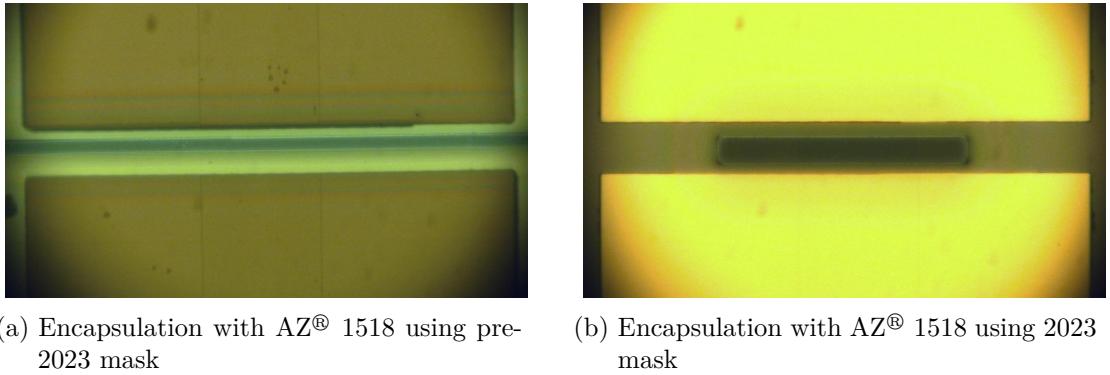


Figure 4.6.: Microscope images of carbon nanotube devices after encapsulation photolithography with hardbaked AZ® 1518.

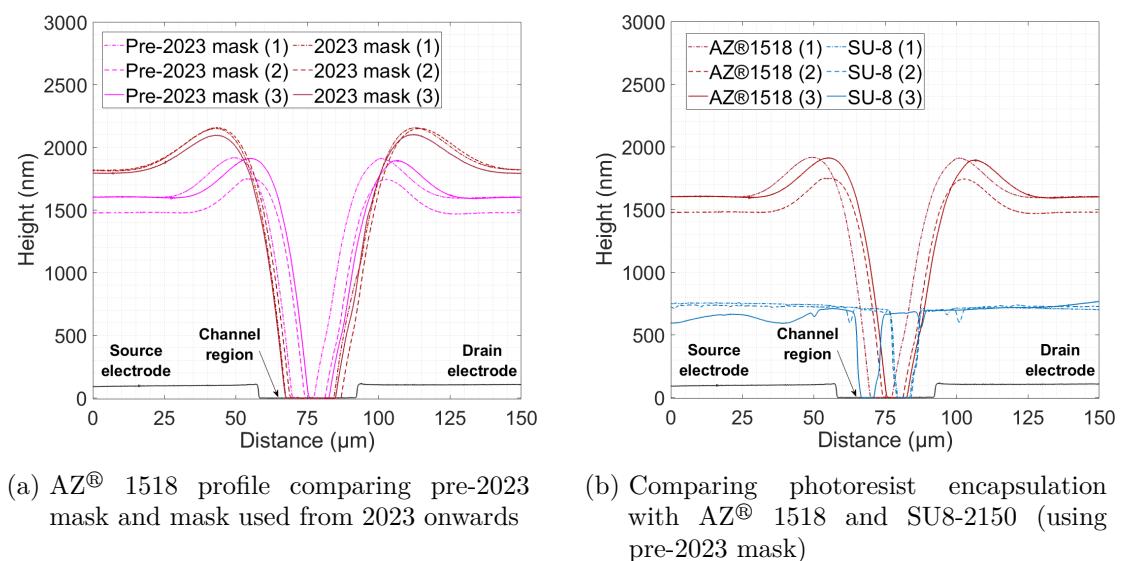


Figure 4.7.: Dektat of carbon nanotube devices after encapsulation photolithography using hardbaked AZ® 1518 and SU-8 2150, taken from various devices.

## 4.2. Photolithography for Carbon Nanotube and Graphene Field-Effect Transistors

### Photoresist encapsulation

Two types of photoresist were initially trialled for encapsulation, AZ® 1518 and SU-8-2150. Both AZ® 1518 [2]–[4] and SU-8 have been previously used for device encapsulation, with SU-8 noted for being particularly stable and biocompatible [5]–[7]. Once developed, the photoresist pattern was exposed to O<sub>2</sub> plasma at 50 W for up to 5 s or at 20 W for 20–25 s to remove excess photoresist from the encapsulation opening. Devices were then hardbaked at 200°C for 1 hour to fully crosslink the encapsulation layer. This crosslinking ensured subsequent device exposure to solvent did not remove the photoresist encapsulation.

The exposed region clear of AZ® 1518 resist was  $6.8 \pm 0.3 \mu\text{m}$  in width when using the old, pre-2023 mask, while the exposed region was  $16.6 \pm 0.4 \mu\text{m}$  when using AZ® 1518 with the new mask from 2023, as seen in Figure 4.7a. As noted previously, the profile was more consistent for the new, 2023 mask between devices than for the old pre-2023 mask.

Compared to AZ® 1518, a relatively thin SU-8 encapsulation layer could be deposited, as seen in Figure 4.7b. Using the pre-2023 mask, the AZ® 1518 encapsulation layer had a height of  $1.7 \pm 0.2 \mu\text{m}$ , while the SU-8 encapsulation layer had a height of  $730 \pm 20 \text{ nm}$ . The SU-8 also had less significant edge features than the AZ® 1518. However, the exposed region was reduced for the SU-8 encapsulation, with an width of only  $3.6 \pm 0.5 \mu\text{m}$ . Photoresist development using SU-8 was significantly more time-sensitive than for the AZ® 1518. This meant when the development time was increased to create a wider encapsulation opening, it was difficult to avoid removing large areas of photoresist across the surface of the encapsulation.

### Dielectric encapsulation

Another approach taken was encapsulation of electrode channels with a dielectric metal oxide/ceramic layer. A electron beam deposition process was used to deposit a 100–150 nm nominal metal oxide layer on devices patterned with the 2023 mask using AZ® nLOF 2020 photoresist. As in Section 4.2.3, the developed photoresist pattern was exposed to O<sub>2</sub> plasma at 50 W for up to 5 s or at 20 W for 20–25 s in a PE-50 plasma cleaner (Plasma Etch, Inc.) before ceramic deposition. Before May 2023, devices were left in TechniStrip® MLO 07 (MicroChemicals) for 5–10 min for lift-off. However, due to concerns over the impact of the constituent chemical DMSO on the nanomaterial region (see Figure 4.3), the lift-off process was altered from May 2023 onwards. After May 2023, devices were soaked in acetone for at least 4 hours and sonicated in clean acetone for 30–60 s to lift-off the photoresist, then washed in IPA and dried with nitrogen.

The initial attempt at fabricating a dielectric encapsulation layer used silicon dioxide as the dielectric. However, silicon dioxide adheres poorly to gold without an metallic adhesive layer present, as shown in Figure 4.8a. Aluminium oxide was chosen as an

#### 4. Fabrication of Carbon Nanotube Network and Graphene Field-Effect Transistors

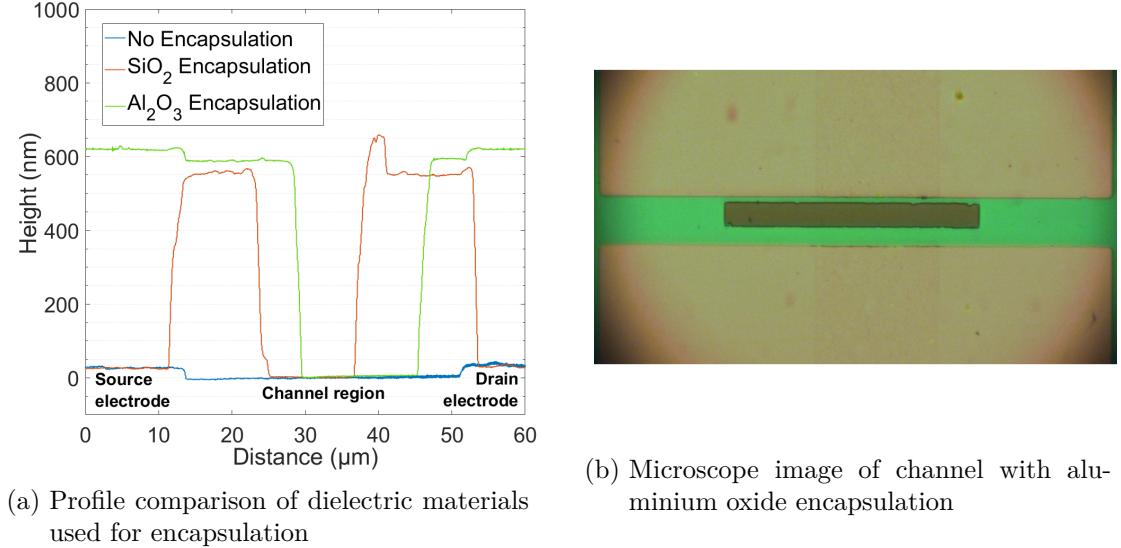


Figure 4.8.: Dektat and microscope image of a device encapsulated with aluminium oxide.

alternative as it sticks well to bulk electrode materials, is heat and chemical resistant, has a relatively high dielectric constant and is bio-compatible [1], [7], [8]. Figure 4.8 shows the aluminium oxide successfully adhered to the electrodes and had a clean profile comparable to that of the SU-8 encapsulation layer after lift-off.

### 4.3. Characterisation via Fluorescence Microscopy

### 4.4. Electrical Characterisation

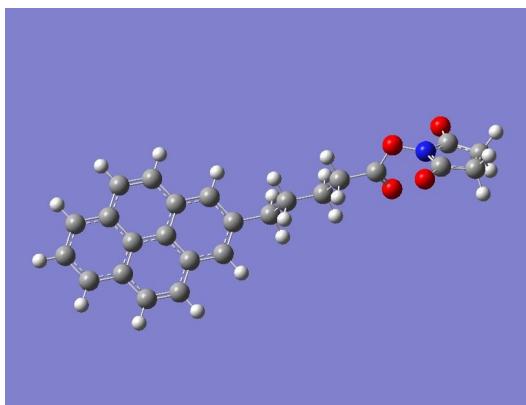
**5.**



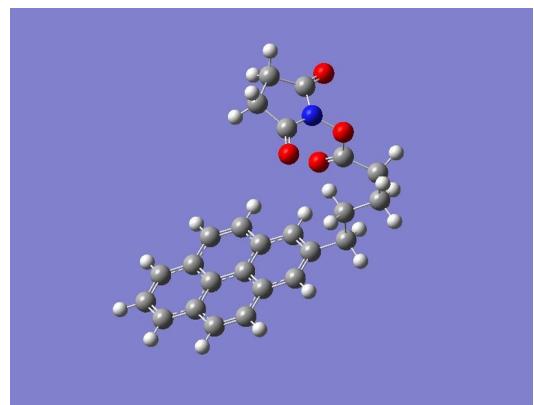
## 6. Functionalisation of Carbon Nanotubes and Graphene with Odorant Receptors

### 6.1. Linker molecules

#### 6.1.1. 1-Pyrenebutanoic acid N-hydroxysuccinimide ester (PBASE)



(a) Hartree-Fock energy: -3427728.67 kJ/mol  
(9 s.f.)



(b) Hartree-Fock energy: -3427729.66 kJ/mol  
(9 s.f.)

Figure 6.1.: Two conformations of PBASE molecule with geometry optimised via *ab initio* calculation (computed using Gaussian 16 [9]). The difference between computed Hartree-Fock energies is 1.0 kJ/mol, small enough that the existence of both molecular conformations is physically possible.

1-Pyrenebutanoic acid N-hydroxysuccinimide ester (variously known commercially and in the literature as 1-Pyrenebutyric acid N-hydroxysuccinimide ester, PBASE, PBSE, PASE, Pyr-NHS, PyBASE, PANHS) is a aromatic, bifunctional molecule commonly used for tethering biomolecules to the carbon rings of graphene and carbon nanotubes. The optimised molecular structure of PBASE is shown in Figure 6.1.

The non-covalent functionalisation of proteins onto a single-walled carbon nanotube using PBASE was first reported by Chen *et al.* in 2001 [10]. Two methods for protein functionalisation and immobilisation were successfully used, with the only differences being the solvent used to dissolve the PBASE powder (DMF, methanol) and the final

## *6. Functionalisation of Carbon Nanotubes and Graphene with Odorant Receptors*

concentration of the resulting solutions (6 mM, 1 mM respectively). The lower concentration may have been used for PBASE in methanol as PBASE powder appears to dissolve poorly in methanol at higher concentrations. Cella *et al.*, Campos *et al.*, Zheng *et al.* and Ohno *et al.* all directly cite Chen *et al.* when discussing functionalisation with PBASE [11]–[14]. Other groups using PBASE for graphene or carbon nanotube functionalisation do not explicitly reference Chen *et al.* in their methodology, but it is apparent they often draw on one of these two original methods. This common ancestry becomes apparent from the high frequency of methods detailing the use of 6 mM PBASE in DMF and 1 mM PBASE in methanol, as seen in Table 6.1.

However, despite this shared heritage, it is also apparent from Table 6.1 that there is a large degree of variation in the methods used for PBASE functionalisation. Various electrical characterisation, microscopy and spectroscopy techniques have been used to demonstrate successful functionalisation. However, there has historically been little justification provided for the exact parameters used in the procedure. As noted by Zhen *et al.* and Hinnemo *et al.*, there is more generally a lack of systematic research into formation of pyrene-derivative monolayers on graphene and other carbon nanomaterials, despite the wide use of this chemistry in the literature [15], [16].

We purchased PBASE from two suppliers, Sigma-Aldrich and Setareh Biotech. Sigma recommends DMF and methanol as suitable solvents for dissolving PBASE alongside chloroform and DMSO. Setareh Biotech indicates methanol can be used for dissolving PBASE. The two suppliers have conflicting information for suitable storage of PBASE, with Sigma recommending room temperature storage while Setareh Biotech recommends storage of  $-5$  to  $-30^{\circ}\text{C}$  and protection from light and moisture. Figure 6.2 compares the shapes of NMR spectra of PBASE from each supplier dissolved in DMSO, alongside a blank DMSO spectrum.

### 6.1. Linker molecules

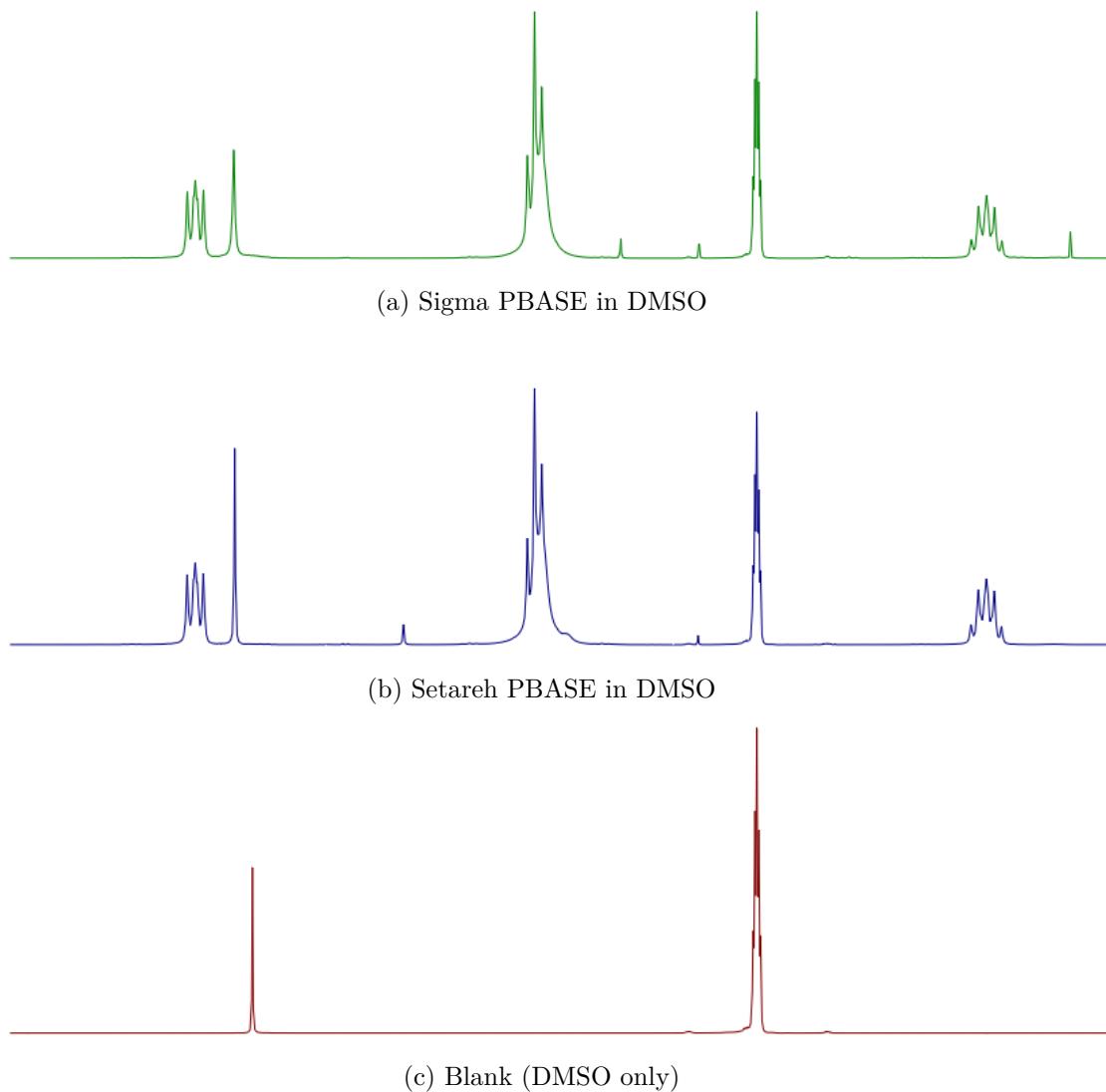


Figure 6.2.: Comparison of NMR spectrum profiles (arbitrary units)

## 6. Functionalisation of Carbon Nanotubes and Graphene with Odorant Receptors

Table 6.1.: Comparison of PBASE functionalisation processes used for immobilisation of proteins and aptamers onto liquid-gated CNTFET and graphene FET sensors

Solvent	Channel	Conc. (mM)	Incubation type	Time (hr)	Rinse steps	References
DMF	CNTs	5	Immersed	1	PBS	Maehashi <i>et al.</i> [17]
		6	Immersed	1	DMF, PBS	García-Aljaro <i>et al.</i> [18]
		6	Immersed	1	DMF	Chen <i>et al.</i> [10]
		6	Immersed	1	DMF	Cella <i>et al.</i> [11]
		6	Immersed	1	DMF	Das <i>et al.</i> [19]
	Graphene	-	-	2	DMF	Kwong Hong Tsang <i>et al.</i> [20]
		-	-	20	-	Wiedman <i>et al.</i> [21]
		0.2	Immersed	20	DMF, IPA, DI water	Gao <i>et al.</i> [22]
		1	100 $\mu$ L droplet	6	DMF, IPA, DI water	Nekrasov <i>et al.</i> [23]
		5	Immersed	1	DMF, DI water	Hwang <i>et al.</i> [24]
		6	6 $\mu$ L droplet	2	DMF, DI water	Nur Nasufiya <i>et al.</i> [25]
		10	10 $\mu$ L droplet	2	DMF, DI water	Campos <i>et al.</i> [12]
		10	Immersed	2	DMF, PBS	Kuscu <i>et al.</i> [26]
		10	Immersed	1	DMF	Xu <i>et al.</i> [27]
		10	Immersed	12	DMF, ethanol, DI water	Khan <i>et al.</i> [28]
2-Methoxyethanol	Graphene	1	Immersed	1	DI water	Ono <i>et al.</i> [29]
Methanol	CNTs	1	Immersed	1	Methanol, DI water	Zheng <i>et al.</i> [13]
		1	Immersed	2	Methanol	Kim <i>et al.</i> [30]
	Graphene	5	Immersed	2	-	Sethi <i>et al.</i> [31]
		5	Immersed	1	Methanol, PBS	Ohno <i>et al.</i> [14]
DMSO	CNTs	10	-	1	DI water	Lopez <i>et al.</i> [32]
		10	Immersed	1	PBS	Strack <i>et al.</i> [33]

*6.2. Verifying Pyrene Attachment to CNT network and graphene*

**6.2. Verifying Pyrene Attachment to CNT network and graphene**

6.2.1. Rhodamine vs FITC

6.2.2. Photoresist contamination

6.2.3. Pyrene-PEG-FITC fluorescence microscopy

Plasma clean comparison

Surfactant comparison

6.2.4. Pyrene-PEG electrical characterisation

**6.3. Functionalisation with PBASE on CNT network**

6.3.1. Varying CNT network fabrication approach

6.3.2. Varying OR-nanodisc preparation process

6.3.3. Varying solvent used

6.3.4. Other functionalisation approaches



## **7. Aqueous sensing**

What I found out.

See for more detailed results



## 8. Vapour Phase Sensing with Transistor Biosensors

### 8.1. Testing Vapour Delivery System

#### 8.1.1. System Description

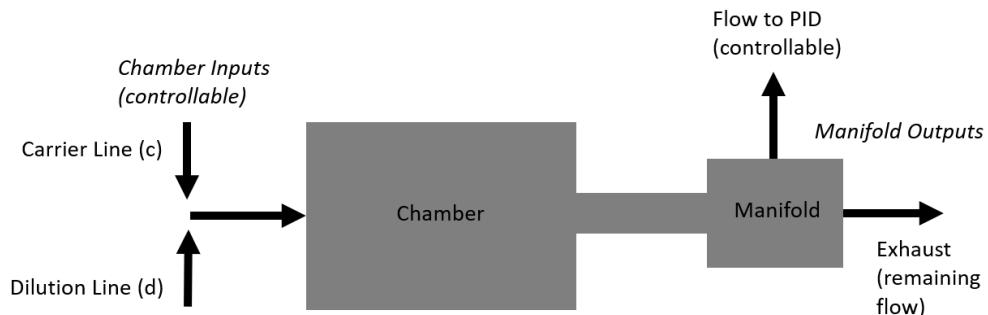


Figure 8.1.: Vapour Delivery System - Schematic of device chamber and manifold

#### 8.1.2. Temperature and Humidity Indicator

#### 8.1.3. Photoionisation Detector

##### Bubbling Vapour

First year report: ““First, a 200 sccm flow of N<sub>2</sub> gas was sent through the dilution line to the device chamber until 1000 s. Then, the flow controller three-way valves were manually adjusted so that the same 200 sccm flow was directed through 50 mL of EtOH analyte in the carrier line. This continued until 2200 s, where the valves were again manually adjusted so that 200 sccm clean N<sub>2</sub> again flowed through the device chamber. The resulting current across the device channel was monitored over this time, and is shown in Figure 19. A response to EtOH exposure and removal is visible.””



## 9. Summary

In summary, this book has no content whatsoever.

[1] 2



## A. Photolithography

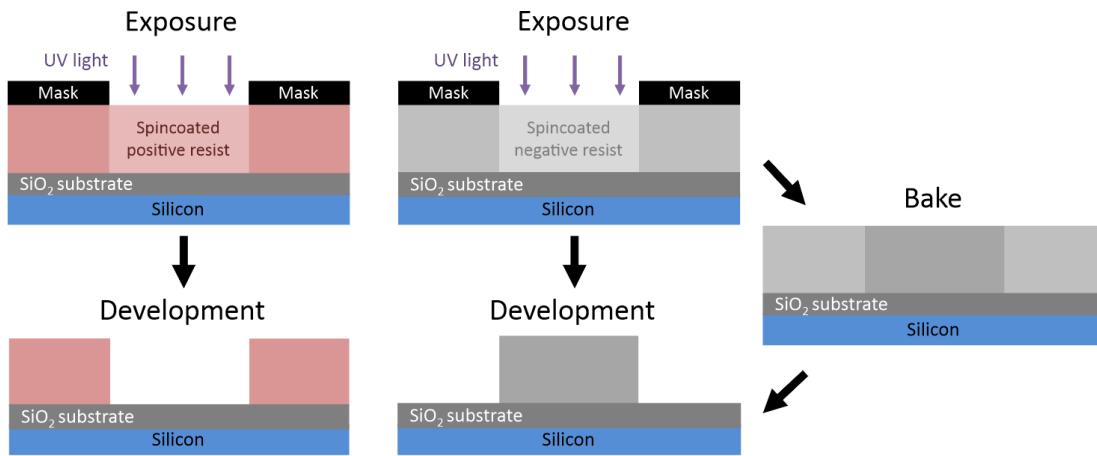


Figure A.1.: A side-view comparison of generic photolithography processes for positive and negative resists in the ideal case. Photolithography with a positive resist requires a single softbake step before exposure, while for negative resists a second baking step is required after exposure (Thicknesses shown not to scale).

This section details some of the standard photolithography procedures used in the device fabrication processes detailed in Chapter 4. Photoresists, also referred to here as “resists”, are UV light-sensitive polymeric resins used for photolithography. Both positive and negative photoresists were used in various fabrication processes. Positive resists are made soluble in alkalines by UV light exposure, meaning exposed areas are removed in the development process. Conversely, negative resists are cross-linked by exposure and a post-exposure bake step. The unexposed areas of the negative resist are then removed in the development process [34]. Figure A.1 gives a visual representation of these differences.

The specific photoresist selected for photolithography depends on the specific use case. The types used in this thesis are positive and negative AZ® photoresists (AZ® 1518, Microchemicals GmbH; AZ® nLOF 2020, Microchemicals GmbH) and SU-8 (SU8-2150, Kayaku Advanced Materials, formerly Microchem). The AZ® resists used here have a minimum film thickness of  $1.5 \mu\text{m}$  [34], while the SU8-2150 has a minimum film thickness of  $0.5 \mu\text{m}$  [35]. Positive resists which have not been thermally crosslinked will soften at higher temperatures ( $\gtrsim 100^\circ\text{C}$  for AZ® 1518), leading to a rounded profile. This is not

### *A. Photolithography*

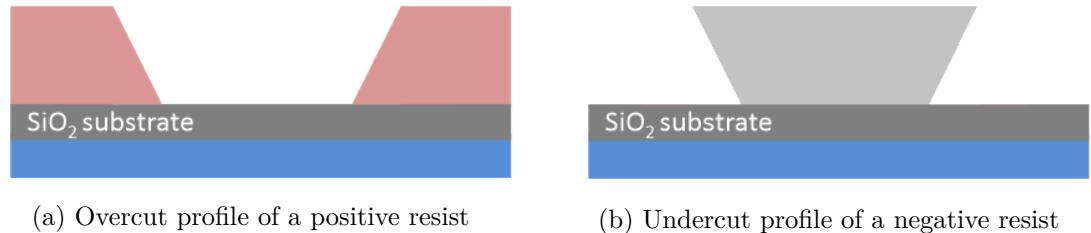


Figure A.2.: Two different resist profiles seen for different types of photoresist. The undercut profile is ideal for thin-film metal deposition and subsequent patterned removal, known as “lift-off”.

the case for negative resists, which are more thermally stable [34]. Each resist therefore has a different cross-section profile, as shown in Figure A.2.

If metal deposition is performed on a positive resist, some metal can collect on the outwardly-sloped sidewalls of the resist (see Figure A.2) which forms significant spikes on the edges of the deposited metal upon lift-off. On the other hand, metal cannot collect on top of the inwardly-sloped negative profile sidewalls, which avoids the formation of large edge spikes. Therefore, the negative resist profile is more suited to metal or metal oxide deposition and lift-off processes, though the process is more sensitive to human error due to requiring more processing steps than positive resist [34]. Finally, when it is suitably processed SU-8 is considered to be more stable and biocompatible than other photoresists [7]. It is especially biocompatible when chemically modified via processes such as isopropanol sonication and O<sub>2</sub> plasma treatment [6].

All photolithographic exposure was performed on a Karl Suss MJB3 Contact Aligner with a USHIO super-high pressure 350 W mercury lamp (USH-350DS, Japan). When performing lithography, the intensity reading from the aligner was 20.8 - 24.2 mW/cm<sup>2</sup> (Note however that an external photometer reading at 400 nm found an intensity output of 17.2 mW/cm<sup>2</sup> when the aligner read 21.0 mW/cm<sup>2</sup>).

In general, photolithography procedures should be performed under yellow lighting, as light wavelengths from 320-450 nm can promote reactions in the photoresist used. Aging of photoresist over time can also significantly affect the photolithography process, and therefore all processes should be re-optimised regularly over time to give the desired result [34]. The range in processing times for some steps of the processes used here are largely due to the effects of aging on the photoresist.

The step-by-step processes for each resist are detailed in the subsequent sections.

## A.1. AZ® 1518 photoresist

1. Spincoat at a final speed of 4000 rotations per minute (rpm) for 1 minute, with an initial acceleration of 500 rpm/s (notes: clean the substrate with acetone, isopropanol (IPA) and nitrogen before spincoating; use only the minimum amount of photoresist required to fully cover the wafer surface; avoid any gaps or bubbles in the photoresist).
2. Softbake 2-4 minutes at 95°C on the hotplate (2 min for individual devices, 4 min for a quarter wafer)
3. Mask expose for 10-12 s (note: clean mask with acetone/IPA and N<sub>2</sub> dry before use)
4. Develop with 3 parts AZ® 326 (2.38 % TMAH metal-ion free developer, Microchemicals GmbH) in 1 part deionised (DI) water for 30-45 s (note: rinse for 10-15 s in one development solution, then perform the rest of the development in clean developer for a cleaner profile)
5. Rinse device for 30 s in DI water to remove excess developer, then dry under nitrogen

## A.2. AZ® nLOF 2020 photoresist

1. Spincoat at final speed of 3000 rotations per minute (rpm) for 1 minute, with an initial acceleration of 500 rpm/s (notes: clean the substrate with acetone, isopropanol (IPA) and nitrogen before spincoating; avoid any gaps or bubbles in the photoresist)
2. Softbake for precisely 60 s at 110°C on the hotplate
3. Mask expose for 2.7-3 s (note: clean mask with acetone/IPA and N<sub>2</sub> dry before use)
4. Post-exposure bake for precisely 60 s at 110°C on the hotplate to cross-link exposed resist
5. Develop with 3 parts AZ® 326 in 1 part DI water for 60-70 s (note: rinse for 30 s in one development solution, then perform the rest of the development in clean developer for a cleaner profile)
6. Rinse device for 30 s in DI water to remove excess developer, then dry under nitrogen

*A. Photolithography*

**A.3. SU8-2150 photoresist**

1. SU-8 was diluted in cyclopentanone until viscosity was low enough to spincoat on substrate and then sonicated at 50°C for 3-4 hours (Note: The dilution ratio used was ~1 part SU-8 to 5 parts cyclopentanone. However, the age of the SU-8 may mean that significant evaporation had occurred prior to use, and the amount of SU-8 actually present is underrepresented by this ratio)
2. Spincoat first with a final speed of 500 rpm (acceleration 500 rpm/s) for 10 seconds, followed by spincoating at 4000 rpm (acceleration 7500 rpm/s) for 40 s.
3. Softbake for 10 minutes at 95°C on the hotplate
4. Mask expose for 6-8 s (note: clean mask with acetone/IPA and N<sub>2</sub> dry before use)
5. Post-exposure bake for 10 minutes at 95°C on the hotplate to cross-link exposed resist
6. Develop with SU-8 developer (Kayaku Advanced Materials, formerly Microchem) for 10-15 s, then clean in IPA for 30 s, repeat this step once then dry under nitrogen

## B. Python Code for Data Analysis



## C. Vapour Delivery System

### C.1. Technical Notes

Two LabView Virtual Instruments (VIs) were adapted from pre-existing VIs for operating the mass flow controllers and monitoring vapour flow into the device chamber, as well as monitoring temperature and humidity in the vapour delivery system's manifold. These VIs were named “ ” A third VI was developed in parallel which combined the first two Virtual Instruments, alongside allowing the sequence of values to control the mass flow controllers.

From Honours report: “ ” Figure 12 gives the right side of the front panel of the LabView VI sample with vapour.VI, which lets us preset an autonomously-performed vapour sensing sequence. Each row in each array module corresponds to a different step in this sequence. The ‘howManySteps’ module lets us set how many of these steps are performed. The ‘Durations Array’ module determines the length of time in seconds each step is performed over. The ‘Carrier Flows Array’ and ‘Dilution Flows Array’ modules let us set the carrier flow and dilution flow, respectively, in standard cubic centimetres per minute (sccm) through the gas rig at each step. The carrier flow pushes analyte vapour into the vapour-sensing device chamber, while dilution flow is used to modify the flow behaviour of the analyte vapour entering the chamber. The vapour sensing sequence as depicted in Figure 12 was used for all vapour sensing runs in this investigation. At the end of the sequence, the data collected about the vapour sensing process was saved as an .lvm file. “ ”

### C.2. Future Improvements



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