NEELU SARASWAT IBHATLA (SRNSZ)

- 1. (i) A logic variable can take one of 2 values:
 - true/1/high
 - for/se/o/low
 - (ii) A logic gate takes one or more inputs and performs a logic function on them, returning an output.
- (iii) A logic function takes one or more boolean inputs and returns a boolean output.

2. NOT	OR	AND			
a a algebrail					
truth table 5	0 D 0 0 1 1 1 1 1 1 1	0 0 0 0 0 V			

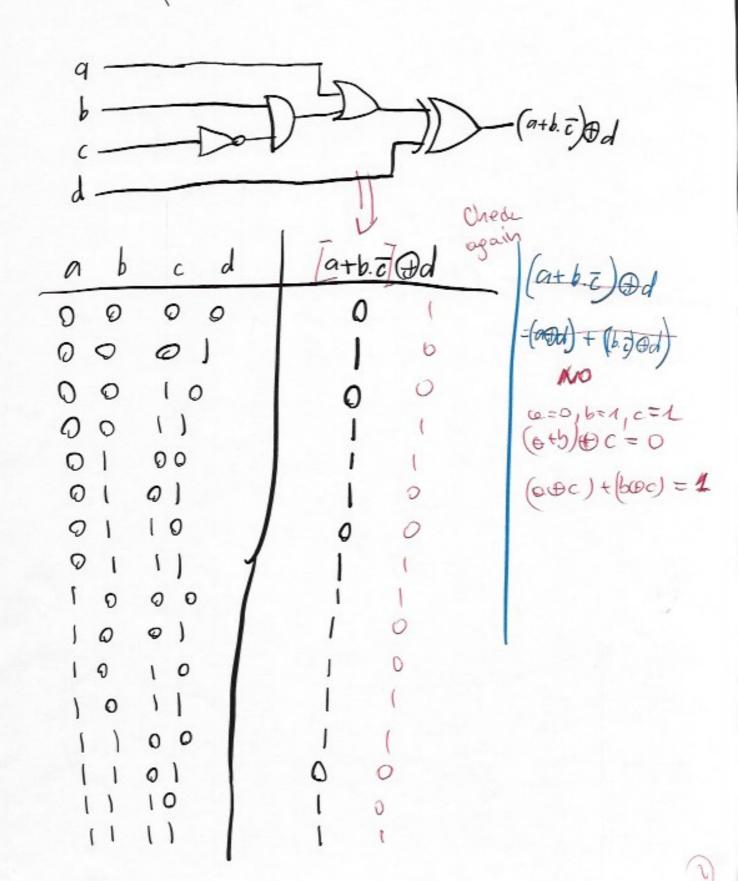
4 mox:
NOR
Do atb

NAND abab a land

a b and

3. A logic function with more than 3 logic gates does make than 3 simple operations.

Example: (a+b.) + d

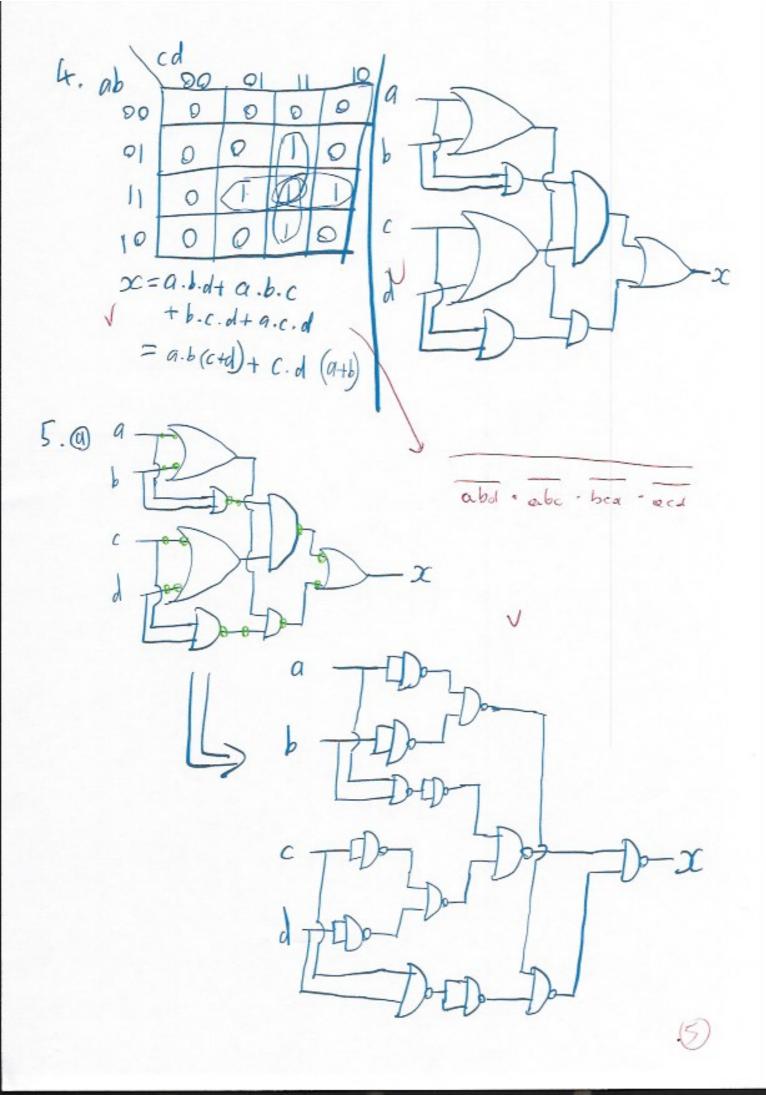


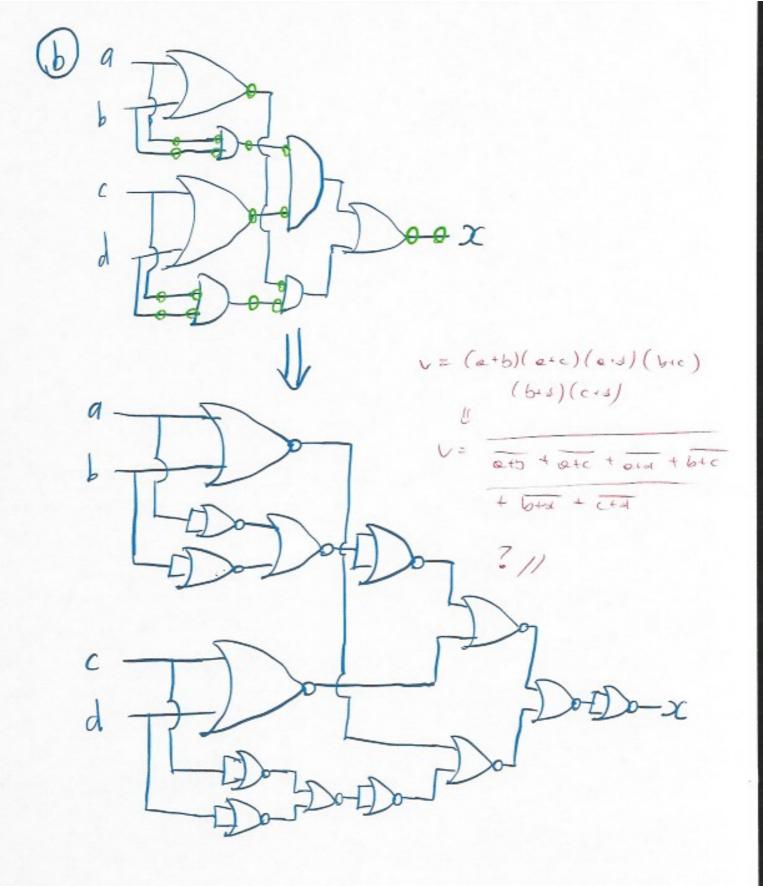
4. 1.0 a b
$$\bar{a}$$
 \bar{a} \bar{b} \bar{b} $\bar{b$

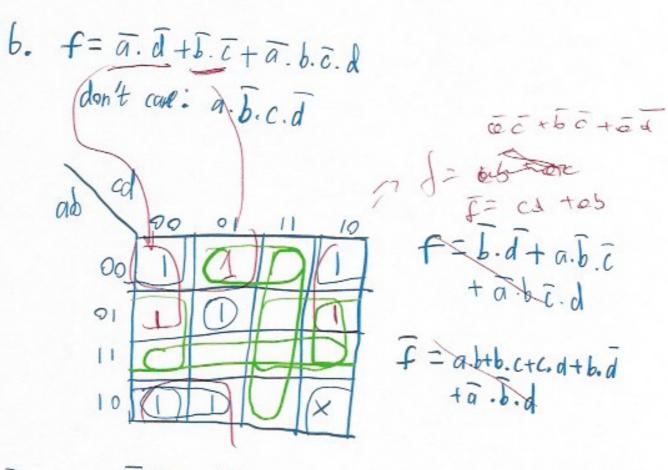
2. (a) LHS=a.b.c+a.b.c = (a.b).
$$(c+\overline{c}) = (a.b).1 = a.b = RHSV$$

(b) LHS=a. $(\overline{a}+b) = a.\overline{a}+a.b=0+a.b=a.b=RHSV$
(c) LHS=a.b+ $\overline{a}.c=(a.b.c+a.b.c+\overline{a}.b.c+\overline{a}.b.c+\overline{a}.b.c$
= a.b. $(c+\overline{c})+\overline{a}.c.(b+\overline{b})+b.c(a+\overline{a})$
= $(a.b+\overline{a}.c+b.c)+a.\overline{a}$
= $(a.b+\overline{a}.c+b.c)+a.\overline{a}$
= $(a+0).(\overline{a}+b)=RHSV$

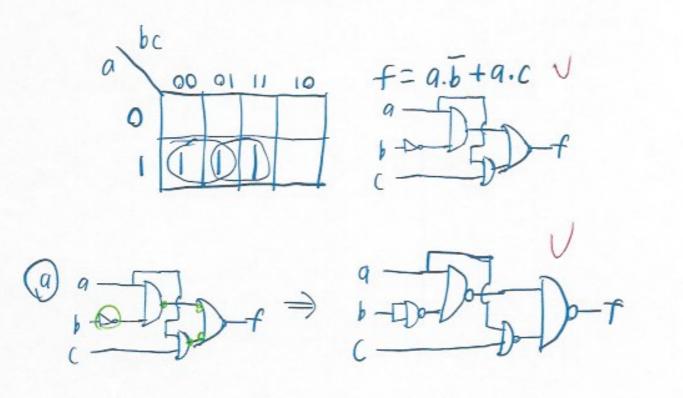
$$\begin{array}{ll}
\text{LHS} &= (a+c) \cdot (a+d) \cdot (b+c) \cdot (b+d) \\
&= (a+c) \cdot (a+d) \cdot (b+c) \cdot (b+d) \\
&= (a+a,c+a,d+c,d) \cdot (b+b,c+b,d+c,d) \\
&= (a+c,d) \cdot (b+c,d) \\
&= (a+b+c) \cdot (b+c,d) \\
&= (a+b+c) \cdot (b+c,d) \cdot (a+b+c) \cdot (a+b+$$



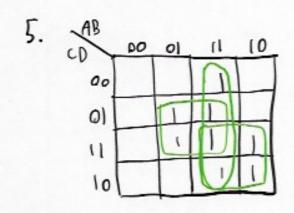




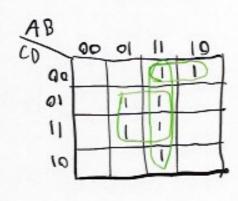
7. $f = \overline{a.b.c} + \overline{a.b.c} + \overline{a.b.c}$



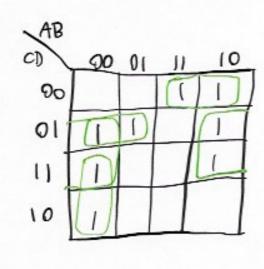
3

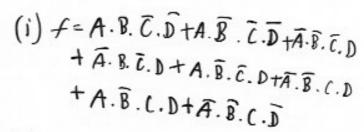


- (i) f=A.B.C.D+A.B.C.D+A.B.C.D +A.B.C.D+A.B.C.D+A.B.C.D +A.B.C.D+A.B.C.D
- (ii) = A. B+B. D+A.C



- (i) f=A.8. C.D+A.B. C.D+A.B. C.D +A.B. C.D+ A.B. (.D+A.B. C.D +A.B. C.D
 - (ii) += A.B+B.D+A.C.D

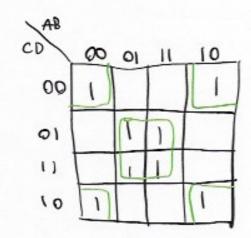




(ii)
$$f = A \cdot \overline{C} \cdot \overline{D} + \overline{A} \cdot \overline{C} \cdot D + \overline{B} \cdot D + \overline{A} \cdot \overline{B} \cdot C$$

$$= \overline{C} (A \cdot \widehat{D} + \widehat{A} \cdot D) + \overline{B} (D + \overline{A} \cdot C)$$

$$= (A \oplus D) \cdot \overline{C} + \overline{B} (D + \overline{A} \cdot C)$$



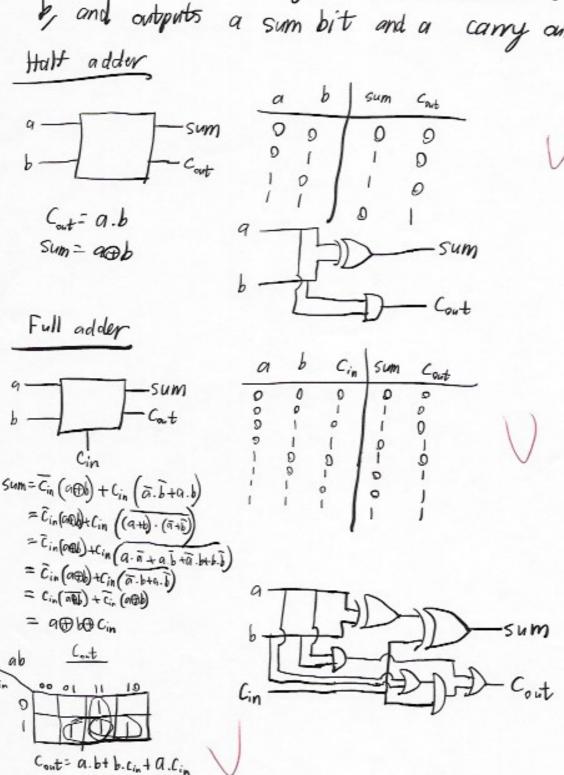
(i) $f = \overline{A} \cdot \overline{B} \cdot \overline{C} \cdot \overline{D} + A \cdot \overline{B} \cdot \overline{C} \cdot \overline{D} + \overline{A} \cdot \overline{B} \cdot \overline{C} \cdot \overline{D}$

(iii) CDAB	00	9	Ц	10	~
00			0		
01	D		0	0	I
n n	0	0	0	0	
10			0		

$$\frac{\overline{f} = A.B + C.D + \overline{B}.D + A.D}{\overline{f} = (\overline{A} + \overline{B}).(\overline{C} + \overline{D}).(B + \overline{D}).(\overline{B} + \overline{D})}$$

$$f = (\overline{A} + \overline{B}) \cdot (\overline{C} + \overline{D}) \cdot (B + \overline{D}) \cdot (\overline{A} + \overline{D})$$

b. F(A, B, C, D)= &m(0,2,5,6,7,8,10,12,13,14,15) Implication Table Column Column 2 Column 3 00-0米 0000 V OK -000 * 206 00 10 V 01014 1000V 1-10* 1010V 1211001 -0-0 01110 1101 1110V ANS: 80 +30+ CO+40 1111 Prime Implicant Chart 13 10 12 - F(A, B, C, D) 5 8,100 (00-0) -A.B. D+B. C.D + 6.00.0 +A. C. D+B. C 8.12 10,14 (1-10 AA.B 6,7,14,15(-11 12/13/14/15(117. A half adder takes 2 input bits (a and b), and outputs a sum bit and a corry out bit. A full adder takes a carry in bit in addition to a and b, and outputs a sum bit and a carry out bit.



= a.b+cin(a+b)