Certified that the project work entitled “**Design and Implementation of Inset Feed Microstrip Antenna for 5G Applications**” carried out by **Lone Faizan Ahmad** (1DS20EC091)**, U. Devendra Kumar** (1DS20EC226)**, Y. V. Gireesh Kumar** (1DS20EC243)**, Sai Suhas Y** (1DS20EC421) are bonafide students of the ECE Dept. of Dayananda Sagar College of Engineering, Bangalore, Karnataka, India in partial fulfillment for the award of Bachelor of Engineering in Electronics & Communication Engineering of the Visvesvaraya Technological University, Belagavi, Karnataka during the academic year 2023-24. It is certified that all corrections/suggestions indicated for project work have been incorporated in the report deposited to the ECE department, the college central library & to the university. This final year project report **(Course Code : 19EC8ICPR2)** Phase-II has been approved as it satisfies the academic requirement in respect of project work prescribed for the said degree.

Dept. Project Coordinators (Section incharges) Project Guide Dr Abhishek – Dr Suma / Dr Manasa / Srividya / Bindu Dr.Ninu Rachel Philip

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Dr. T.C.Manjunath, Ph.D. (IIT Bombay) Principal, DSCE

External Project Viva-Voce

Name of the project examiners (int & ext) with date :

1. : Signature :
2. : Signature :