

# serial\_asm.asm

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1; Serial C/ASM Mix Example
2; Jason Losh
3
4; -----
5; Hardware Target
6; -----
7
8; Target Platform: EK-TM4C123GXL Evaluation Board
9; Target uC:      TM4C123GH6PM
10; System Clock:   40 MHz
11
12; Hardware configuration:
13; Red LED:
14;   PF1 drives an NPN transistor that powers the red LED
15; Green LED:
16;   PF3 drives an NPN transistor that powers the green LED
17; Pushbutton:
18;   SW1 pulls pin PF4 low (internal pull-up is used)
19; UART Interface:
20;   U0TX (PA1) and U0RX (PA0) are connected to the 2nd controller
21;   The USB on the 2nd controller enumerates to an ICD1 interface and a virtual COM port
22;   Configured to 115,200 baud, 8N1
23
24; -----
25; Device includes, defines, and assembler directives
26; -----
27
28 .def waitPbPress
29 .def putcUart0
30 .def putsUart0
31 .def getcUart0
32
33; -----
34; Register values and large immediate values
35; -----
36
37.thumb
38.text
39GPIO_PORTF_DATA_R      .field  0x400253FC
40UART0_FR_R             .field  0x4000C018
41UART0_DR_R             .field  0x4000C000
42
43; -----
44; Subroutines
45; -----
46
47; Blocking function that returns only when SW1 is pressed
48waitPbPress:
49                LDR    R0, GPIO_PORTF_DATA_R    ; get pointer to port F
50                LDR    R0, [R0]                  ; read port F
51                AND    R0, #0x10                  ; mask off all but bit 4
52                CBZ    R0, retry                  ; 0 if bit set test (note: only support 0-126
                    branches)
53                BX     R14                        ; return from subroutine
54retry:         B      waitPbPress
55

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56; Blocking function that writes serial data when the buffer is not full
57putcUart0:
58            LDR    R1, UART0_FR_R        ; get pointer to UART0 FR register
59            LDR    R1, [R1]              ; read FR
60            AND    R1, #0x20              ; mask off all but bit 5 (TX full)
61            CBNZ   R1, retryPutcUart      ; 1 if full
62            LDR    R1, UART0_DR_R        ; get pointer to UART data register
63            STR    R0, [R1]              ; write transmit data
64            BX     LR                    ; return from subroutine
65retryPutcUart: B    putcUart0
66
67; Blocking function that writes a string when the UART buffer is not full
68putsUart0:
69            PUSH   {R4, LR}              ; save R4 and LR (return add to caller of this
function)
70            MOV    R4, R0                ; copy string pointer to R4 where it is safe
before putcUart0 call
71nextPutsUart: LDRB   R0, [R4], #1        ; read next character of string
72            CBZ    R0, donePutsUart      ; if null terminator, exit
73            BL     putcUart0             ; push LR, call putcUart0
74            B      nextPutsUart
75donePutsUart: POP   {R4, PC}            ; pop off R4, pop off return address into PC
(easier than POP LR, BX LR)
76
77; Blocking function that returns with serial data once the buffer is not empty
78getcUart0:
79            LDR    R0, UART0_FR_R        ; get pointer to UART0 FR register
80            LDR    R0, [R0]              ; read FR
81            AND    R0, #0x10              ; mask off all but bit 4 (RX empty)
82            CBNZ   R0, retryGetcUart     ; 1 if empty
83            LDR    R0, UART0_DR_R        ; get pointer to UART data register
84            LDR    R0, [R0]              ; read received data
85            AND    R0, #0xFF              ; mask off all but bits 0-7
86            BX     R14                   ; return from subroutine
87retryGetcUart: B    getcUart0
88
89.endm
90

```