

freq_time.c

```
1 // Frequency Counter / Timer Example
2 // Jason Losh
3
4 //-----
5 // Hardware Target
6 //-----
7
8 // Target Platform: EK-TM4C123GXL with LCD/Temperature Sensor
9 // Target uC:      TM4C123GH6PM
10 // System Clock:   40 MHz
11
12 // Hardware configuration:
13 // Red LED:
14 //   PF1 drives an NPN transistor that powers the red LED
15 // Green LED:
16 //   PF3 drives an NPN transistor that powers the green LED
17 // Blue LED:
18 //   PF2 drives an NPN transistor that powers the blue LED
19 // Pushbutton:
20 //   SW1 pulls pin PF4 low (internal pull-up is used)
21 // Red Backlight LED:
22 //   PB5 drives an NPN transistor that powers the red LED
23 // Green Backlight LED:
24 //   PE5 drives an NPN transistor that powers the green LED
25 // Blue Backlight LED:
26 //   PE4 drives an NPN transistor that powers the blue LED
27 // LM60 Temperature Sensor:
28 //   AN0/PE3 is driven by the sensor (Vout = 424mV + 6.25mV / degC with +/-2degC uncalibrated
   error)
29 // ST7565R Graphics LCD Display Interface:
30 //   MOSI (SSI2Tx) on PB7
31 //   MISO (SSI2Rx) is not used by the LCD display but the pin is used for GPIO for A0
32 //   SCLK (SSI2Clk) on PB4
33 //   A0 connected to PB6
34 //   ~CS connected to PB1
35 // Frequency counter and timer input:
36 //   FREQ_IN (WT5CCP0) on PD6
37
38 //-----
39 // Device includes, defines, and assembler directives
40 //-----
41
42 #include <stdint.h>
43 #include <stdio.h>
44 #include <stdbool.h>
45 #include <string.h>
46 #include "tm4c123gh6pm.h"
47 #include "graphics_lcd.h"
48 #include "wait.h"
49 #define RED_BL_LED    (*((volatile uint32_t *) (0x42000000 + (0x400053FC-0x40000000)*32 + 5*4)))
50 #define GREEN_BL_LED  (*((volatile uint32_t *) (0x42000000 + (0x400243FC-0x40000000)*32 + 5*4)))
51 #define BLUE_BL_LED   (*((volatile uint32_t *) (0x42000000 + (0x400243FC-0x40000000)*32 + 4*4)))
52
53 #define RED_LED        (*((volatile uint32_t *) (0x42000000 + (0x400253FC-0x40000000)*32 + 1*4)))
54 #define GREEN_LED      (*((volatile uint32_t *) (0x42000000 + (0x400253FC-0x40000000)*32 + 3*4)))
55 #define BLUE_LED       (*((volatile uint32_t *) (0x42000000 + (0x400253FC-0x40000000)*32 + 2*4)))
```

freq_time.c

```

56 #define PUSH_BUTTON  (*((volatile uint32_t *) (0x42000000 + (0x400253FC-0x40000000)*32 + 4*4)))
57
58 //-----
59 // Global variables
60 //-----
61
62 bool timeMode = false;
63 uint32_t frequency = 0;
64 uint32_t time = 0;
65 bool freqUpdate = false;
66 bool timeUpdate = false;
67
68 //-----
69 // Subroutines
70 //-----
71
72 void setCounterMode()
73 {
74     SYSCCTL_RCGCWTIMER_R |= SYSCCTL_RCGCWTIMER_R5; // turn-on timer
75     WTIMER5_CTL_R &= ~TIMER_CTL_TAEN; // turn-off counter before reconfiguring
76     WTIMER5_CFG_R = 4; // configure as 32-bit counter (A only)
77     WTIMER5_TAMR_R = TIMER_TAMR_TAMR_CAP | TIMER_TAMR_TACDIR; // configure for edge count
78     mode, count up
79     WTIMER5_CTL_R = 0; //
80     WTIMER5_IMR_R = 0; // turn-off interrupts
81     WTIMER5_TAV_R = 0; // zero counter for first period
82     WTIMER5_CTL_R |= TIMER_CTL_TAEN; // turn-on counter
83     NVIC_EN3_R &= ~(1 << (INT_WTIMER5A-16-96)); // turn-off interrupt 120 (WTIMER5A)
84 }
85 void setTimerMode()
86 {
87     SYSCCTL_RCGCWTIMER_R |= SYSCCTL_RCGCWTIMER_R5; // turn-on timer
88     WTIMER5_CTL_R &= ~TIMER_CTL_TAEN; // turn-off counter before reconfiguring
89     WTIMER5_CFG_R = 4; // configure as 32-bit counter (A only)
90     WTIMER5_TAMR_R = TIMER_TAMR_TACMR | TIMER_TAMR_TAMR_CAP | TIMER_TAMR_TACDIR; // configure
91     for edge time mode, count up
92     WTIMER5_CTL_R = TIMER_CTL_TAEVENT_POS; // measure time from positive edge to
93     positive edge
94     WTIMER5_IMR_R = TIMER_IMR_CAEIM; // turn-on interrupts
95     WTIMER5_TAV_R = 0; // zero counter for first period
96     WTIMER5_CTL_R |= TIMER_CTL_TAEN; // turn-on counter
97     NVIC_EN3_R |= 1 << (INT_WTIMER5A-16-96); // turn-on interrupt 120 (WTIMER5A)
98 }
99 // Initialize Hardware
100 void initHw()
101 {
102     // Configure HW to work with 16 MHz XTAL, PLL enabled, system clock of 40 MHz
103     SYSCCTL_RCC_R = SYSCCTL_RCC_XTAL_16MHZ | SYSCCTL_RCC_OSCSRC_MAIN | SYSCCTL_RCC_USESYSDIV | (4
104     << SYSCCTL_RCC_SYSDIV_S);
105
106     // Set GPIO ports to use APB (not needed since default configuration -- for clarity)
107     // Note UART on port A must use APB
108     SYSCCTL_GPIOHBCTL_R = 0;
109
110     // Enable GPIO port B, D, E, and F peripherals

```

freq_time.c

```

108     SYSCTL_RCGC2_R = SYSCTL_RCGC2_GPIOB | SYSCTL_RCGC2_GPIOD | SYSCTL_RCGC2_GPIOE |
        SYSCTL_RCGC2_GPIOF;
109
110     // Configure LED and pushbutton pins
111     GPIO_PORTF_DIR_R = 0x0E; // bits 1-3 are outputs, other pins are inputs
112     GPIO_PORTF_DR2R_R = 0x0E; // set drive strength to 2mA (not needed since default
        configuration -- for clarity)
113     GPIO_PORTF_DEN_R = 0x1E; // enable LEDs and pushbuttons
114     GPIO_PORTF_PUR_R = 0x10; // enable internal pull-up for push button
115
116     // Configure three backlight LEDs
117     GPIO_PORTB_DIR_R |= 0x20; // make bit5 an output
118     GPIO_PORTB_DR2R_R |= 0x20; // set drive strength to 2mA
119     GPIO_PORTB_DEN_R |= 0x20; // enable bit5 for digital
120     GPIO_PORTE_DIR_R |= 0x30; // make bits 4 and 5 outputs
121     GPIO_PORTE_DR2R_R |= 0x30; // set drive strength to 2mA
122     GPIO_PORTE_DEN_R |= 0x30; // enable bits 4 and 5 for digital
123
124     // Configure A0 and ~CS for graphics LCD
125     GPIO_PORTB_DIR_R |= 0x42; // make bits 1 and 6 outputs
126     GPIO_PORTB_DR2R_R |= 0x42; // set drive strength to 2mA
127     GPIO_PORTB_DEN_R |= 0x42; // enable bits 1 and 6 for digital
128
129     // Configure SSI2 pins for SPI configuration
130     SYSCTL_RCGCSSI_R |= SYSCTL_RCGCSSI_R2; // turn-on SSI2 clocking
131     GPIO_PORTB_DIR_R |= 0x90; // make bits 4 and 7 outputs
132     GPIO_PORTB_DR2R_R |= 0x90; // set drive strength to 2mA
133     GPIO_PORTB_AFSEL_R |= 0x90; // select alternative functions for MOSI,
        SCLK pins
134     GPIO_PORTB_PCTL_R = GPIO_PCTL_PB7_SSI2TX | GPIO_PCTL_PB4_SSI2CLK; // map alt fns to SSI2
135     GPIO_PORTB_DEN_R |= 0x90; // enable digital operation on TX, CLK
        pins
136
137     // Configure the SSI2 as a SPI master, mode 3, 8bit operation, 1 MHz bit rate
138     SSI2_CR1_R &= ~SSI_CR1_SSE; // turn off SSI2 to allow
        re-configuration
139     SSI2_CR1_R = 0; // select master mode
140     SSI2_CC_R = 0; // select system clock as the clock
        source
141     SSI2_CPSR_R = 40; // set bit rate to 1 MHz (if SR=0 in CRO)
142     SSI2_CRO_R = SSI_CRO_SPH | SSI_CRO_SPO | SSI_CRO_FRF_MOTO | SSI_CRO_DSS_8; // set SR=0,
        mode 3 (SPH=1, SPO=1), 8-bit
143     SSI2_CR1_R |= SSI_CR1_SSE; // turn on SSI2
144
145     // Configure FREQ_IN for frequency counter
146     GPIO_PORTD_AFSEL_R |= 0x40; // select alternative functions for
        FREQ_IN pin
147     GPIO_PORTD_PCTL_R &= ~GPIO_PCTL_PD6_M; // map alt fns to FREQ_IN
148     GPIO_PORTD_PCTL_R |= GPIO_PCTL_PD6_WT5CCP0;
149     GPIO_PORTD_DEN_R |= 0x40; // enable bit 6 for digital input
150
151     // Configure Wide Timer 5 as counter
152     if (timeMode)
153         setTimerMode();
154     else
155         setCounterMode();

```

freq_time.c

```

156
157 // Configure Timer 1 as the time base
158 SYSCTL_RCGCTIMER_R |= SYSCTL_RCGCTIMER_R1; // turn-on timer
159 TIMER1_CTL_R &= ~TIMER_CTL_TAEN; // turn-off timer before reconfiguring
160 TIMER1_CFG_R = TIMER_CFG_32_BIT_TIMER; // configure as 32-bit timer (A+B)
161 TIMER1_TAMR_R = TIMER_TAMR_TAMR_PERIOD; // configure for periodic mode (count
    down)
162 TIMER1_TAILR_R = 0x2625A00; // set load value to 40e6 for 1 Hz
    interrupt rate
163 TIMER1_IMR_R = TIMER_IMR_TATOIM; // turn-on interrupts
164 NVIC_ENO_R |= 1 << (INT_TIMER1A-16); // turn-on interrupt 37 (TIMER1A)
165 TIMER1_CTL_R |= TIMER_CTL_TAEN; // turn-on timer
166 }
167
168 // Frequency counter service publishing latest frequency measurements every second
169 void Timer1Isr()
170 {
171     if (!timeMode)
172     {
173         frequency = WTIMER5_TAV_R; // read counter input
174         WTIMER5_TAV_R = 0; // reset counter for next period
175         freqUpdate = true; // set update flag
176         GREEN_LED ^= 1; // status
177     }
178     TIMER1_ICR_R = TIMER_ICR_TATOCINT; // clear interrupt flag
179 }
180
181 // Period timer service publishing latest time measurements every positive edge
182 void WideTimer5Isr()
183 {
184     if (timeMode)
185     {
186         time = WTIMER5_TAV_R; // read counter input
187         WTIMER5_TAV_R = 0; // zero counter for next edge
188         time /= 40; // scale to us units
189         timeUpdate = true; // set update flag
190         GREEN_LED ^= 1; // status
191     }
192     WTIMER5_ICR_R = TIMER_ICR_CAECINT; // clear interrupt flag
193 }
194
195 //-----
196 // Main
197 //-----
198
199 int main(void)
200 {
201     // Initialize hardware
202     initHw();
203
204     // Turn-on all LEDs to create white backlight
205     RED_BL_LED = 1;
206     GREEN_BL_LED = 1;
207     BLUE_BL_LED = 1;
208
209     // Initialize graphics LCD

```

freq_time.c

```

210  ini tGraphi csLcd();
211
212  // Draw legend
213  setGraphi csLcdTextPosi ti on(0, 0);
214  putsGraphi csLcd("Frequency (Hz)");
215  setGraphi csLcdTextPosi ti on(0, 2);
216  putsGraphi csLcd("Period (us)");
217
218  BLUE_LED = timeMode;
219
220  // Endless loop performing multiple tasks
221  // If frequency is updated, then update the display
222  char str[10];
223  while (1)
224  {
225      if (freqUpdate)
226      {
227          freqUpdate = false;
228          sprintf(str, "%7lu", frequency);
229          setGraphi csLcdTextPosi ti on(0, 1);
230          putsGraphi csLcd(str);
231      }
232      if (timeUpdate)
233      {
234          timeUpdate = false;
235          sprintf(str, "%7lu", time);
236          setGraphi csLcdTextPosi ti on(0, 3);
237          putsGraphi csLcd(str);
238      }
239      if (!PUSH_BUTTON)
240      {
241          timeMode = !timeMode;
242          BLUE_LED = timeMode;
243          if (timeMode)
244              setTimerMode();
245          else
246              setCounterMode();
247          wai tMi crosecond(250000);
248      }
249  }
250 }
251
252

```