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1// Keyboard Example
2// Jason Losh
4//-----
 5// Hardware Target
8// Target Platform: EK-TM4C123GXL Evaluation Board
9// Target uC:
               TM4C123GH6PM
10// System Clock: 40 MHz
11
12// Hardware configuration:
13// Red Backlight LED:
14// PB5 drives an NPN transistor that powers the red LED
15// Green Backlight LED:
16// PE4 drives an NPN transistor that powers the green LED
17// Blue Backlight LED:
18// PE5 drives an NPN transistor that powers the blue LED
19// Blue LED:
20// PF2 drives an NPN transistor that powers the blue LED
21// Green LED:
22// PF3 drives an NPN transistor that powers the green LED
23// Pushbutton:
24 //
      SW1 pulls pin PF4 low (internal pull-up is used)
25// UART Interface:
26//
      UOTX (PA1) and UORX (PA0) are connected to the 2nd controller
27 //
      The USB on the 2nd controller enumerates to an ICDI interface and a virtual COM port
28 //
      Configured to 115, 200 baud, 8N1
29// 4x4 Keyboard
30//
      Column 0-3 outputs on PA6, PA7, PD2, PD3 are connected to cathode of diodes whose anode
  connects to column of keyboard
      Rows 0-3 inputs connected to PE1, PE2, PE3, PF1 which are pulled high
32 //
      To locate a key (r, c), the column c is driven low so the row r reads as low
33
35// Device includes, defines, and assembler directives
37
38#include <stdint.h>
39#include <stdbool.h>
40#include <string.h>
41#include "tm4c123gh6pm.h"
42#include "wait.h"
43#include "kb. h"
44
45#define GREEN_LED
                      (*((volatile uint32_t *)(0x42000000 + (0x400253FC-0x40000000)*32 + 3*4)))
                      (*((volatile uint32_t *)(0x42000000 + (0x400253FC-0x40000000)*32 + 2*4)))
46#define BLUE_LED
47#define PUSH_BUTTON (*((volatile uint32_t *)(0x42000000 + (0x400253FC-0x40000000)*32 + 4*4)))
48
49 //----
50// Subroutines
51//----
53// Blocking function that returns only when SW1 is pressed
54 void waitPbPress()
55 {
```

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56
       while(PUSH_BUTTON);
 57 }
 58
 59// Initialize Hardware
 60 void initHw()
 61 {
       // Configure HW to work with 16 MHz XTAL, PLL enabled, system clock of 40 MHz
 62
 63
       SYSCTL_RCC_R = SYSCTL_RCC_XTAL_16MHZ | SYSCTL_RCC_OSCSRC_MAIN | SYSCTL_RCC_USESYSDIV | (4
   << SYSCTL_RCC_SYSDIV_S);
 64
       // Set GPIO ports to use APB (not needed since default configuration -- for clarity)
 65
       // Note UART on port A must use APB
 66
 67
       SYSCTL\_GPIOHBCTL\_R = 0;
 68
 69
       // Enable GPIO port A, D, E, and F peripherals
       SYSCTL_RCGC2_R = SYSCTL_RCGC2_GPIOA | SYSCTL_RCGC2_GPIOD | SYSCTL_RCGC2_GPIOE |
   SYSCTL_RCGC2_GPI OF;
 71
 72
       // Configure LED and pushbutton pins
 73
       GPIO_PORTF_DIR_R = 0xOC; // bits 3 and 2 are outputs, other pins are inputs
 74
       GPIO_PORTF_DR2R_R = 0x0C; // set drive strength to 2mA (not needed since default
   configuration -- for clarity)
 75
       GPIO_PORTF_DEN_R = 0x1C; // enable LEDs and pushbuttons
 76
       GPIO_PORTF_PUR_R = 0x10; // enable internal pull-up for push button
 77
 78
       // Configure keyboard
 79
       // Columns 0-3 connected through diodes to PA6, PA7, PD2, PD3
 80
       // Rows 0-3 connected to PE1, PE2, PE3, PF1
       GPIO_PORTA_DIR_R = 0xCO; // bits 6 and 7 are outputs
 81
 82
       GPIO_PORTD_DIR_R \mid = 0x0C; // bits 2 and 3 are outputs
 83
       GPIO_PORTA_DEN_R |= 0xCO; // bits 6 and 7 are digital
 84
       GPIO_PORTD_DEN_R |= 0x0C; // bits 6 and 7 are digital
 85
       GPIO_PORTE_DEN_R \mid= 0x0E; // bits 1-3 are digital
 86
       GPIO_PORTF_DEN_R = 0x02; // bit 1 is digital
 87
       GPIO_PORTE_PUR_R = 0x0E; // enable internal pull-up for rows 0-2
       GPIO_PORTF_PUR_R = 0x02; // enable internal pull-up for row 3
 88
 89
 90
       // Configure UARTO pins
 91
       SYSCTL_RCGCUART_R |= SYSCTL_RCGCUART_RO;
                                                         // turn-on UARTO, leave other warts in
   same status
 92
       GPIO_PORTA_DEN_R |= 3;
                                                         // default, added for clarity
 93
       GPIO_PORTA_AFSEL_R |= 3;
                                                         // default, added for clarity
 94
       GPIO_PORTA_PCTL_R = GPIO_PCTL_PA1_UOTX | GPIO_PCTL_PA0_UORX;
 95
       // Configure UARTO to 115200 baud, 8N1 format (must be 3 clocks from clock enable and
   config writes)
 97
                                                         // turn-off UARTO to allow safe
       UARTO\_CTL\_R = 0;
   programmi ng
 98
       UARTO CC R = UART CC CS SYSCLK;
                                                         // use system clock (40 MHz)
       UARTO_IBRD_R = 21;
                                                         // r = 40 \text{ MHz} / (Nx115.2kHz), set
   floor(r)=21, where N=16
100
       UARTO_FBRD_R = 45;
                                                         // \text{ round(fract(r)*64)=45}
101
       UARTO_LCRH_R = UART_LCRH_WLEN_8; // configure for 8N1 w/o FIFO
102
       UARTO_CTL_R = UART_CTL_TXE | UART_CTL_RXE | UART_CTL_UARTEN; // enable TX, RX, and module
103
       UARTO_IM_R = UART_IM_RXIM;
                                                         // turn-on RX interrupt
104
       NVIC_ENO_R |= 1 << (INT_UARTO-16);
                                                         // turn-on interrupt 21 (UARTO)
```

```
105
106
       // Configure Timer 1 for keyboard service
107
       SYSCTL_RCGCTIMER_R |= SYSCTL_RCGCTIMER_R1;
                                                          // turn-on timer
                                                          // turn-off timer before reconfiguring
108
       TIMER1_CTL_R &= ~TIMER_CTL_TAEN;
109
       TIMER1 CFG R = TIMER CFG 32 BIT TIMER;
                                                          // configure as 32-bit timer (A+B)
110
       TIMER1\_TAMR\_R = TIMER\_TAMR\_TAMR\_PERIOD;
                                                          // configure for periodic mode (count
   down)
111
       TIMER1_TAILR_R = 0x30D40;
                                                          // set load value to 2e5 for 200 Hz
   interrupt rate
112
       TIMER1_IMR_R = TIMER_IMR_TATOIM;
                                                          // turn-on interrupts
113
       NVIC_ENO_R |= 1 << (INT_TIMER1A-16);
                                                          // turn-on interrupt 37 (TIMER1A)
114
                                                          // turn-on timer
       TIMER1_CTL_R |= TIMER_CTL_TAEN;
115}
116
117// Blocking function that writes a serial character when the UART buffer is not full
118 voi d putcUart0(char c)
119 {
120
       while (UARTO_FR_R & UART_FR_TXFF);
121
       UARTO_DR_R = c;
122}
123
124// Blocking function that writes a string when the UART buffer is not full
125 void putsUart0(char* str)
126 {
127
       int i;
128
       for (i = 0; i < strlen(str); i++)
129
         putcUart0(str[i]);
130}
131
132// For each received character, toggle the green LED
133// For each received "1", set the red LED
134// For each received "0", clear the red LED
135 void UartOlsr()
136 {
137
       char c = UARTO_DR_R & OxFF;
       if (c == '1')
138
           GREEN\_LED = 1;
139
140
       if (c == '0')
141
           GREEN\_LED = 0;
142}
143
145// Main
147
148 int main(void)
149 {
150
       // Initialize hardware
151
       initHw();
152
153
       GREEN\_LED = 1;
154
       wai tMi crosecond (250000);
155
       GREEN\_LED = 0;
156
       wai tMi crosecond (250000);
157
158
       // Display greeting
```

```
putsUart0("Keyboard Example\r\n");
159
       putsUartO("Press '0' or '1' to turn LED on and off\r\n");
160
161
       // Poll keyboard
162
163
       char c;
164
       while(1)
165
166
           // Send characters to UARTO if available
           if (kbhit())
167
168
169
               GREEN_LED ^= 1;
               c = getKey();
170
               if (c != 'D')
171
172
                   putcUart0(c);
173
               el se
174
                   putsUart0("\r\n");
175
           // Emulate a foreground task that is running continuously
176
177
           BLUE_LED ^= 1;
           wai tMi crosecond(250000);
178
179
       }
180}
181
```