

## serial\_c.c

```
1 // Serial C/ASM Mix Example
2 // Jason Losh
3
4 //-----
5 // Hardware Target
6 //-----
7
8 // Target Platform: EK-TM4C123GXL Evaluation Board
9 // Target uC:      TM4C123GH6PM
10 // System Clock:   40 MHz
11
12 // Hardware configuration:
13 // Red LED:
14 //   PF1 drives an NPN transistor that powers the red LED
15 // Green LED:
16 //   PF3 drives an NPN transistor that powers the green LED
17 // Pushbutton:
18 //   SW1 pulls pin PF4 low (internal pull-up is used)
19 // UART Interface:
20 //   U0TX (PA1) and U0RX (PA0) are connected to the 2nd controller
21 //   The USB on the 2nd controller enumerates to an ICD1 interface and a virtual COM port
22 //   Configured to 115,200 baud, 8N1
23
24 //-----
25 // Device includes, defines, and assembler directives
26 //-----
27
28 #include <stdint.h>
29 #include <stdbool.h>
30 #include <string.h>
31 #include "tm4c123gh6pm.h"
32
33 #define RED_LED      (*((volatile uint32_t *) (0x42000000 + (0x400253FC-0x40000000)*32 + 1*4)))
34 #define GREEN_LED    (*((volatile uint32_t *) (0x42000000 + (0x400253FC-0x40000000)*32 + 3*4)))
35 #define PUSH_BUTTON  (*((volatile uint32_t *) (0x42000000 + (0x400253FC-0x40000000)*32 + 4*4)))
36
37 //-----
38 // Subroutines
39 //-----
40
41 // Blocking function that returns only when SW1 is pressed
42 extern void waitPbPress();
43
44 // Initialize Hardware
45 void initHw()
46 {
47     // Configure HW to work with 16 MHz XTAL, PLL enabled, system clock of 40 MHz
48     SYSCTL_RCC_R = SYSCTL_RCC_XTAL_16MHZ | SYSCTL_RCC_OSCSRC_MAIN | SYSCTL_RCC_USESYSDIV | (4
49     << SYSCTL_RCC_SYSDIV_S);
50
51     // Set GPIO ports to use APB (not needed since default configuration -- for clarity)
52     // Note UART on port A must use APB
53     SYSCTL_GPIOHBCCTL_R = 0;
54
55     // Enable GPIO port A and F peripherals
56     SYSCTL_RCGC2_R = SYSCTL_RCGC2_GPIOA | SYSCTL_RCGC2_GPIOF;
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56
57 // Configure LED and pushbutton pins
58 GPIO_PORTF_DIR_R = 0x0A; // bits 1 and 3 are outputs, other pins are inputs
59 GPIO_PORTF_DR2R_R = 0x0A; // set drive strength to 2mA (not needed since default
    configuration -- for clarity)
60 GPIO_PORTF_DEN_R = 0x1A; // enable LEDs and pushbuttons
61 GPIO_PORTF_PUR_R = 0x10; // enable internal pull-up for push button
62
63 // Configure UART0 pins
64 SYSCTL_RCGCUART_R |= SYSCTL_RCGCUART_R0; // turn-on UART0, leave other uarts in
    same status
65 GPIO_PORTA_DEN_R |= 3; // default, added for clarity
66 GPIO_PORTA_AFSEL_R |= 3; // default, added for clarity
67 GPIO_PORTA_PCTL_R = GPIO_PCTL_PA1_U0TX | GPIO_PCTL_PA0_U0RX;
68
69 // Configure UART0 to 115200 baud, 8N1 format (must be 3 clocks from clock enable and
    config writes)
70 UART0_CTL_R = 0; // turn-off UART0 to allow safe
    programming
71 UART0_CC_R = UART_CC_CS_SYSCLK; // use system clock (40 MHz)
72 UART0_IBRD_R = 21; // r = 40 MHz / (Nx115.2kHz), set
    floor(r)=21, where N=16
73 UART0_FBRD_R = 45; // round(fract(r)*64)=45
74 UART0_LCRH_R = UART_LCRH_WLEN_8 | UART_LCRH_FEN; // configure for 8N1 w/ 16-level FIFO
75 UART0_CTL_R = UART_CTL_TXE | UART_CTL_RXE | UART_CTL_UARTEN; // enable TX, RX, and module
76 }
77
78 // Blocking function that writes a serial character when the UART buffer is not full
79 extern void putcUart0(char c);
80
81 // Blocking function that writes a string when the UART buffer is not full
82 extern void putsUart0(char* str);
83
84 // Blocking function that returns with serial data once the buffer is not empty
85 extern char getcUart0();
86
87 //-----
88 // Main
89 //-----
90
91 int main(void)
92 {
93     // Initialize hardware
94     initHw();
95
96     putsUart0("Serial Example\r\n");
97     putsUart0("Press '0' or '1'\r\n");
98
99     // Wait for PB press
100    waitPbPress();
101
102    // For each received character, toggle the green LED
103    // For each received "1", set the red LED
104    // For each received "0", clear the red LED
105    while(1)
106    {

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serial\_c.c

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107     char c = getcUart0();
108     GREEN_LED ^= 1;
109     if (c == '1')
110         RED_LED = 1;
111     if (c == '0')
112         RED_LED = 0;
113 }
114 }
115
```