INFO: [HLS 200-0] Workspace /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1 opened at Fri Mar 16 09:57:35 IST 2018

INFO: [HLS 200-100] Execute config\_clock -quiet -name default -period 10 -default=false

INFO: [SYN 201-201] Setting up clock 'default' with a period of 10ns.

INFO: [HLS 200-100] Command config\_clock returned 0; 0 sec.

INFO: [HLS 200-100] Execute open\_platform DefaultPlatform

INFO: [HLS 200-100] Command open\_platform returned 0; 0 sec.

INFO: [HLS 200-100] Execute import\_lib /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/artix7/artix7

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx.lib

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/xilinx\_interface.lib

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/plb46.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/fsl.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/axi4.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/maxi.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/saxilite.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/dsp48.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/ip/xfft.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/ip/dds\_compiler.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/ip/xfir.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx\_old.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx\_vivado.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx.hlp

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/target\_info.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/xilinx\_interface.hlp

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/maxi.hlp

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/saxilite.hlp

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/dsp48.hlp

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/artix7/dsp48e1.hlp

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute get\_default\_platform

INFO: [HLS 200-100] Command get\_default\_platform returned 0; 0 sec.

INFO: [HLS 200-100] Execute config\_chip\_info -quiet -resource {SLICE 15850} {LUT 63400} {FF 126800} {DSP48E 240} {BRAM 270}

INFO: [HLS 200-100] Command config\_chip\_info returned 0; 0 sec.

INFO: [HLS 200-100] Execute config\_chip\_info -quiet -speed medium

INFO: [HLS 200-100] Command config\_chip\_info returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx.hlp

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/target\_info.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/xilinx\_interface.hlp

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/maxi.hlp

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/saxilite.hlp

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/dsp48.hlp

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command import\_lib returned 0; 0.02 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/artix7/artix7.gen

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/virtex.gen

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx.gen

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/plb46.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/fsl.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/axi4.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/nativeAXI4.gen

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/saxilite.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/maxi.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/scripts/xilinxcoregen.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/XilEDKCoreGen.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/ip/dds\_compiler.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/ip/xfft.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0.08 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/ip/xfir.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/ip/util.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0.11 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/dsp48.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0.11 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0.11 sec.

INFO: [HLS 200-100] Execute open\_platform DefaultPlatform

INFO: [HLS 200-100] Command open\_platform returned 0; 0 sec.

INFO: [HLS 200-100] Execute import\_lib /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/artix7/artix7\_fpv6

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx\_fpv7.lib

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx\_hp.lib

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx\_fpv.hlp

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx.hlp

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/target\_info.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/xilinx\_interface.hlp

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/maxi.hlp

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/saxilite.hlp

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/dsp48.hlp

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/artix7/artix7\_hp.hlp

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Command import\_lib returned 0; 0.02 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/artix7/artix7\_fpv6.gen

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx\_fpv6.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute set\_part xc7a35tcpg236-3

INFO: [HLS 200-100] Execute add\_library xilinx/artix7/artix7:xc7a35t:cpg236:-3

INFO: [HLS 200-100] Execute get\_default\_platform

INFO: [HLS 200-100] Command get\_default\_platform returned 0; 0 sec.

INFO: [HLS 200-100] Execute license\_isbetapart xc7a35t

INFO: [HLS 200-100] Command license\_isbetapart returned 1; 0 sec.

INFO: [HLS 200-100] Execute get\_default\_platform

INFO: [HLS 200-100] Command get\_default\_platform returned 0; 0 sec.

INFO: [HLS 200-100] Execute config\_chip\_info -quiet -resource {SLICE 5200} {LUT 20800} {FF 41600} {DSP48E 90} {BRAM 100}

INFO: [HLS 200-100] Command config\_chip\_info returned 0; 0 sec.

INFO: [HLS 200-100] Execute config\_chip\_info -quiet -speed fast

INFO: [HLS 200-100] Command config\_chip\_info returned 0; 0 sec.

INFO: [HLS 200-100] Command add\_library returned 0; 0 sec.

INFO: [HLS 200-100] Execute add\_library xilinx/artix7/artix7\_fpv6

INFO: [HLS 200-100] Execute get\_default\_platform

INFO: [HLS 200-100] Command get\_default\_platform returned 0; 0 sec.

INFO: [HLS 200-100] Command add\_library returned 0; 0.01 sec.

INFO: [HLS 200-10] Setting target device to 'xc7a35tcpg236-3'

INFO: [HLS 200-100] Execute get\_default\_platform

INFO: [HLS 200-100] Command get\_default\_platform returned 0; 0 sec.

INFO: [HLS 200-100] Command set\_part returned 0; 0.06 sec.

INFO: [HLS 200-100] Execute get\_default\_platform

INFO: [HLS 200-100] Command get\_default\_platform returned 0; 0 sec.

INFO: [HLS 200-100] Execute config\_chip\_info -quiet -resource {SLICE 5200} {LUT 20800} {FF 41600} {DSP48E 90} {BRAM 100}

INFO: [HLS 200-100] Command config\_chip\_info returned 0; 0 sec.

INFO: [HLS 200-100] Execute config\_chip\_info -quiet -speed fast

INFO: [HLS 200-100] Command config\_chip\_info returned 0; 0 sec.

INFO: [HLS 200-100] Command open\_solution returned 0; 0.24 sec.

INFO: [HLS 200-100] Execute cosim\_design -rtl vhdl

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/global.setting.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/global.setting.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/global.setting.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/generic/autopilot/common.gen

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/generic/autopilot/APCoreGen.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/generic/autopilot/op.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/generic/autopilot/op\_simcore.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/generic/autopilot/interface.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/global.setting.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/artix7/artix7.gen

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/virtex.gen

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx.gen

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/plb46.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/fsl.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/axi4.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/nativeAXI4.gen

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/saxilite.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/maxi.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/scripts/xilinxcoregen.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0.01 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/interface/XilEDKCoreGen.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/ip/dds\_compiler.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/ip/xfft.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0.08 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/ip/xfir.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/ip/util.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0.1 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/dsp48.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0.1 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0.1 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/artix7/artix7\_fpv6.gen

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/common/technology/xilinx/common/xilinx\_fpv6.gen

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/global.setting.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/matrixmul.rtl\_wrap.cfg.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/matrixmul.tbgen.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute is\_encrypted /home/drsatya/Desktop/lab1/matrixmul\_test.cpp

INFO: [HLS 200-100] Command is\_encrypted returned 0; 0 sec.

INFO: [HLS 200-100] Execute is\_encrypted /home/drsatya/Desktop/lab1/matrixmul.h

INFO: [HLS 200-100] Command is\_encrypted returned 0; 0 sec.

INFO: [HLS 200-100] Execute is\_encrypted /home/drsatya/Desktop/lab1/matrixmul.cpp

INFO: [HLS 200-100] Command is\_encrypted returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/matrixmul.tbgen.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/matrixmul.tbgen.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [COSIM 212-47] Using XSIM for RTL simulation.

INFO: [COSIM 212-14] Instrumenting C test bench ...

INFO: [HLS 200-0] Marker-Pragma convertor: /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/./sim/autowrap/testbench/matrixmul\_test.cpp\_pre.cpp /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/./sim/autowrap/testbench/matrixmul\_test.cpp\_pre.cpp.line.cpp ./sim/autowrap/testbench/line.tmp 1

INFO: [HLS 200-0] Converting Markers to Pragmas...

INFO: [HLS 200-0] Marker-Pragma convertor: ./sim/autowrap/testbench/matrixmul\_test.cpp\_pre.cpp.tb.cpp.line ./sim/autowrap/testbench/matrixmul\_test.cpp\_pre.cpp.tb.cpp ./sim/autowrap/testbench/line.tmp 0

INFO: [HLS 200-0] Converting Pragmas to Markers...

INFO: [HLS 200-0] Marker-Pragma convertor: /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/./sim/autowrap/testbench/matrixmul.cpp\_pre.cpp /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/./sim/autowrap/testbench/matrixmul.cpp\_pre.cpp.line.cpp ./sim/autowrap/testbench/line.tmp 1

INFO: [HLS 200-0] Converting Markers to Pragmas...

INFO: [HLS 200-0] Marker-Pragma convertor: ./sim/autowrap/testbench/matrixmul.cpp\_pre.cpp.tb.cpp.line ./sim/autowrap/testbench/matrixmul.cpp\_pre.cpp.tb.cpp ./sim/autowrap/testbench/line.tmp 0

INFO: [HLS 200-0] Converting Pragmas to Markers...

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/./sim/autowrap/testbench/tb.status.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/global.setting.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/matrixmul.rtl\_wrap.cfg.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/matrixmul.rtl\_wrap.cfg.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/matrixmul.rtl\_wrap.cfg.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/matrixmul.tbgen.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/.autopilot/db/matrixmul.tbgen.tcl

INFO: [HLS 200-100] Command ap\_source returned 0; 0 sec.

INFO: [HLS 200-100] Execute source .run\_sim.tcl

INFO: [COSIM 212-302] Starting C TB testing ...

INFO: [HLS 200-100] Command ap\_source returned 0; 0.04 sec.

ERROR: [COSIM 212-330] Aborting co-simulation: top function 'matrixmul' is not invoked in the test bench.

ERROR: [COSIM 212-4] \*\*\* C/RTL co-simulation finished: FAIL \*\*\*

INFO: [HLS 200-100] Command cosim\_design returned 2; 6.37 sec.

INFO: [HLS 200-100] Command ap\_source returned 1; 6.62 sec.

command 'ap\_source' returned error code

while executing

"source /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/cosim.tcl"

invoked from within

"hls::main /opt/Xilinx/Vivado\_HLS/2017.2/bin/INTEGER\_MATRIX\_MULTIPLICATION/MATRIXMULTIPLICATION\_16\_SEQUENCE\_INTEGER/solution1/cosim.tcl"

("uplevel" body line 1)

invoked from within

"uplevel 1 hls::main {\*}$args"

(procedure "hls\_proc" line 5)

invoked from within

"hls\_proc $argv"

INFO: [HLS 200-100] Execute cleanup\_all

INFO: [HLS 200-100] Command cleanup\_all returned 0; 0 sec.