INFO: [SIM 2] \*\*\*\*\*\*\*\*\*\*\*\*\*\*\* CSIM start \*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

INFO: [SIM 4] CSIM will launch GCC as the compiler.

Compiling(apcc) ../../../../../../../../../home/drsatya/neha/add\_tb.c in debug mode

INFO: [HLS 200-10] Running '/opt/Xilinx/Vivado\_HLS/2017.2/bin/unwrapped/lnx64.o/apcc'

INFO: [HLS 200-10] For user 'root' on host 'drsatya-OptiPlex-990' (Linux\_x86\_64 version 3.8.0-38-generic) on Thu Mar 08 11:50:39 IST 2018

INFO: [HLS 200-10] On os Ubuntu 12.04.4 LTS

INFO: [HLS 200-10] In directory '/opt/Xilinx/Vivado\_HLS/2017.2/bin/sequence\_1\_8\_march/solution1/csim/build'

INFO: [APCC 202-3] Tmp directory is /tmp/apcc\_db\_root/116731520490039251138

INFO: [APCC 202-1] APCC is done.

Compiling(apcc) ../../../../../../../../../home/drsatya/neha/add.c in debug mode

INFO: [HLS 200-10] Running '/opt/Xilinx/Vivado\_HLS/2017.2/bin/unwrapped/lnx64.o/apcc'

INFO: [HLS 200-10] For user 'root' on host 'drsatya-OptiPlex-990' (Linux\_x86\_64 version 3.8.0-38-generic) on Thu Mar 08 11:50:41 IST 2018

INFO: [HLS 200-10] On os Ubuntu 12.04.4 LTS

INFO: [HLS 200-10] In directory '/opt/Xilinx/Vivado\_HLS/2017.2/bin/sequence\_1\_8\_march/solution1/csim/build'

INFO: [APCC 202-3] Tmp directory is /tmp/apcc\_db\_root/117281520490041890620

INFO: [APCC 202-1] APCC is done.

Generating csim.exe

2.000000

4.000000

6.000000

8.000000

10.000000

12.000000

14.000000

16.000000

INFO: [SIM 1] CSim done with 0 errors.

INFO: [SIM 3] \*\*\*\*\*\*\*\*\*\*\*\*\*\*\* CSIM finish \*\*\*\*\*\*\*\*\*\*\*\*\*\*\*