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# File generated by Vivado(TM) HLS - High-Level Synthesis from C, C++ and SystemC

# Version: 2017.2

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\_\_SIM\_FPO\_\_ = 1

\_\_SIM\_OPENCV\_\_ = 1

\_\_SIM\_FFT\_\_ = 1

\_\_SIM\_FIR\_\_ = 1

\_\_SIM\_DDS\_\_ = 1

TARGET := cosim.pc.exe

AUTOPILOT\_ROOT := /opt/Xilinx/Vivado\_HLS/2017.2

AUTOPILOT\_MACH := lnx64

ifdef COSIM\_M32

AUTOPILOT\_MACH := lnx32

IFLAG += -m32

endif

ifdef AP\_GCC\_M32

AUTOPILOT\_MACH := Linux\_x86

IFLAG += -m32

endif

IFLAG += -fPIC

ifndef AP\_GCC\_PATH

AP\_GCC\_PATH := /opt/Xilinx/Vivado\_HLS/2017.2/lnx64/tools/gcc/bin

endif

CCFLAG +=

IFLAG += -g

IFLAG += -I "${AUTOPILOT\_ROOT}/include"

IFLAG += -I "${AUTOPILOT\_ROOT}/include/ap\_sysc"

IFLAG += -I "${AUTOPILOT\_ROOT}/common/technology/generic/SystemC"

IFLAG += -I "${AUTOPILOT\_ROOT}/common/technology/generic/SystemC/AESL\_FP\_comp"

IFLAG += -I "${AUTOPILOT\_ROOT}/common/technology/generic/SystemC/AESL\_comp"

IFLAG += -I "${AUTOPILOT\_ROOT}/${AUTOPILOT\_MACH}/tools/systemc/include"

IFLAG += -I "${AUTOPILOT\_ROOT}/${AUTOPILOT\_MACH}/tools/auto\_cc/include"

IFLAG += -I "/usr/include/x86\_64-linux-gnu"

IFLAG += -D\_\_SIM\_FPO\_\_

IFLAG += -D\_\_SIM\_OPENCV\_\_

IFLAG += -D\_\_SIM\_FFT\_\_

IFLAG += -D\_\_SIM\_FIR\_\_

IFLAG += -D\_\_SIM\_DDS\_\_

LFLAG += -L "${AUTOPILOT\_ROOT}/${AUTOPILOT\_MACH}/tools/systemc/lib" -lsystemc -lpthread

IFLAG += -D\_\_RTL\_SIMULATION\_\_

IFLAG += -D\_\_xilinx\_ip\_top=

DFLAG += -DAUTOCC

DFLAG += -DAESL\_EXTERN\_C

include ./Makefile.rules

all : $(TARGET)

AUTOCC := apcc

$(ObjDir)/add\_tb.c\_pre.c.tb.o : add\_tb.c\_pre.c.tb.c $(ObjDir)/.dir

$(Echo) " Compiling (apcc) add\_tb.c\_pre.c.tb.c" $(AVE\_DIR\_DLOG)

$(Verb) $(AUTOCC) -fno-builtin-isinf -fno-builtin-isnan -c $(IFLAG) $(DFLAG) $< -o $@; \

$(ObjDir)/add.c\_pre.c.tb.o : add.c\_pre.c.tb.c $(ObjDir)/.dir

$(Echo) " Compiling (apcc) add.c\_pre.c.tb.c" $(AVE\_DIR\_DLOG)

$(Verb) $(AUTOCC) -fno-builtin-isinf -fno-builtin-isnan -c $(IFLAG) $(DFLAG) $< -o $@; \