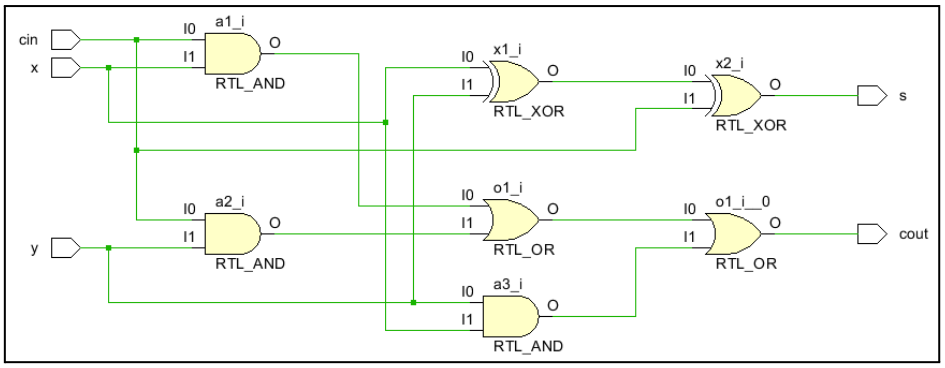
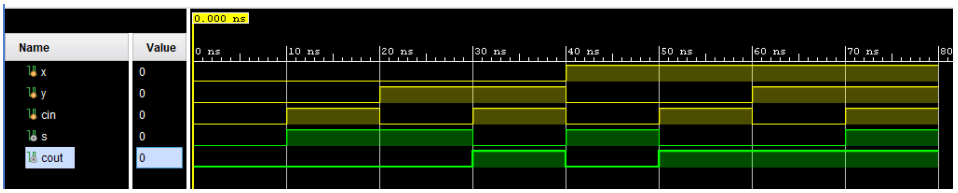
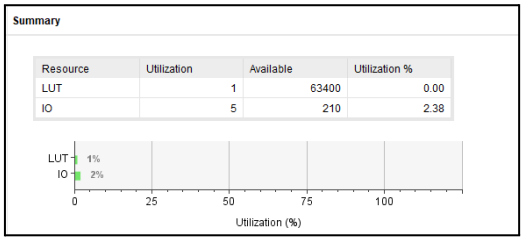
DSD LAB SESSION 2

Design & Simulate a Full Adder Circuit using Structural Modeling

Full Adder







Full Subtractor