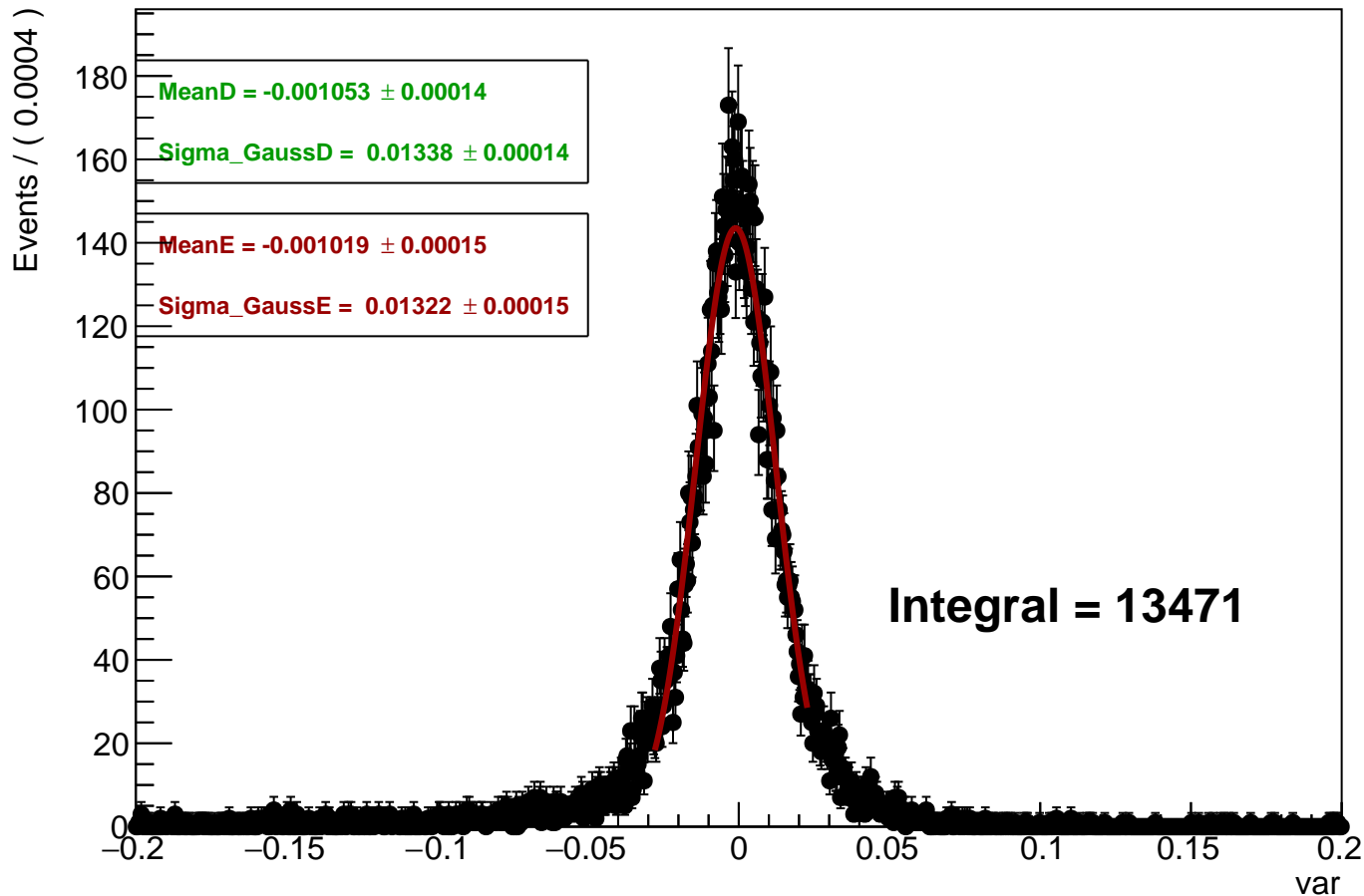
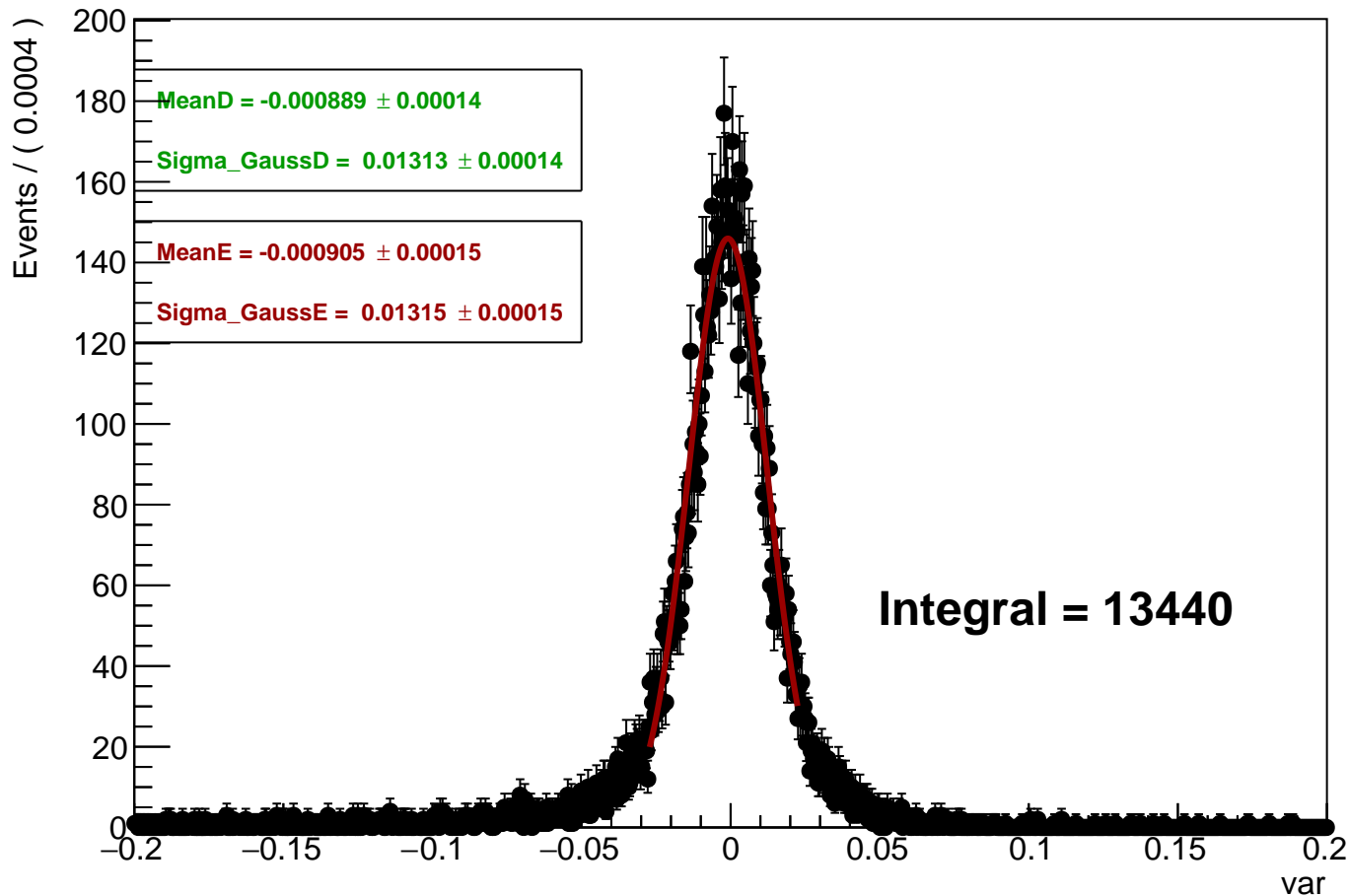


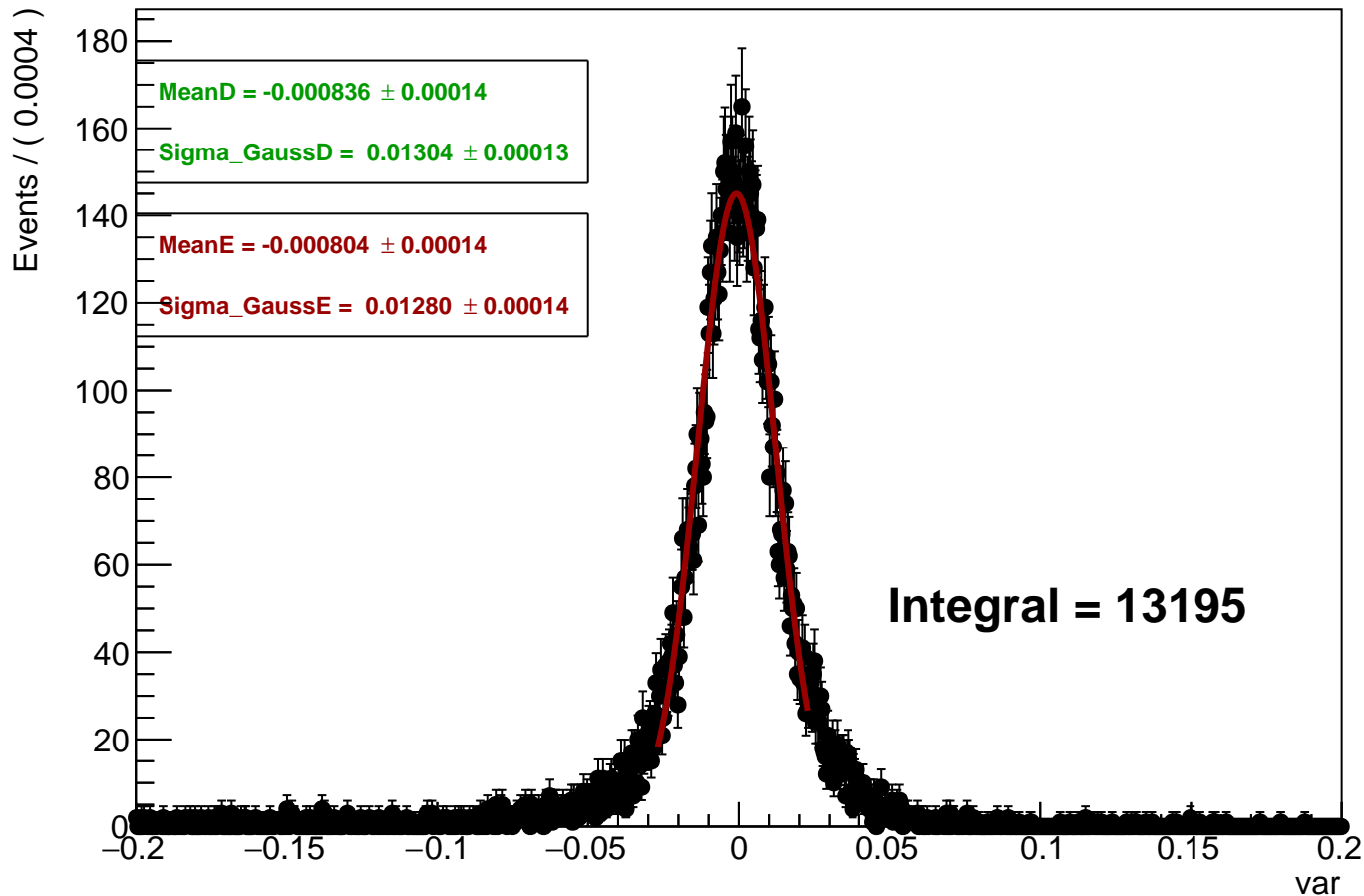
h2\_Resolution\_UL\_Roch\_VX+BS\_Inclusive\_d0InclusivePtRes\_vsPhi\_0\_0



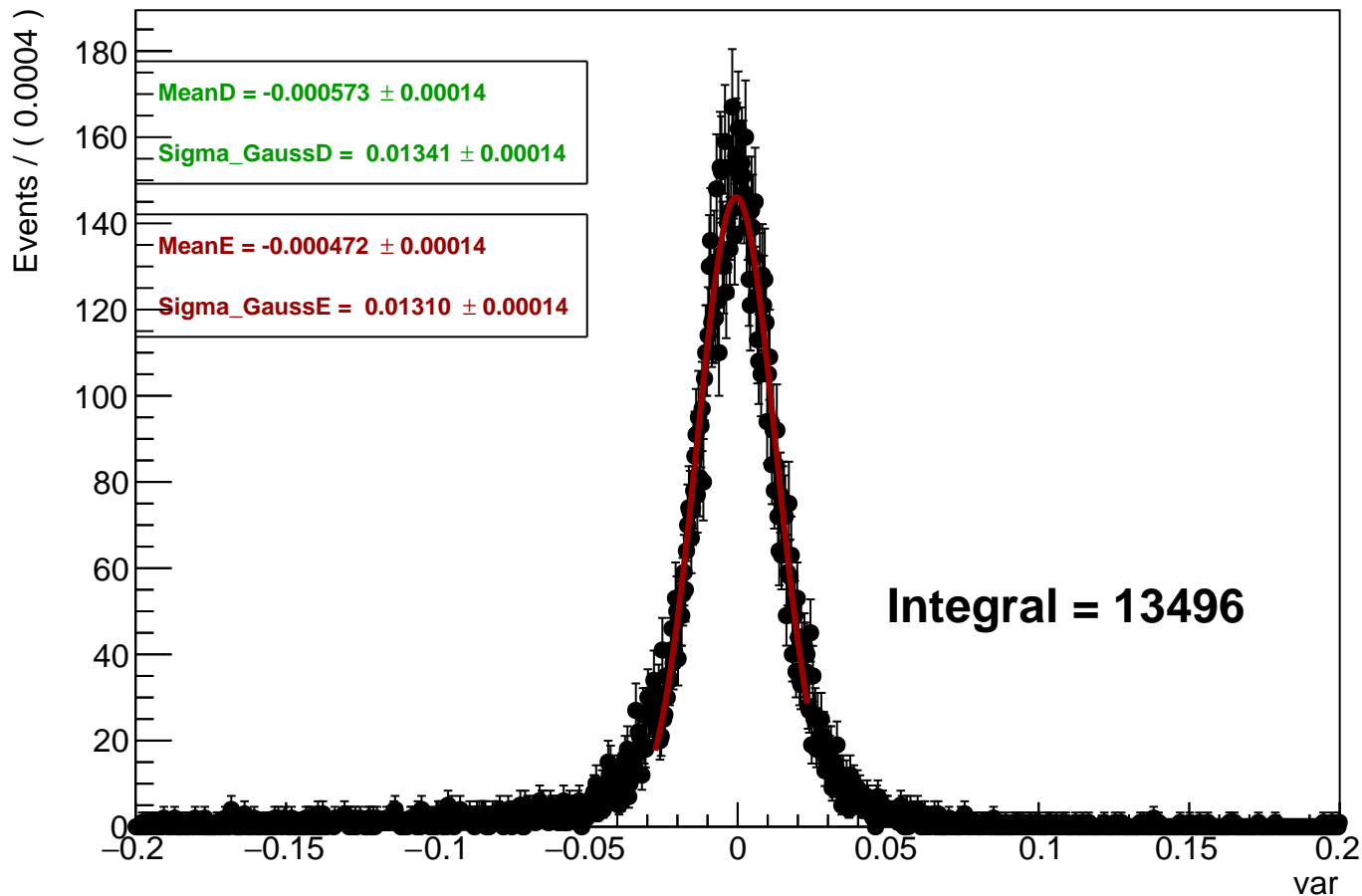
h2\_Resolution\_UL\_Roch\_VX+BS\_Inclusive\_d0InclusivePtRes\_vsPhi\_0\_1



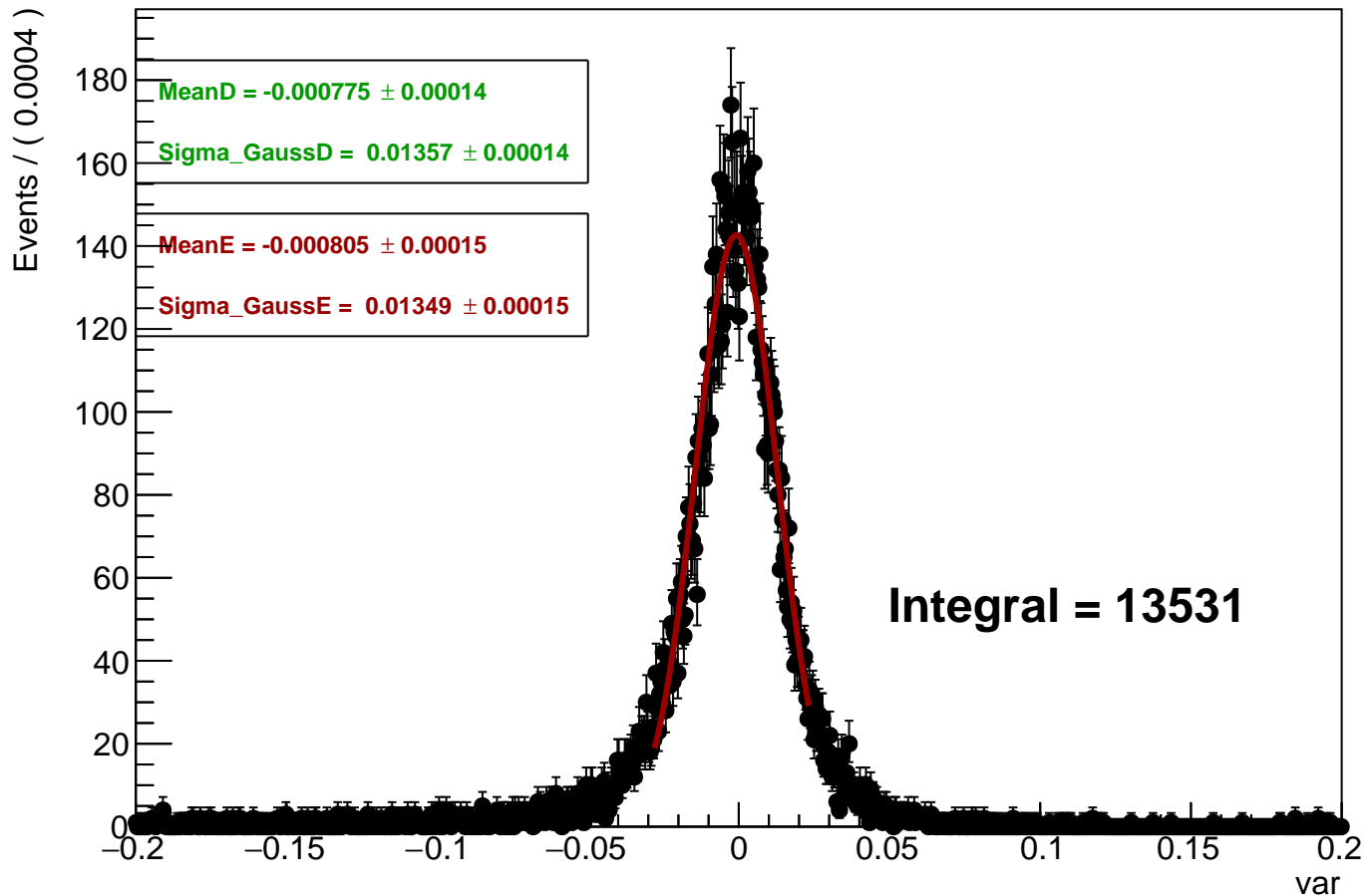
# h2\_Resolution\_UL\_Roch\_VX+BS\_Inclusive\_d0InclusivePtRes\_vsPhi\_1\_1



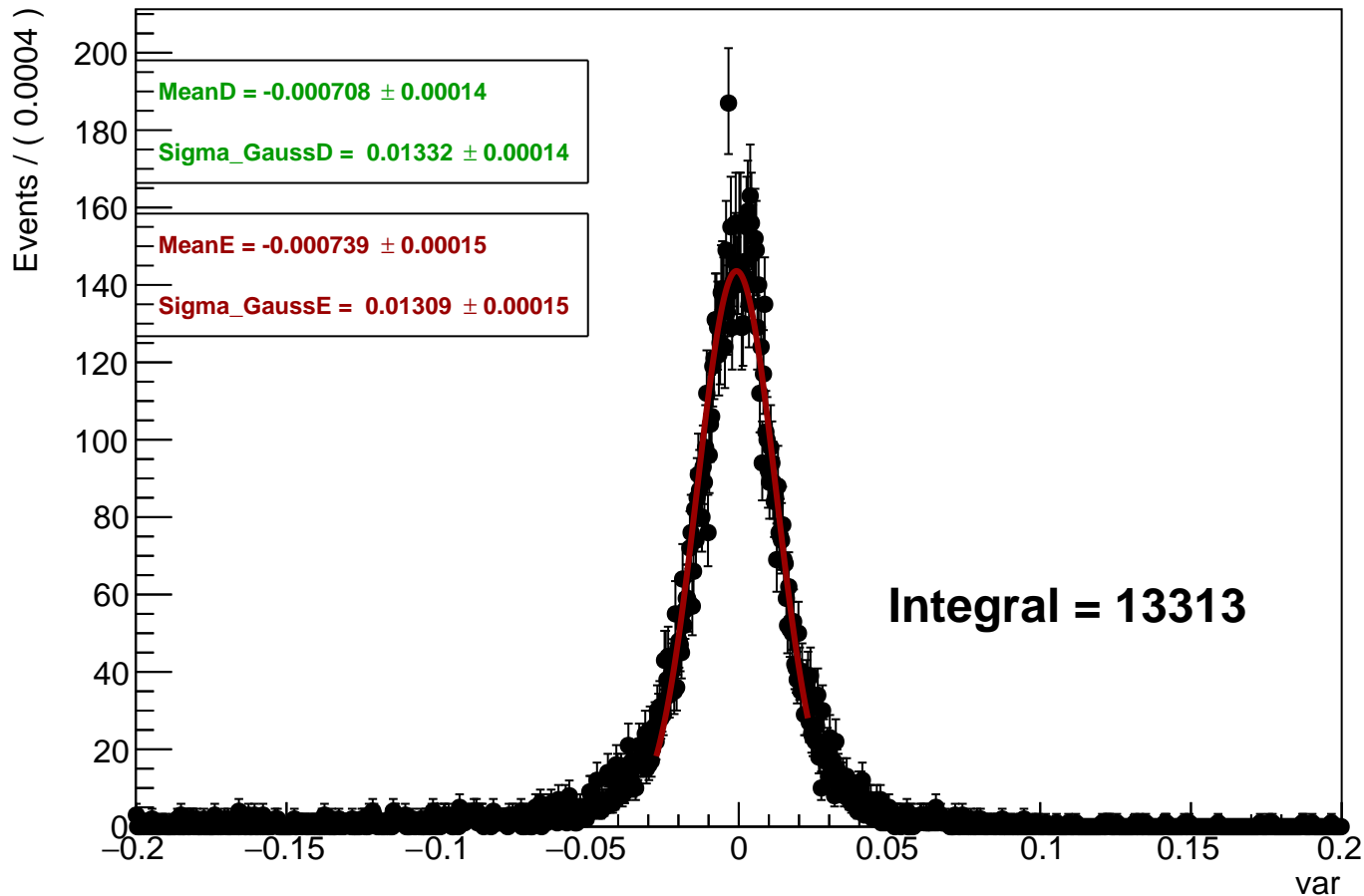
h2\_Resolution\_UL\_Roch\_VX+BS\_Inclusive\_d0InclusivePtRes\_vsPhi\_1\_1



h2\_Resolution\_UL\_Roch\_VX+BS\_Inclusive\_d0InclusivePtRes\_vsPhi\_1\_2

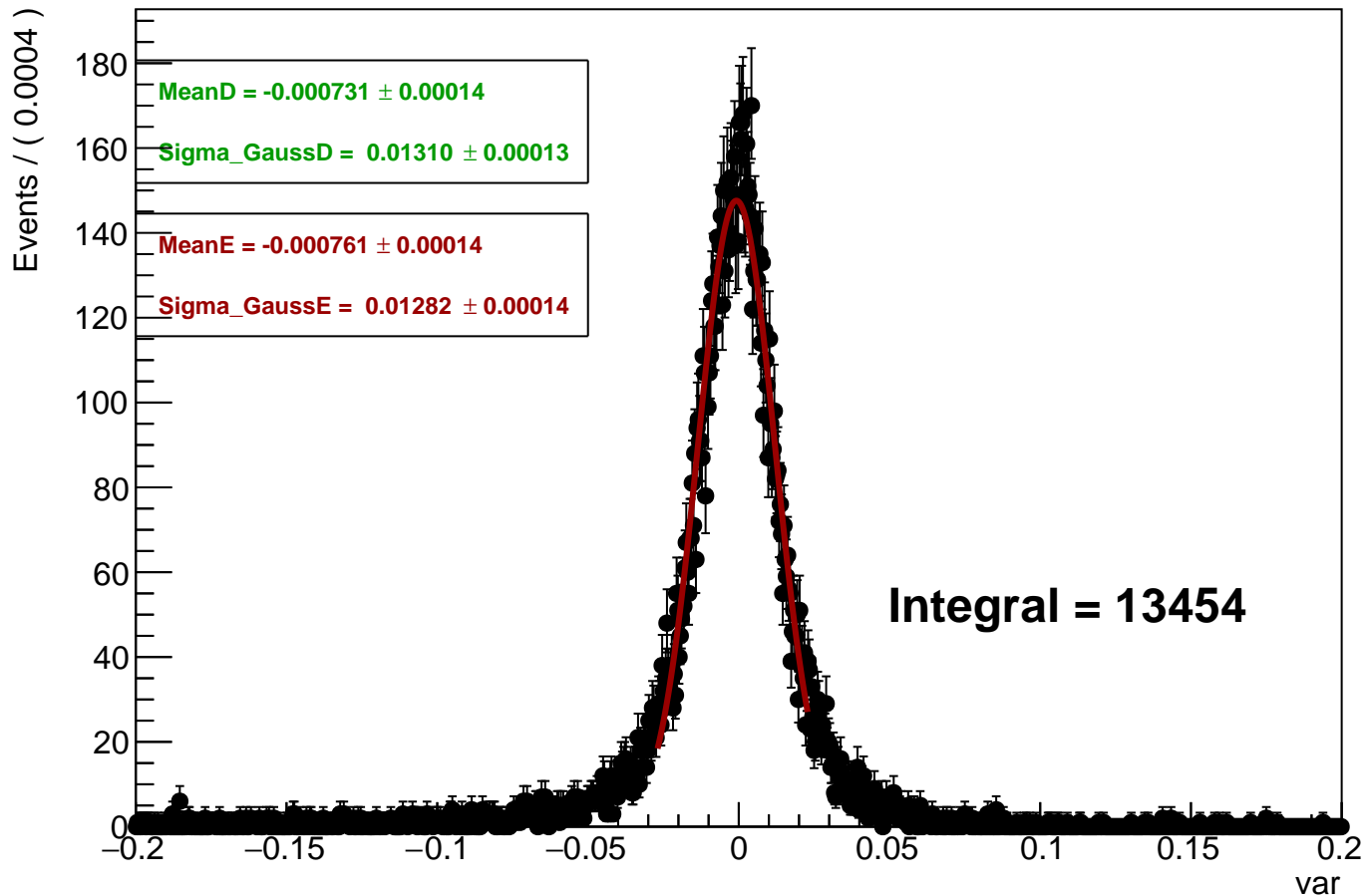


# h2\_Resolution\_UL\_Roch\_VX+BS\_Inclusive\_d0InclusivePtRes\_vsPhi\_2\_2

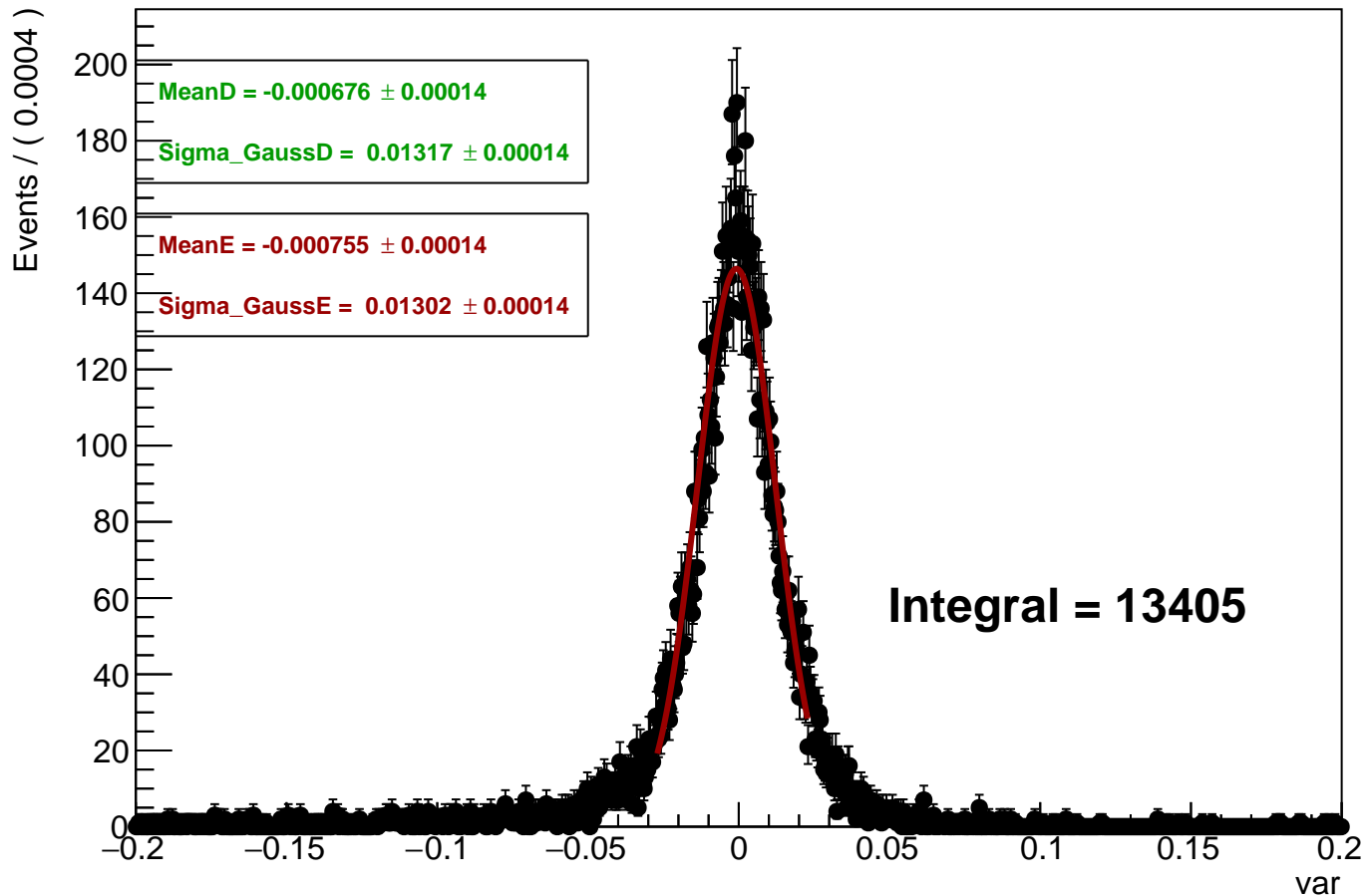




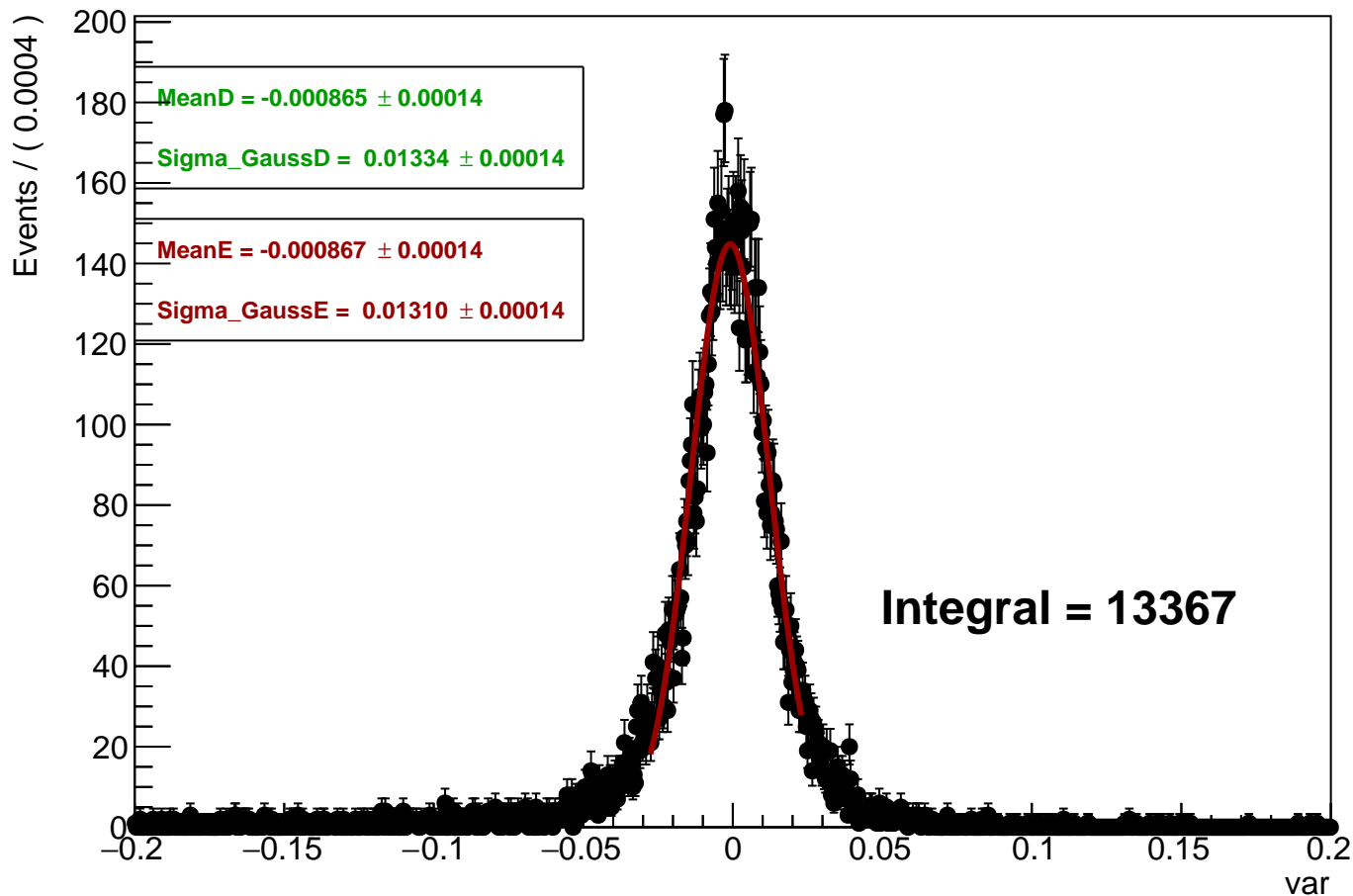
h2\_Resolution\_UL\_Roch\_VX+BS\_Inclusive\_d0InclusivePtRes\_vsPhi\_2\_2



# h2\_Resolution\_UL\_Roch\_VX+BS\_Inclusive\_d0InclusivePtRes\_vsPhi\_2\_3



h2\_Resolution\_UL\_Roch\_VX+BS\_Inclusive\_d0InclusivePtRes\_vsPhi\_3\_3



h2\_Resolution\_UL\_Roch\_VX+BS\_Inclusive\_d0InclusivePtRes\_vsPhi\_3\_3

