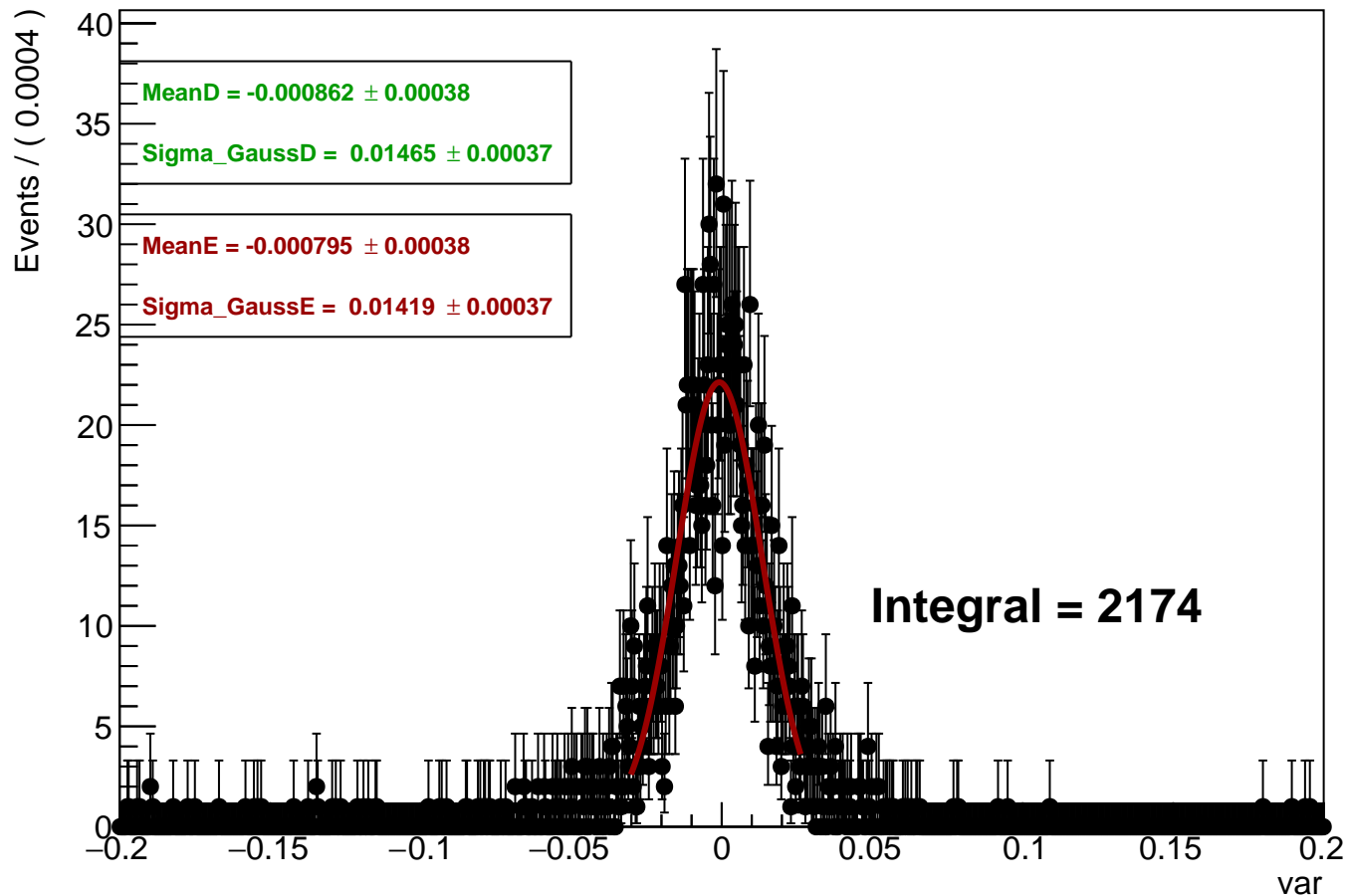
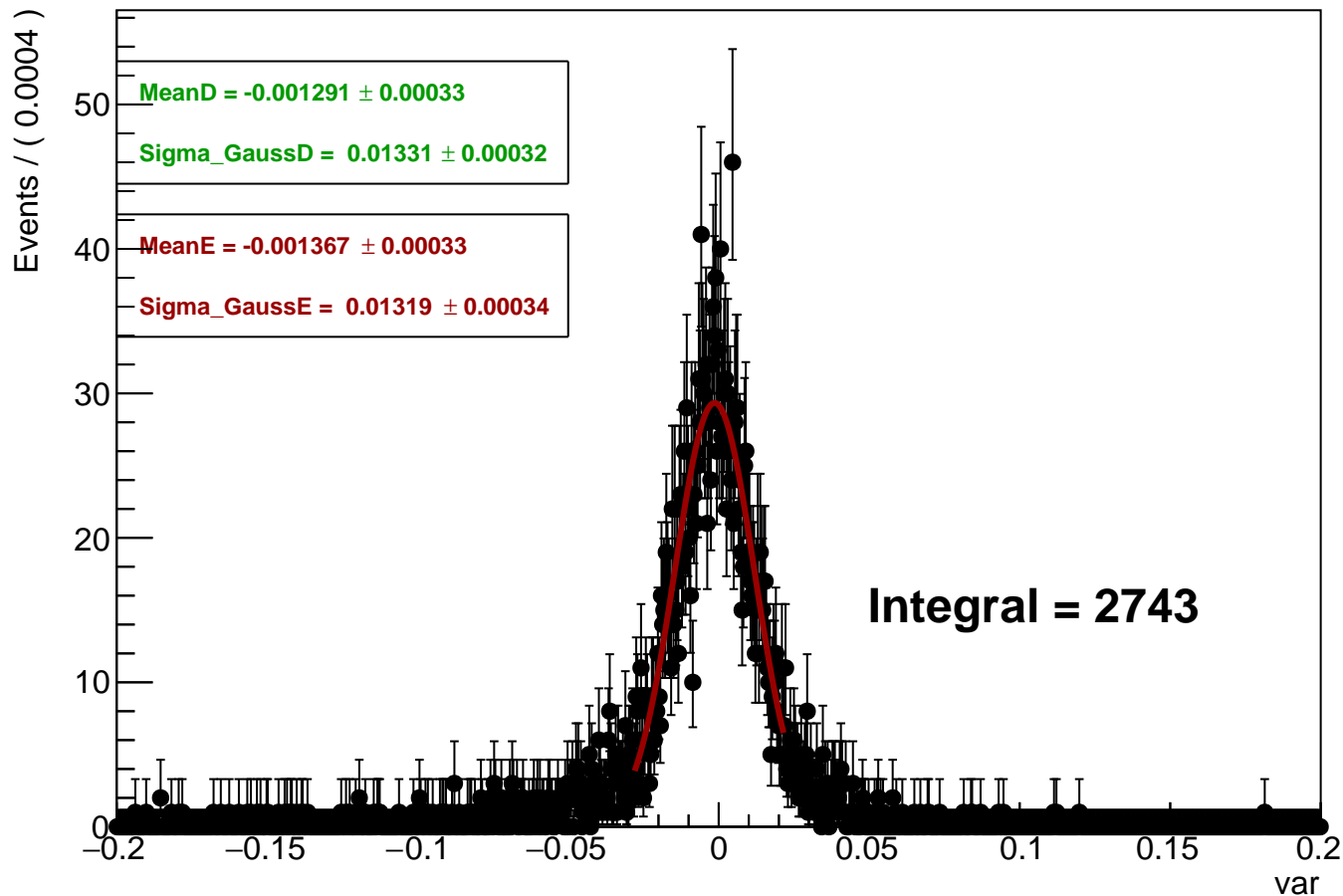


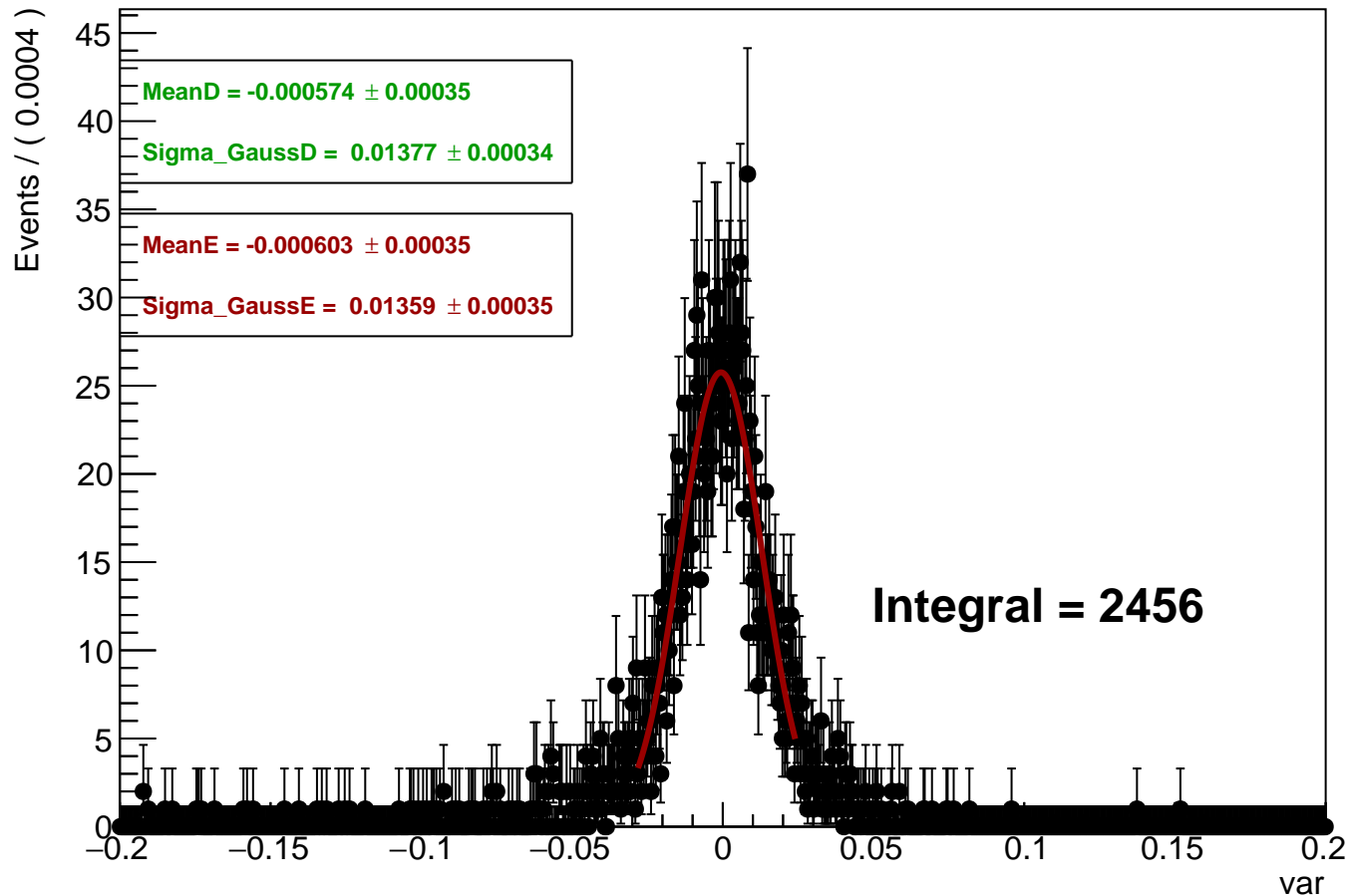
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_0_17



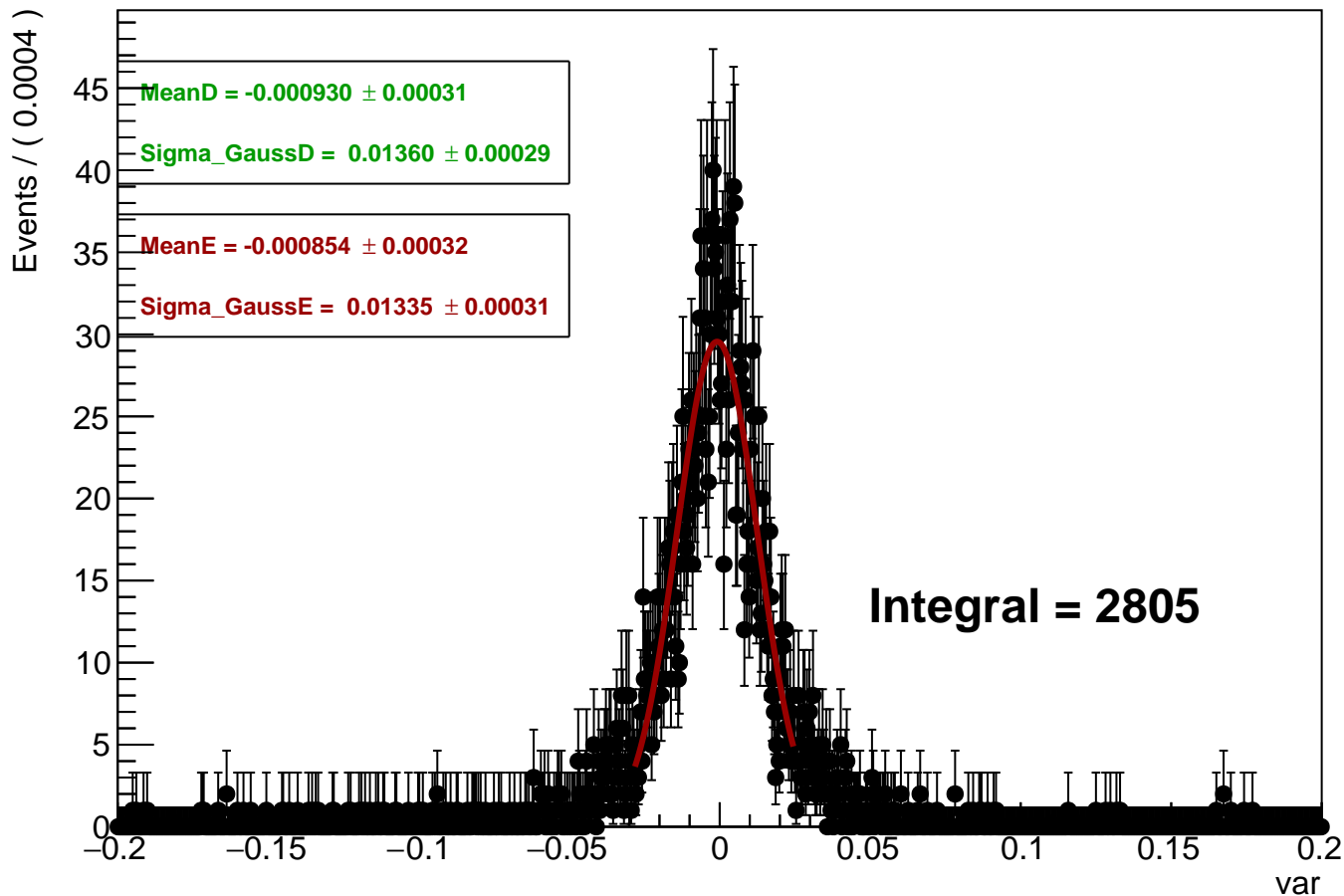
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_17_20



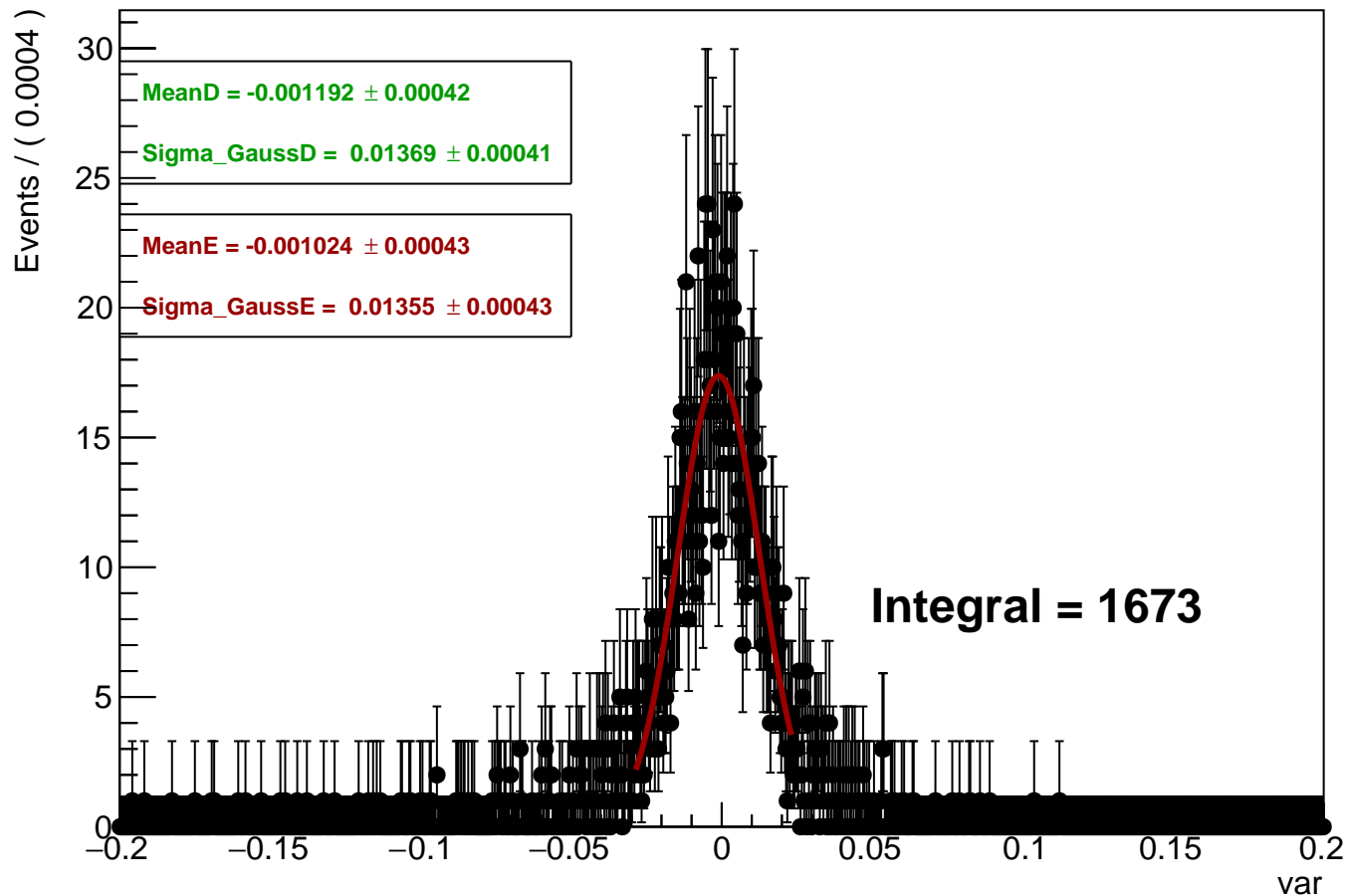
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_20_22



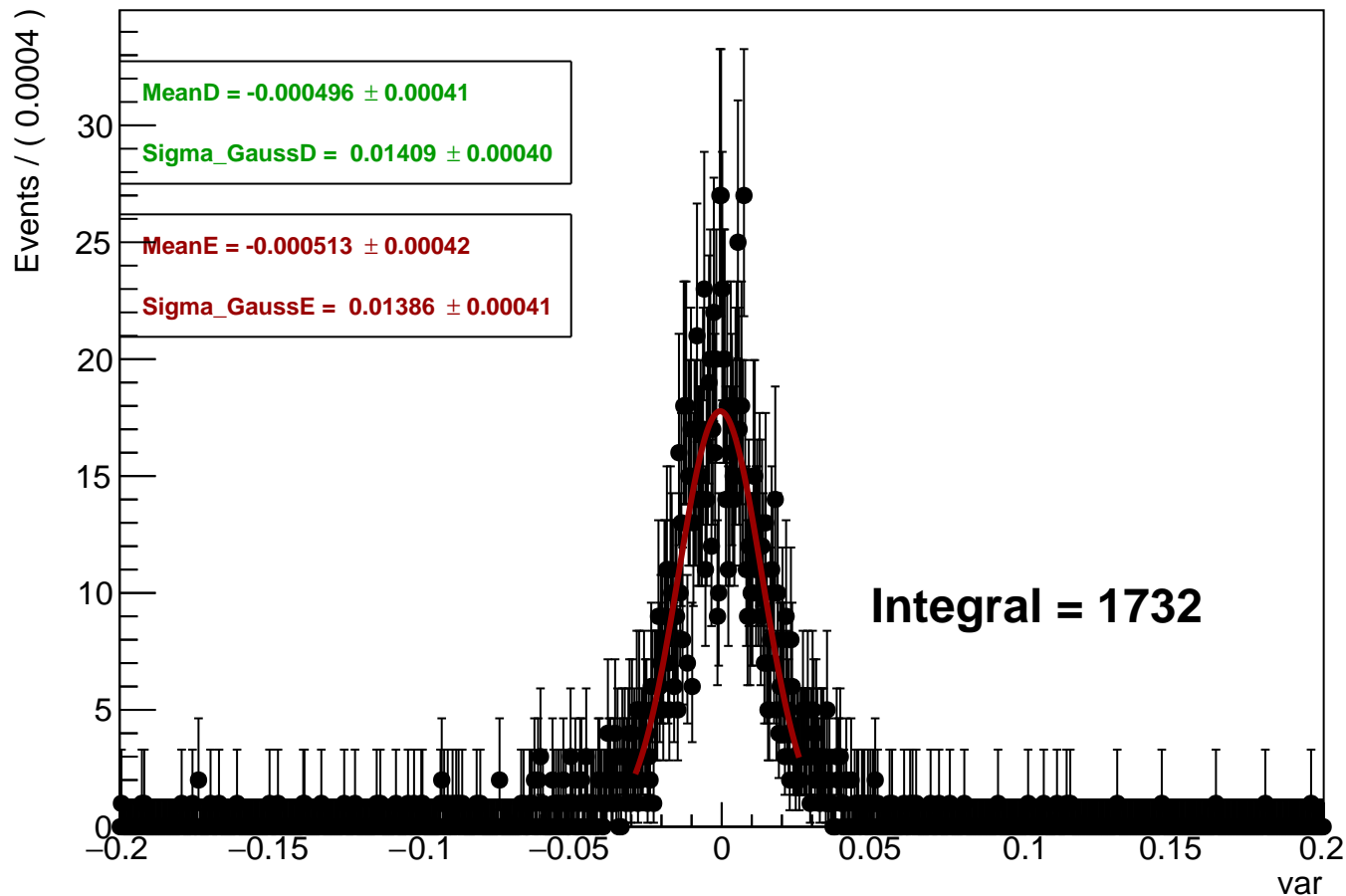
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_22_24



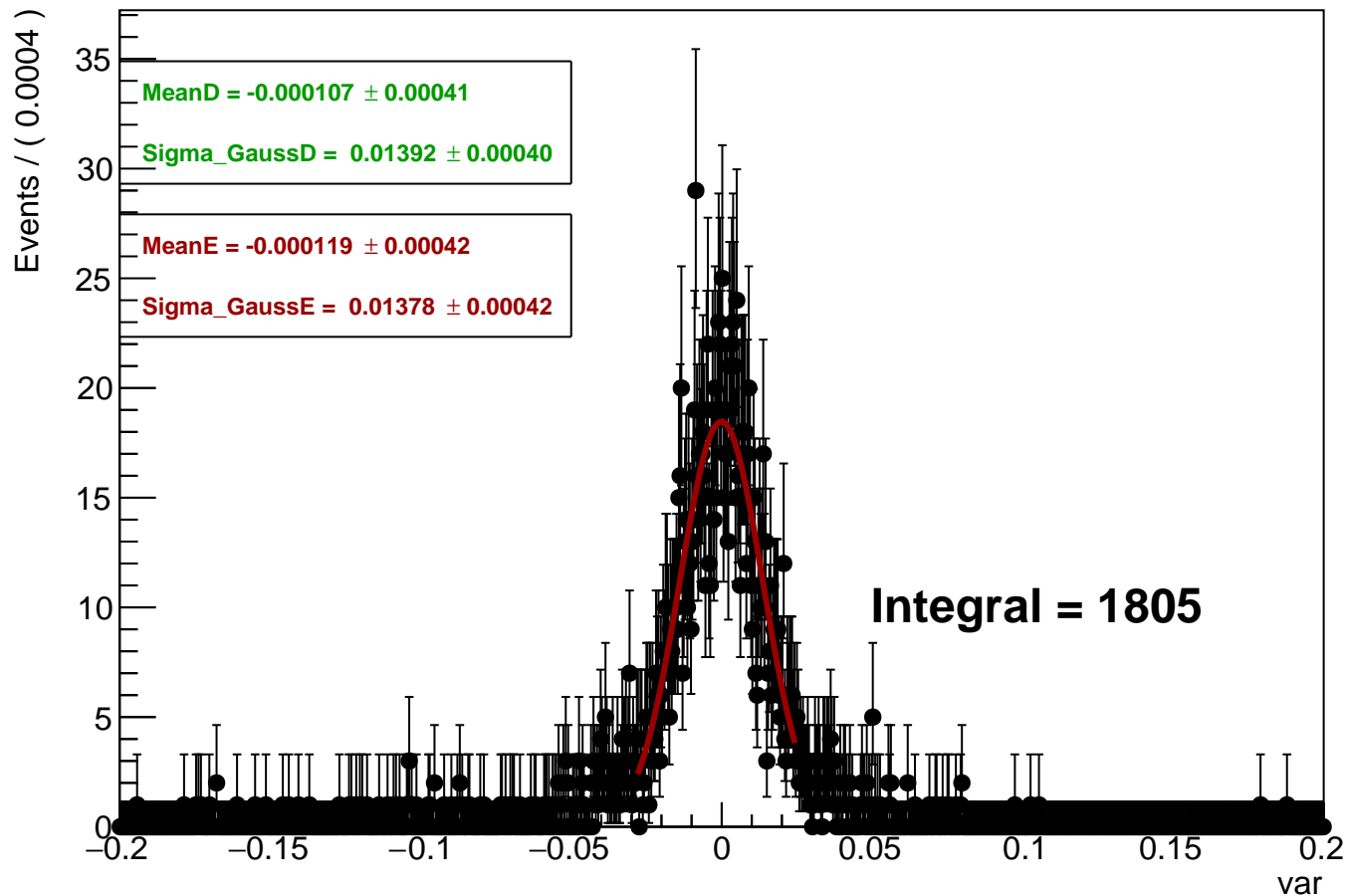
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_24_25



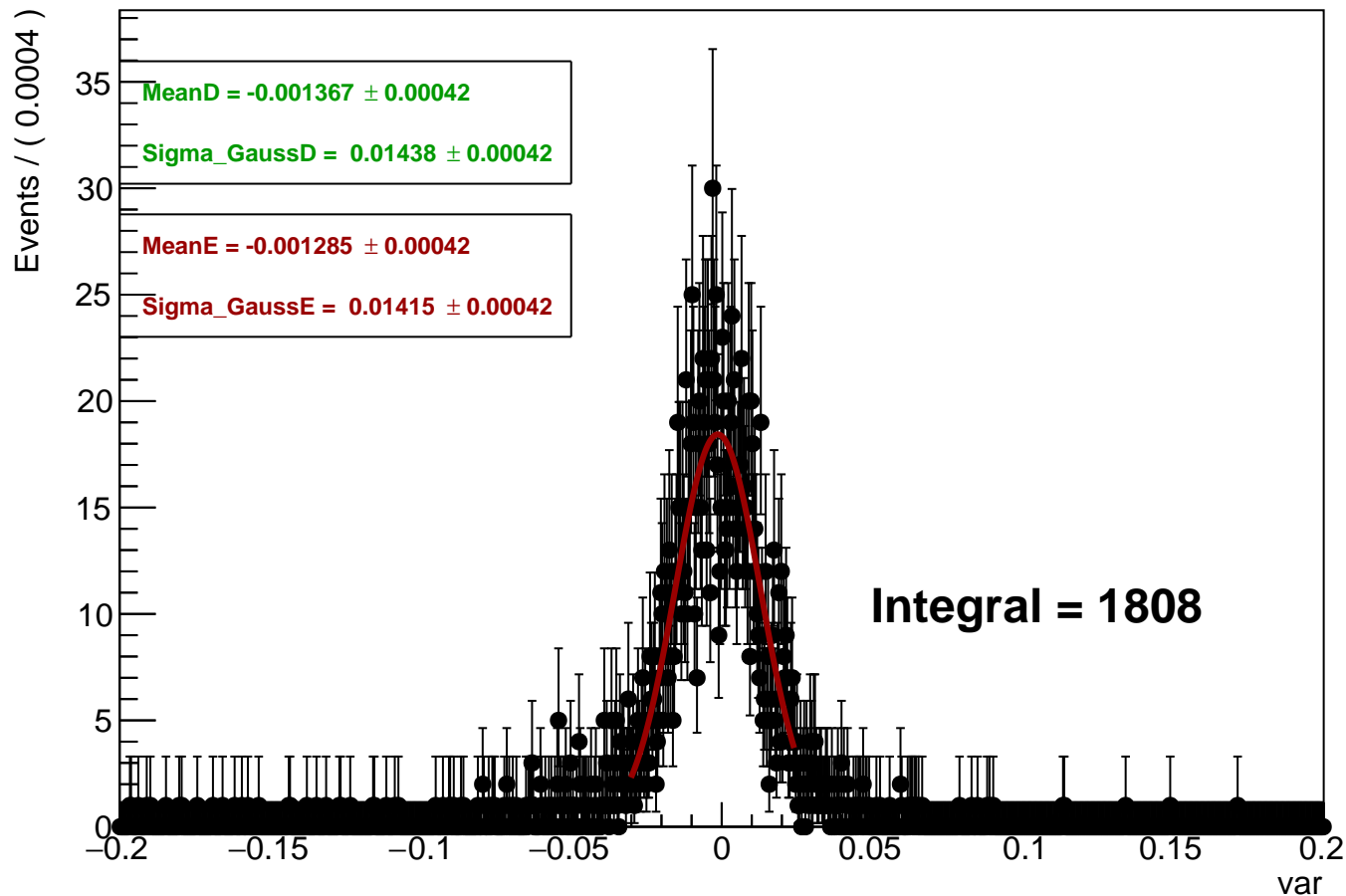
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_25_26



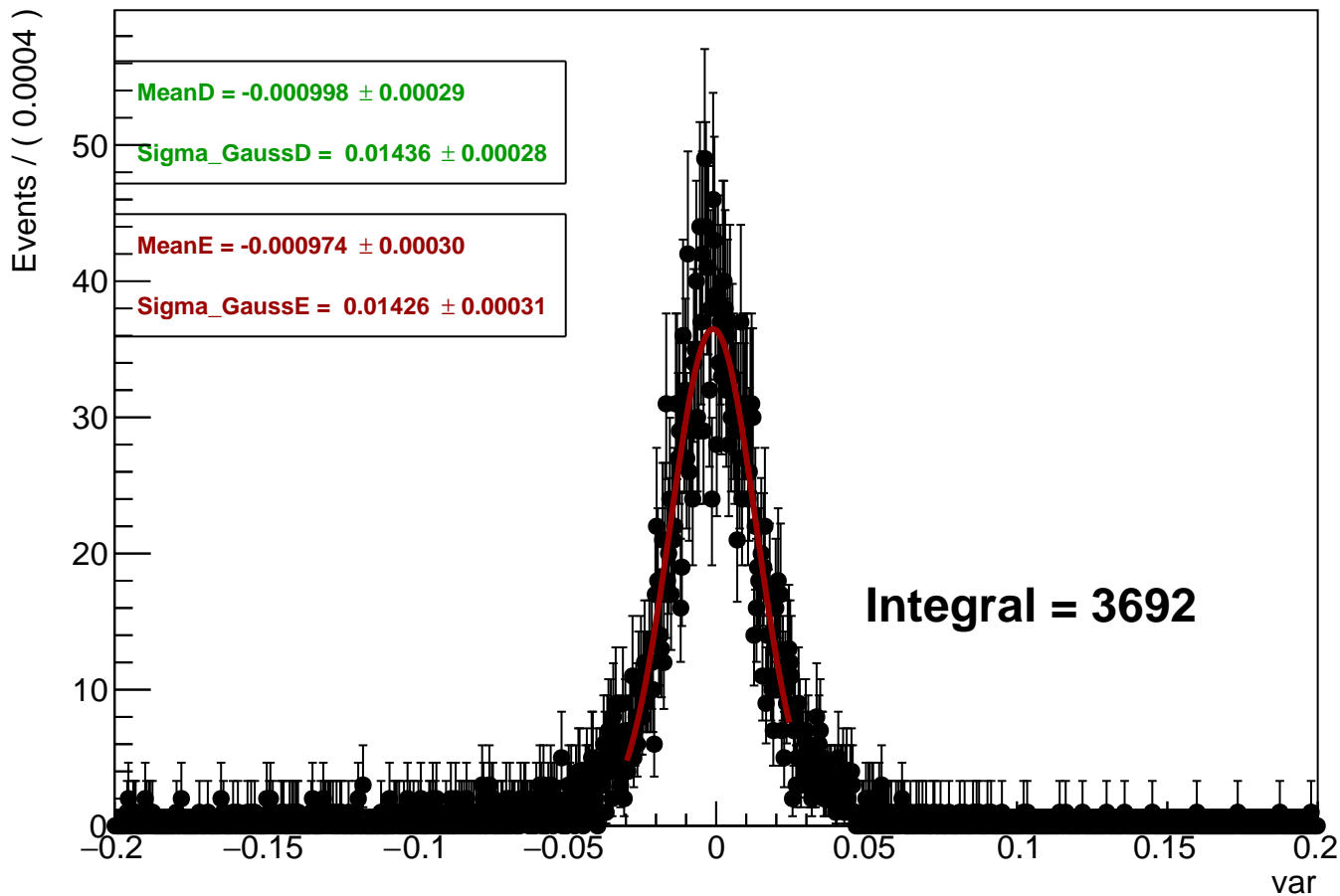
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_26_27



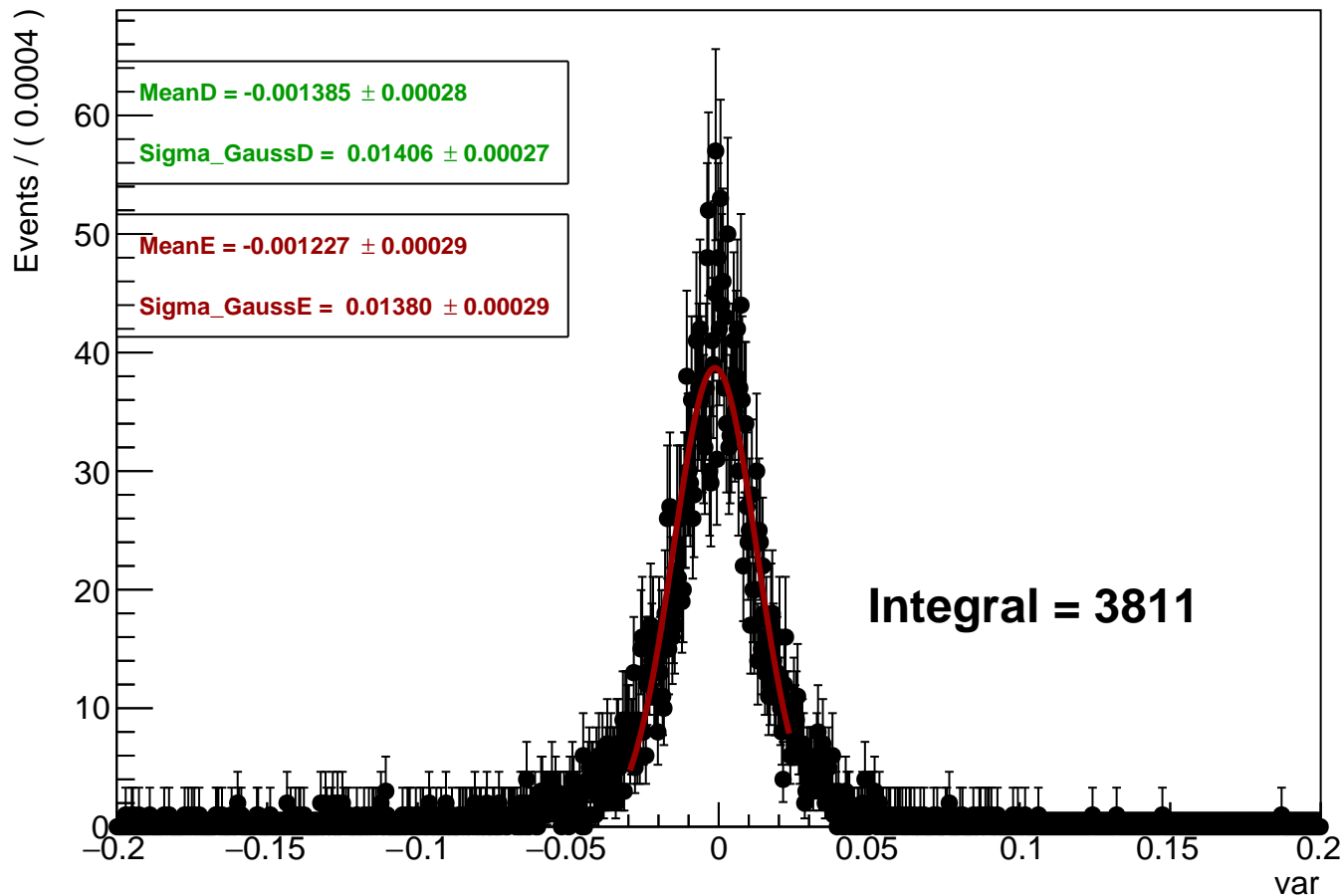
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_27_28



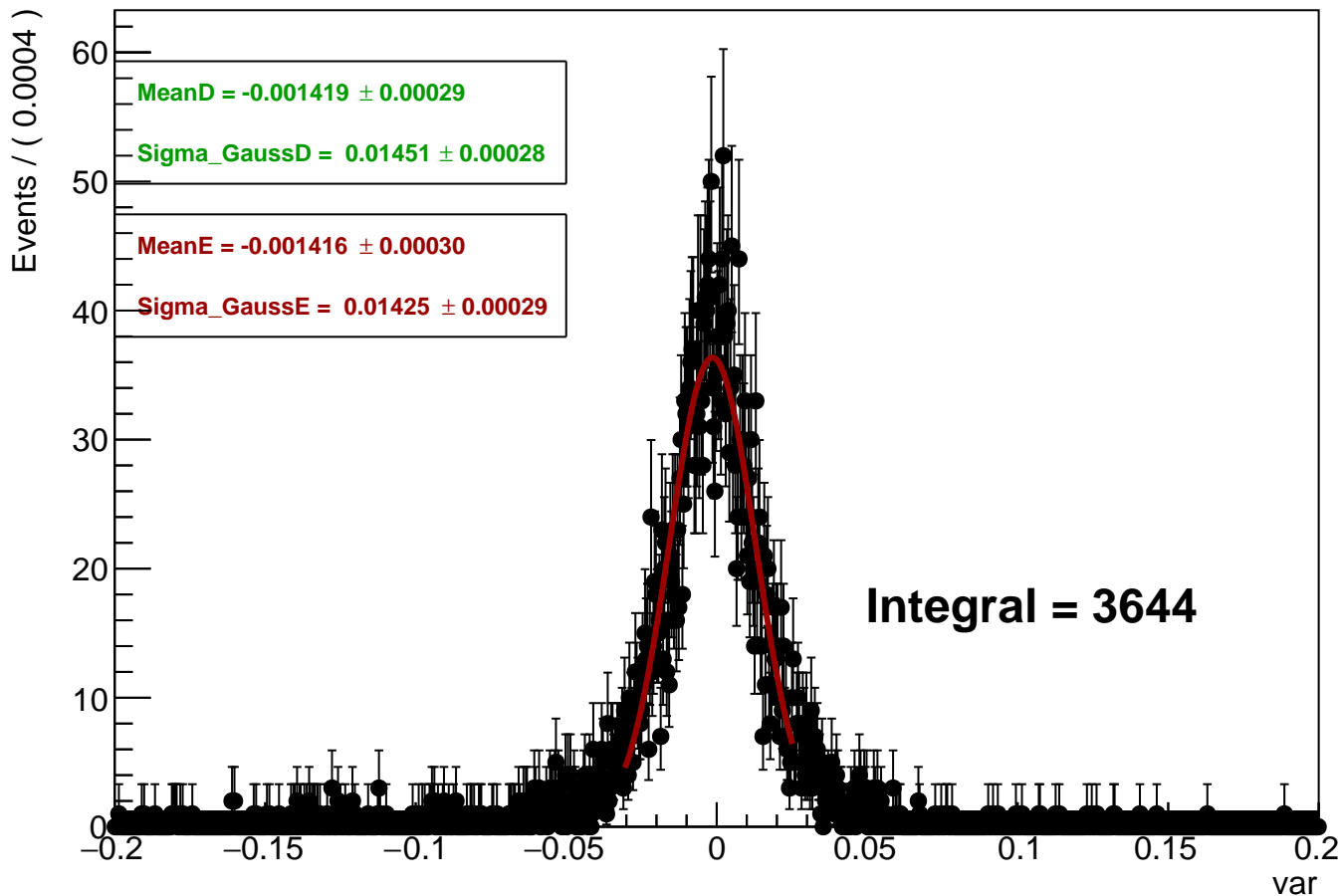
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_28_30



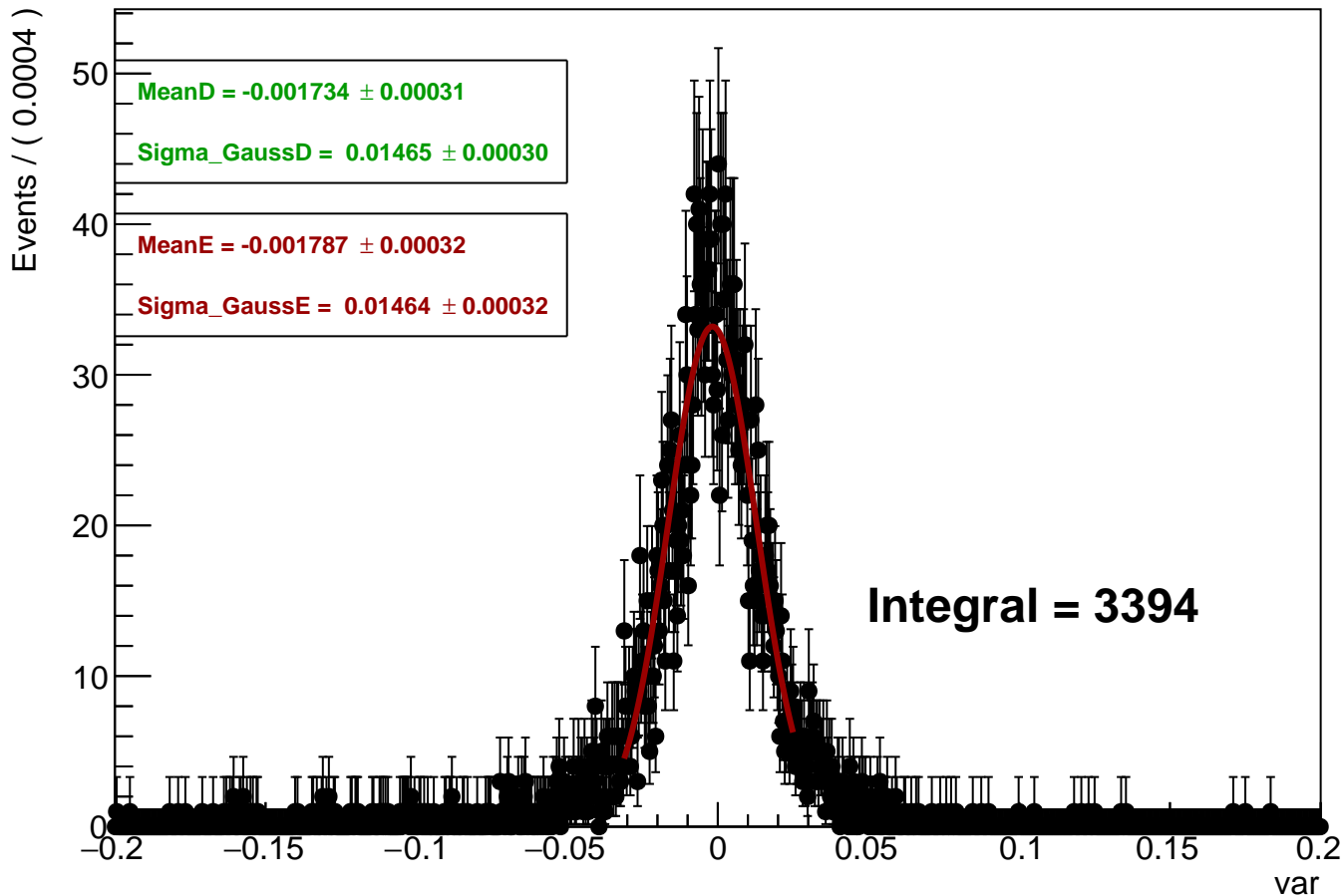
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_30_32



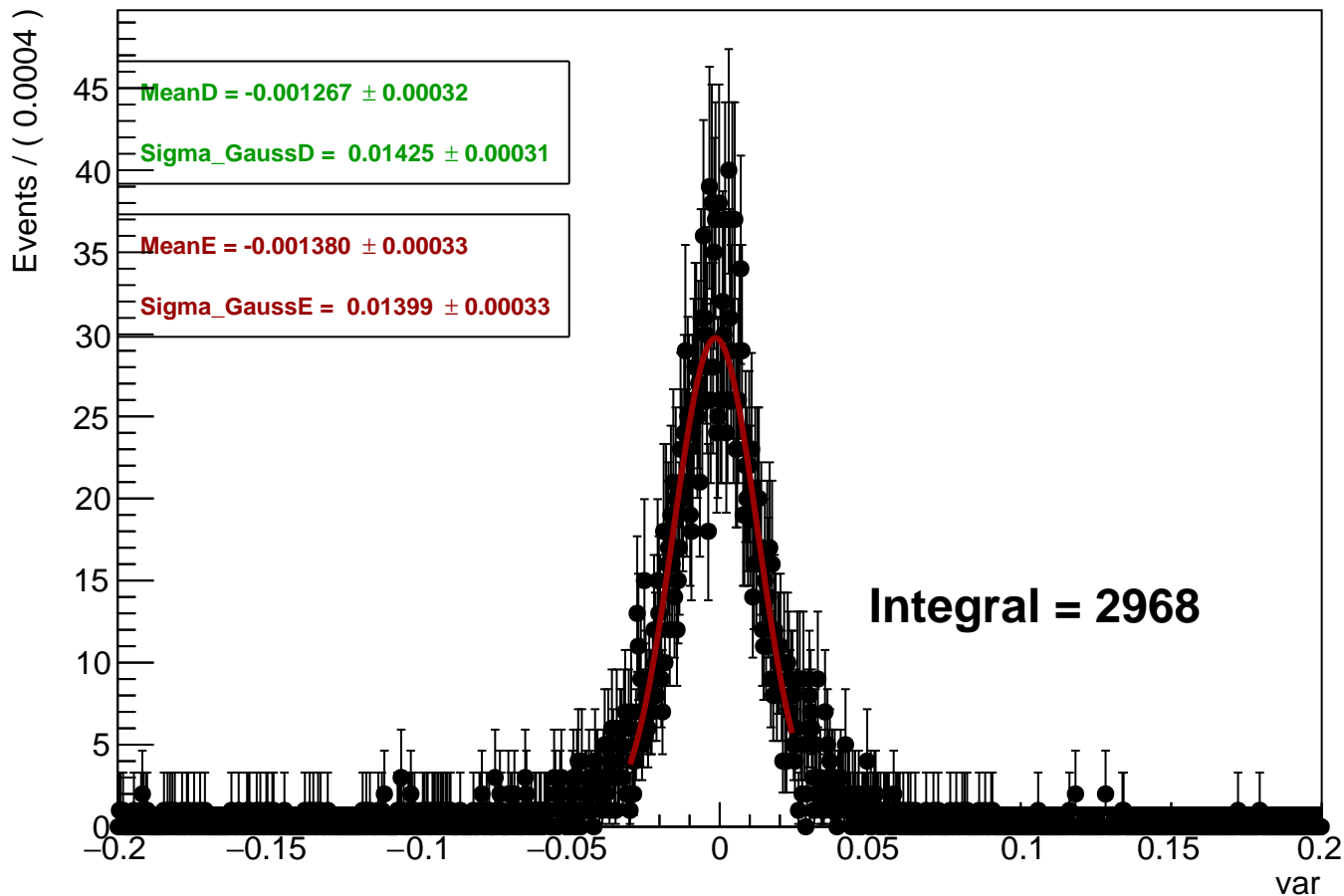
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_32_34



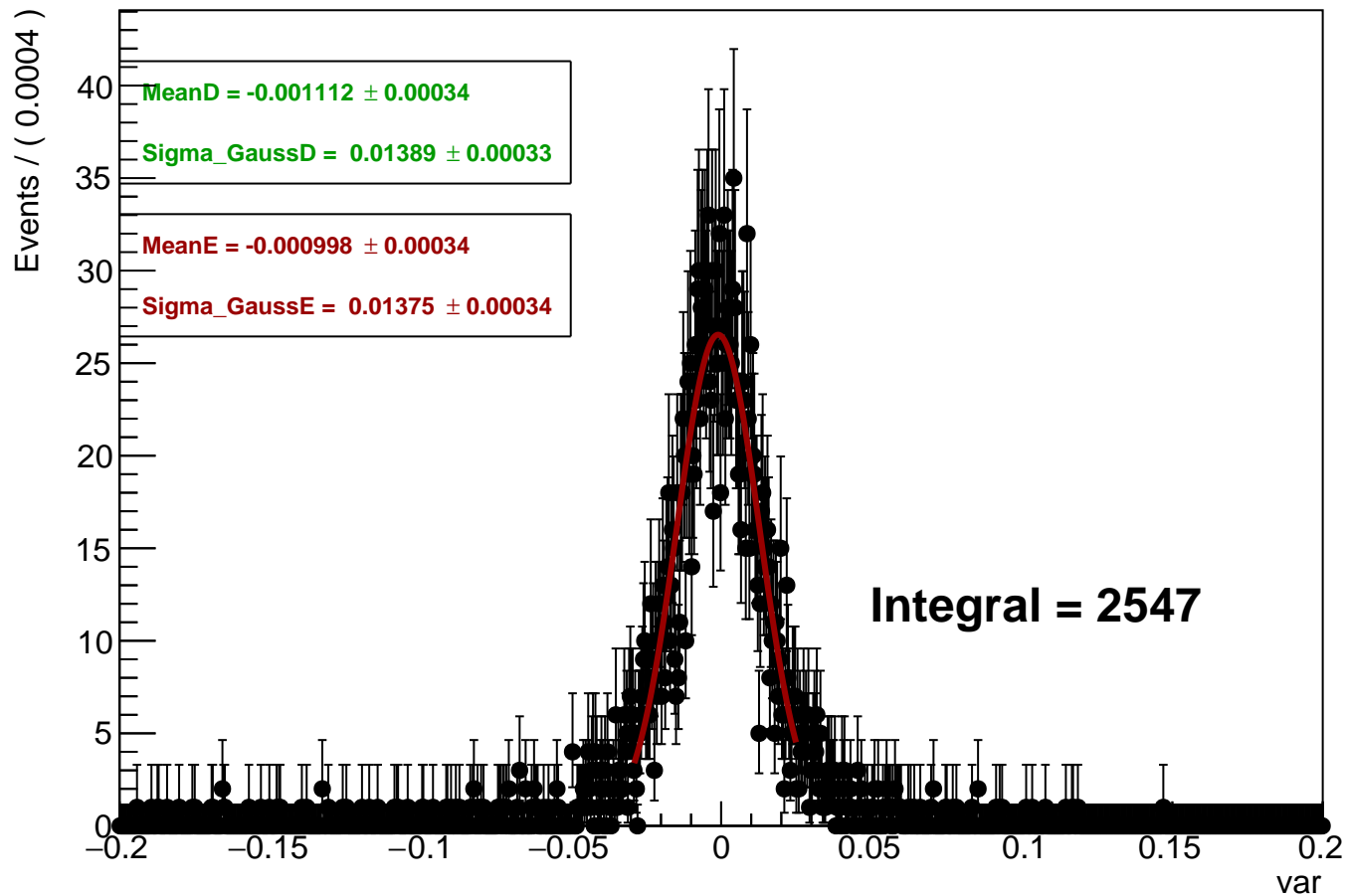
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_34_36



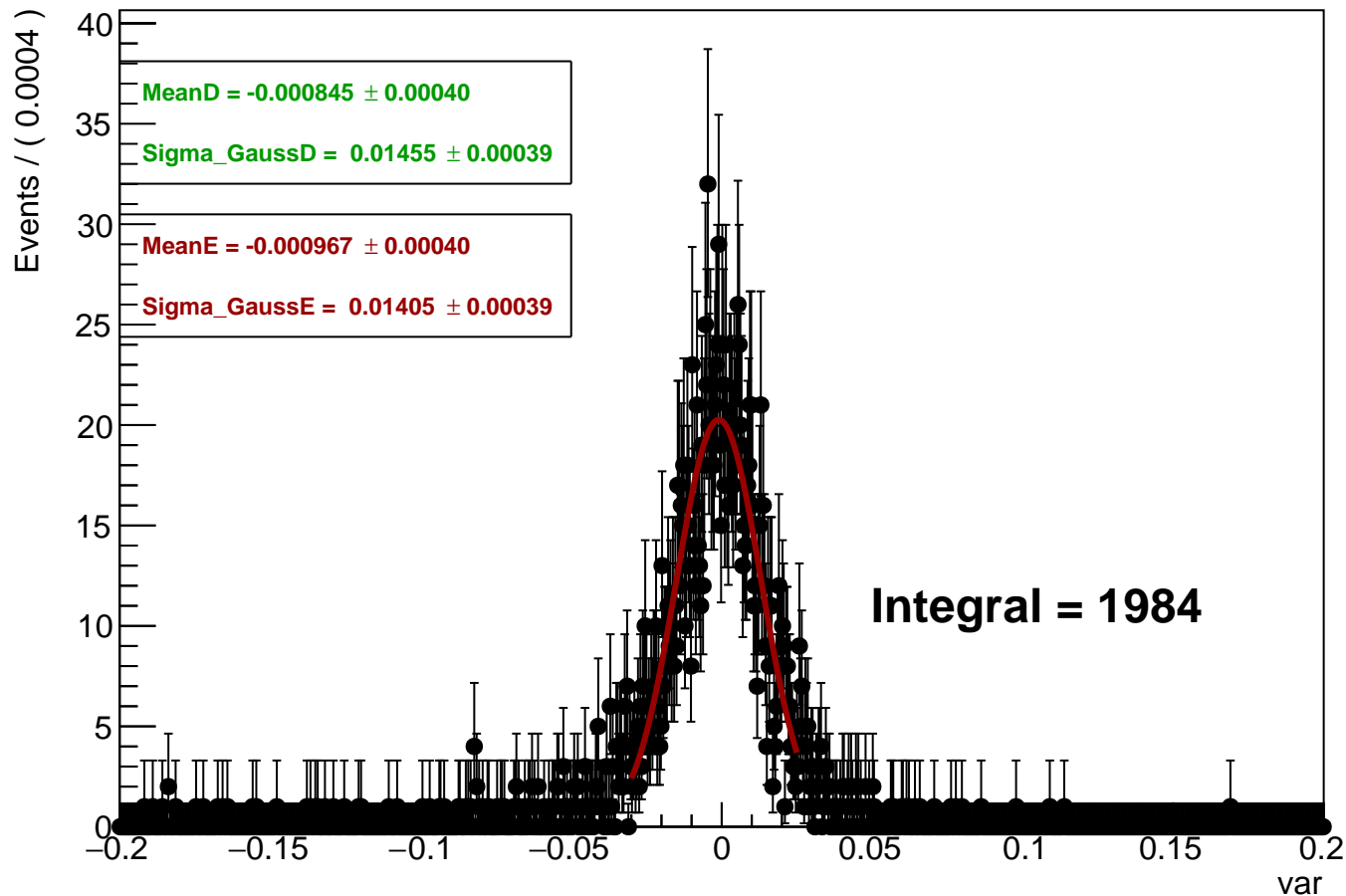
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_36_38



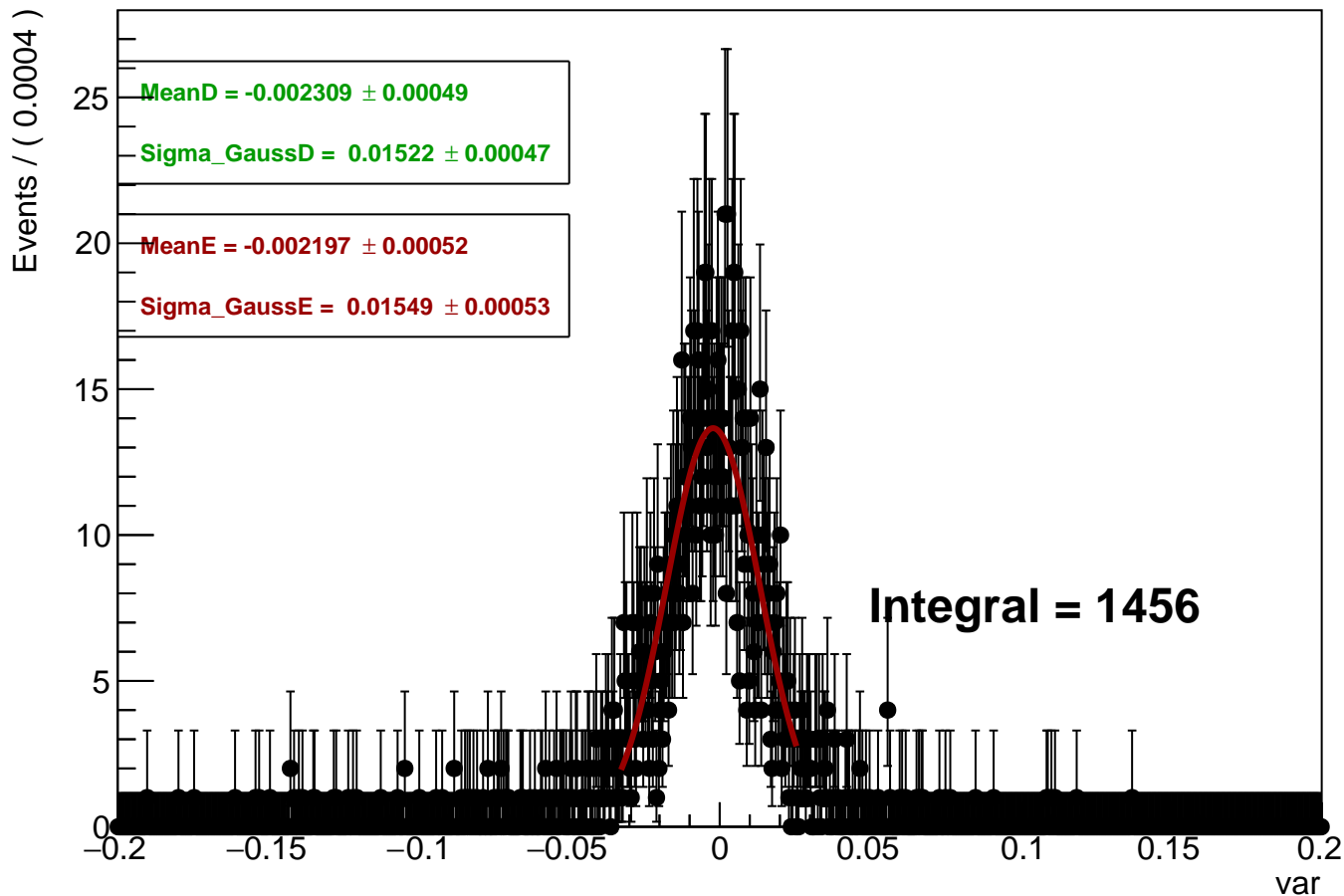
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_38_40



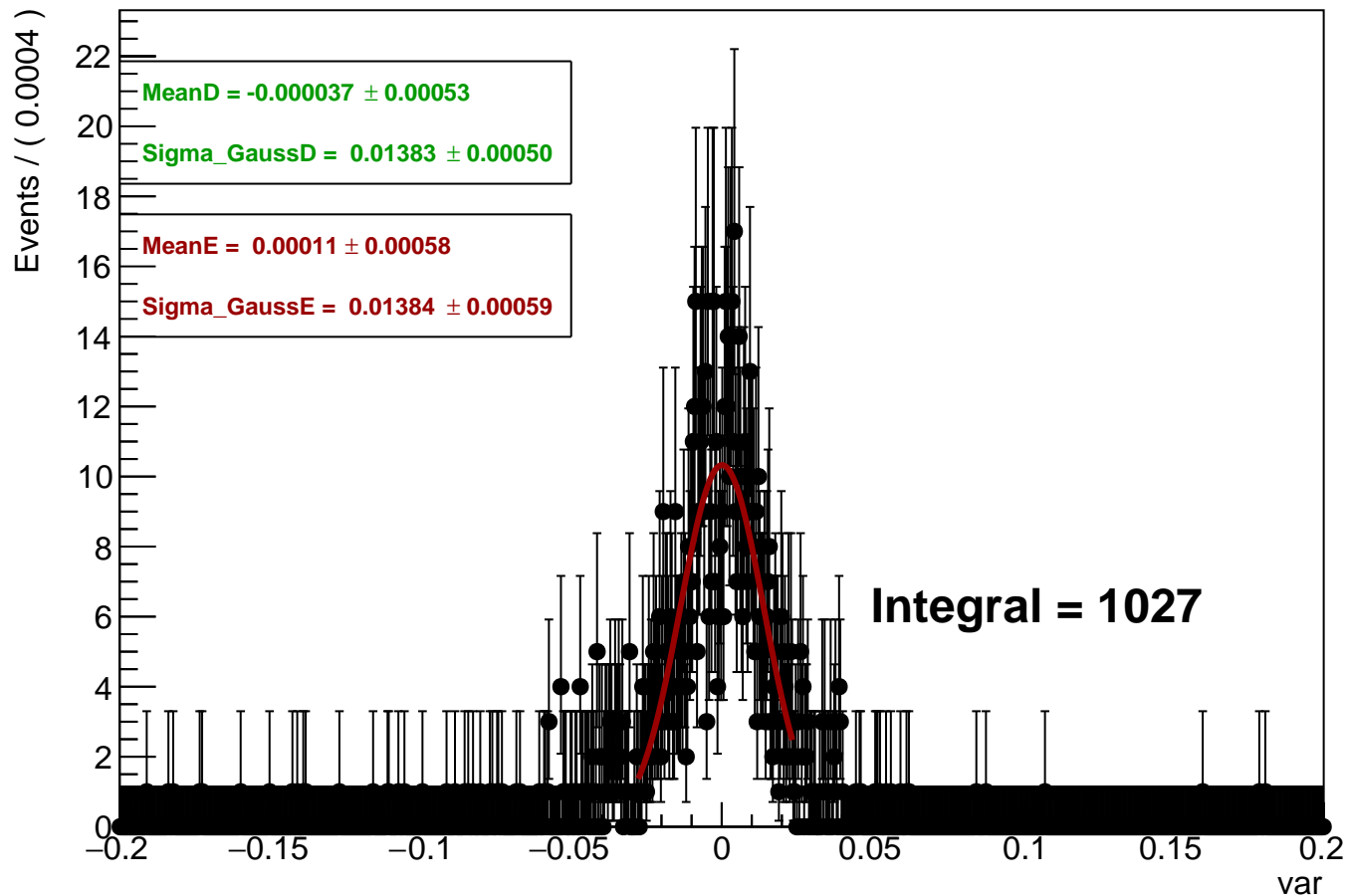
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_40_42



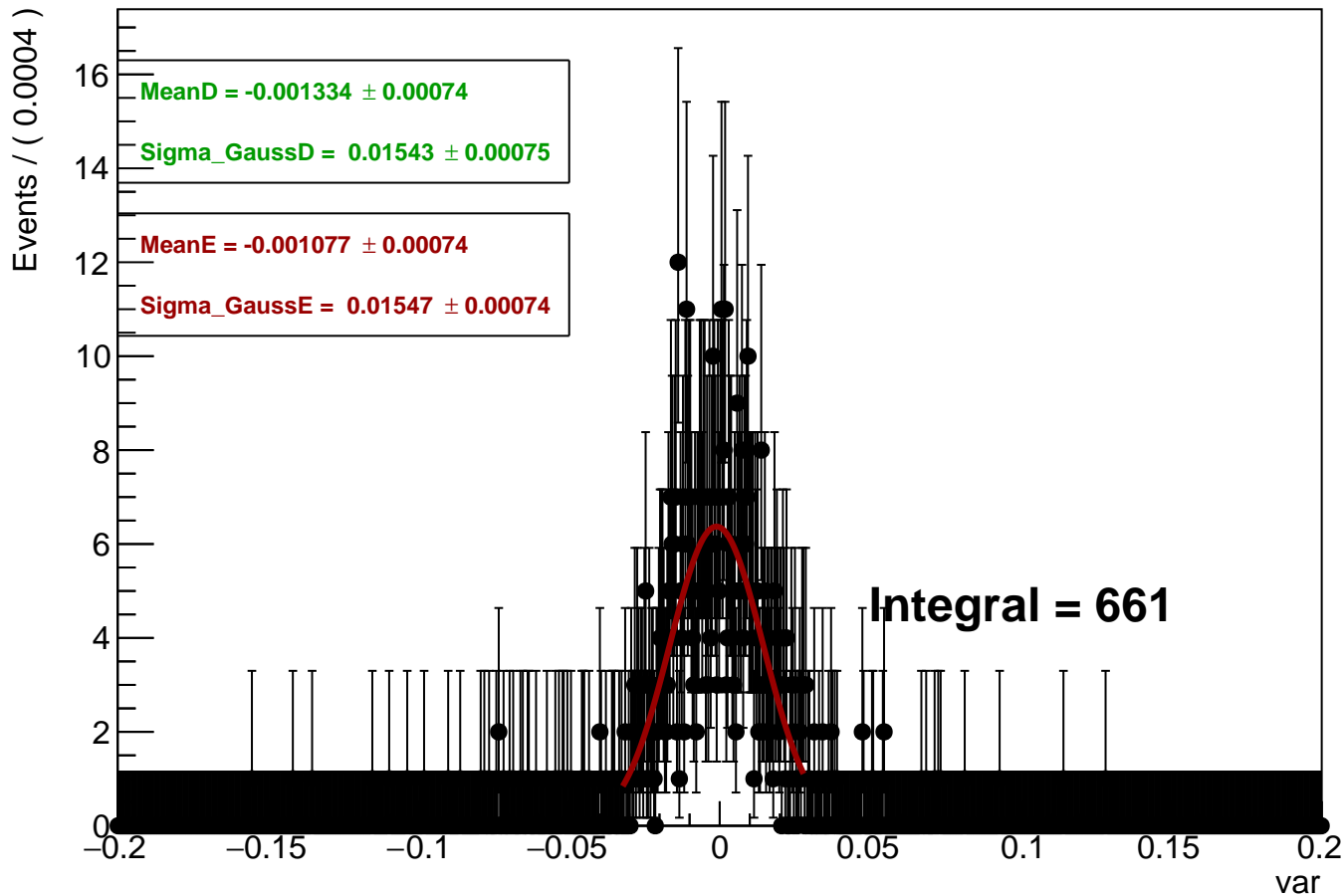
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_42_44



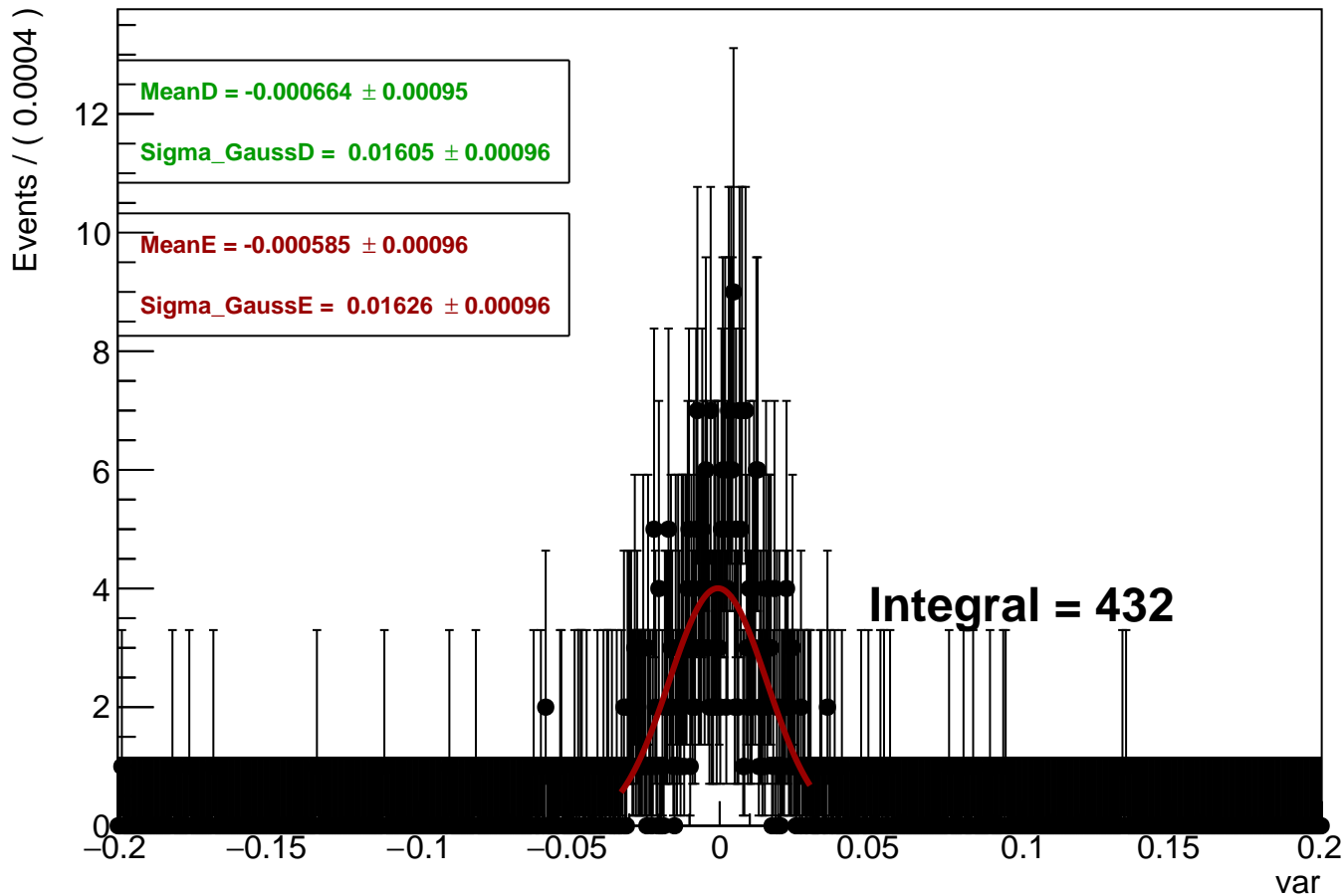
h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_44_46



h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_46_48



h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_48_50



h2_Resolution_Run3_Roch_Inclusive_d0InclusivePtRes_vsVtx_50_60

