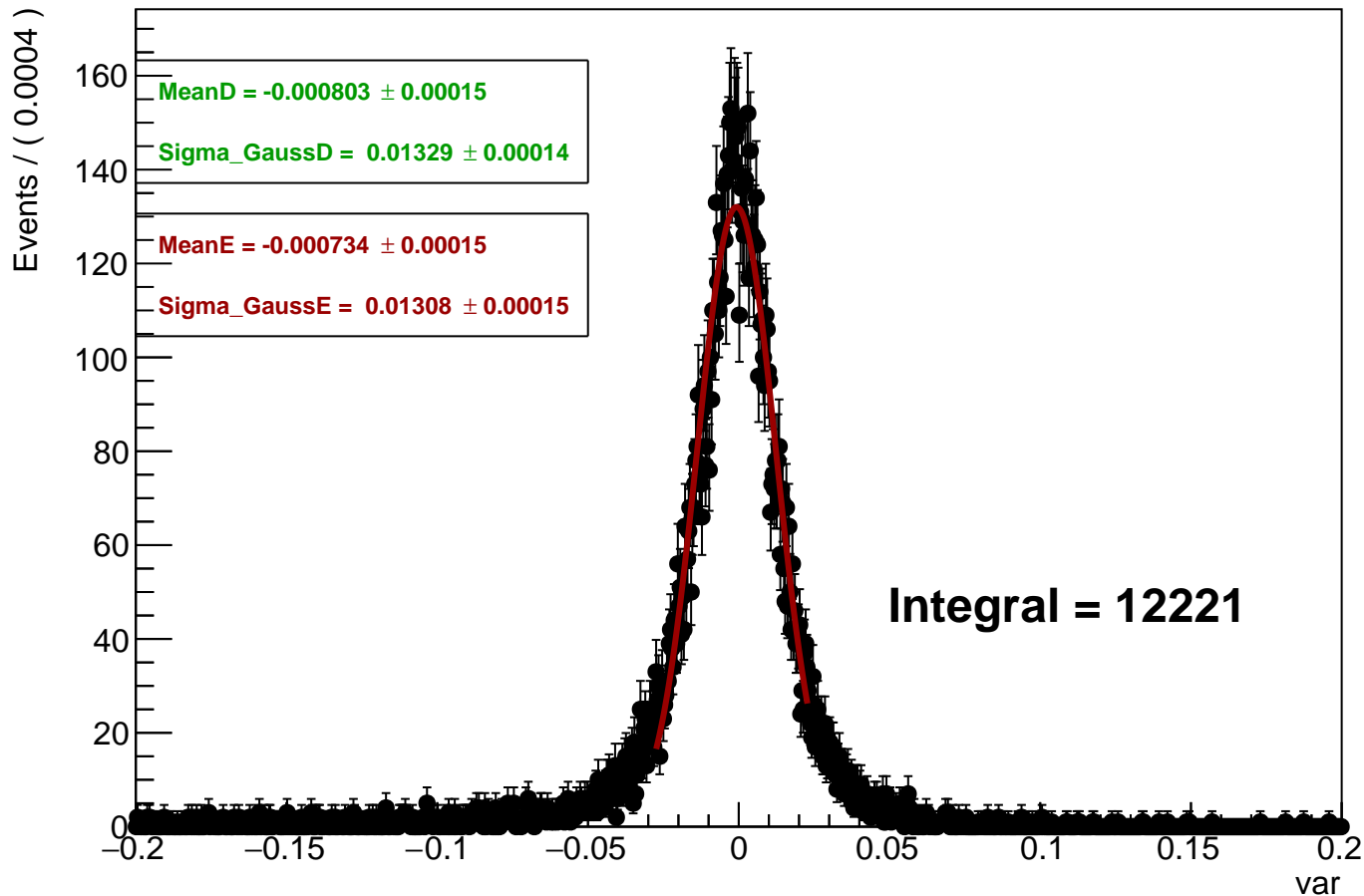
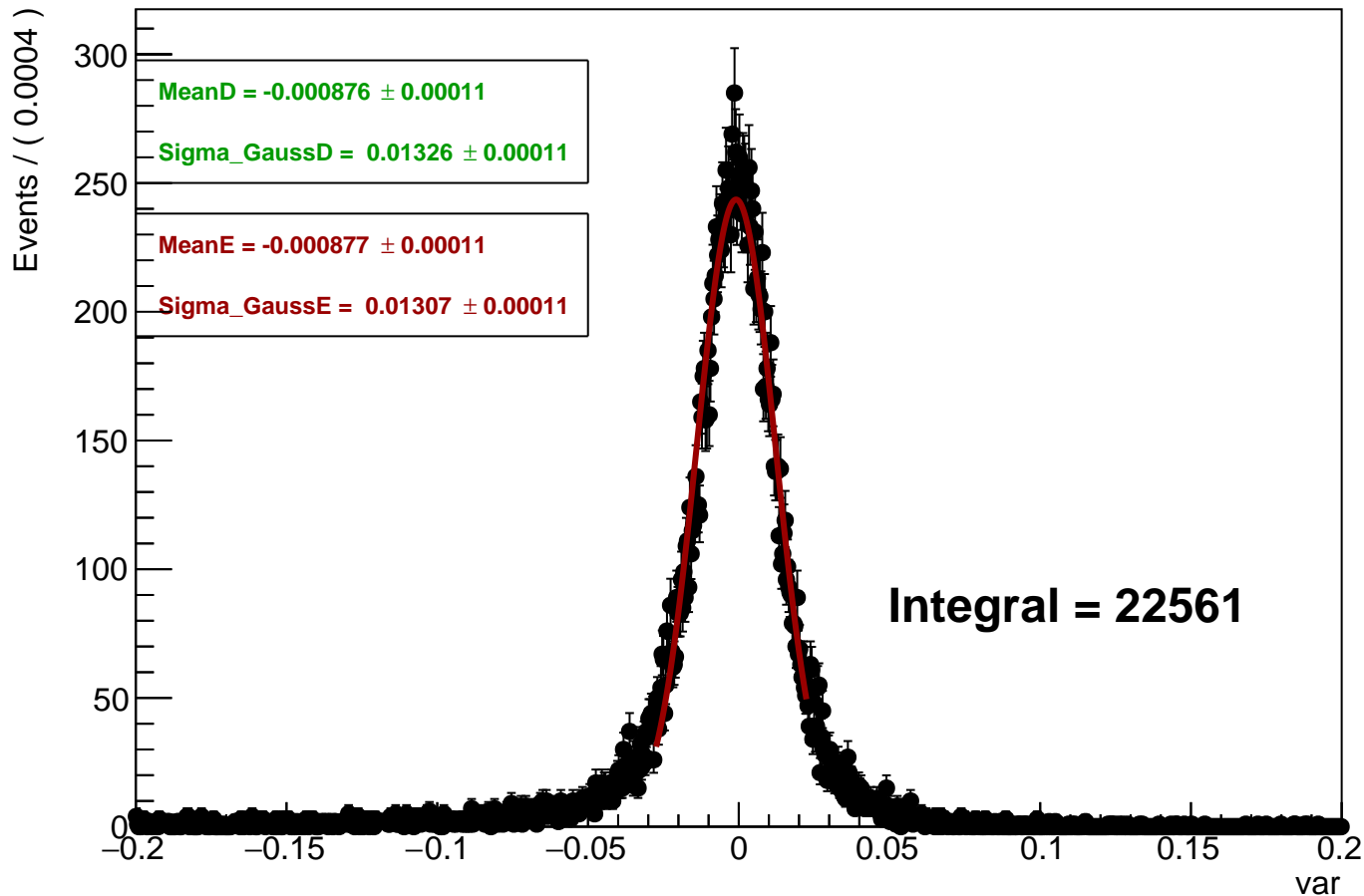
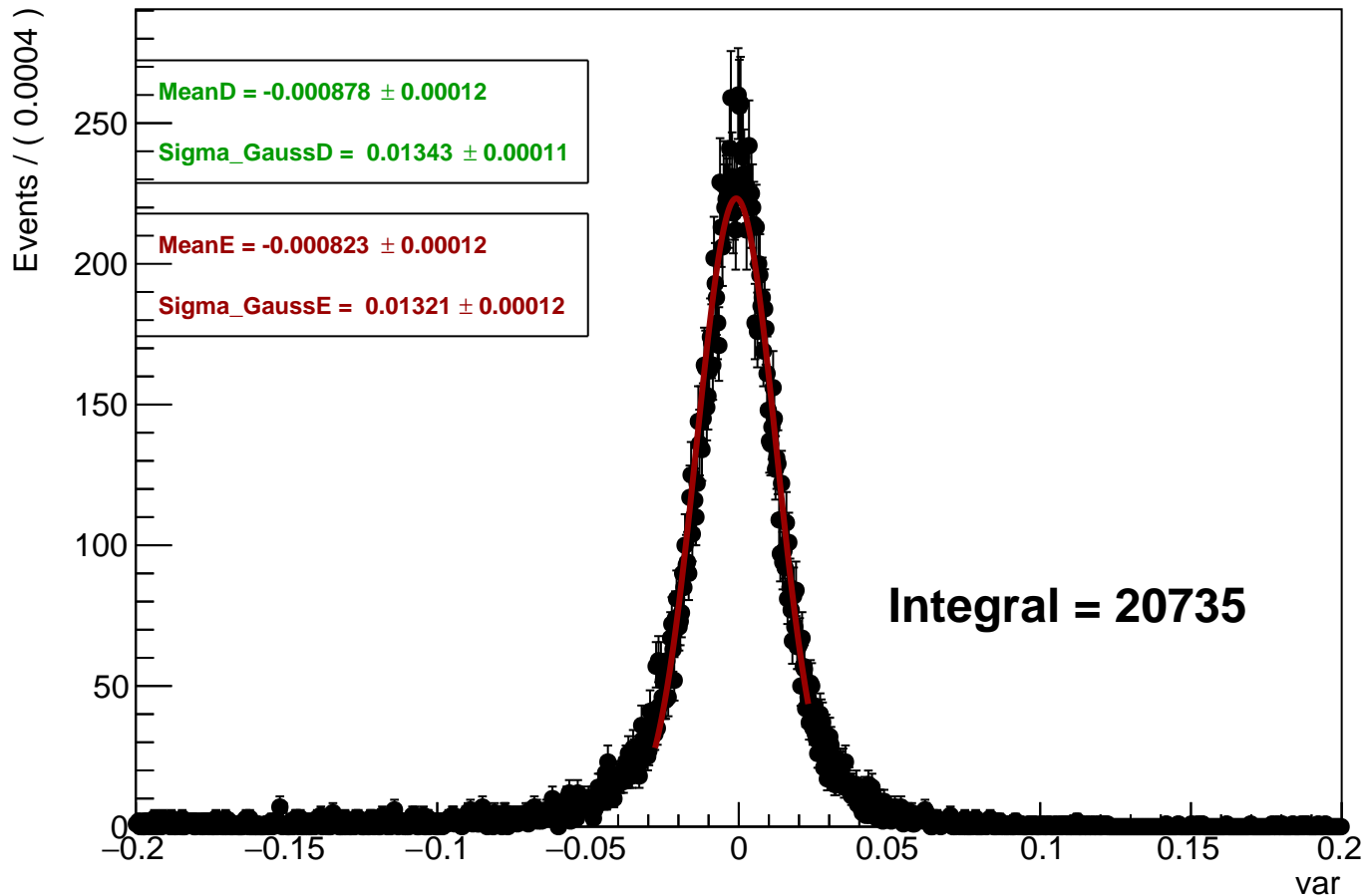
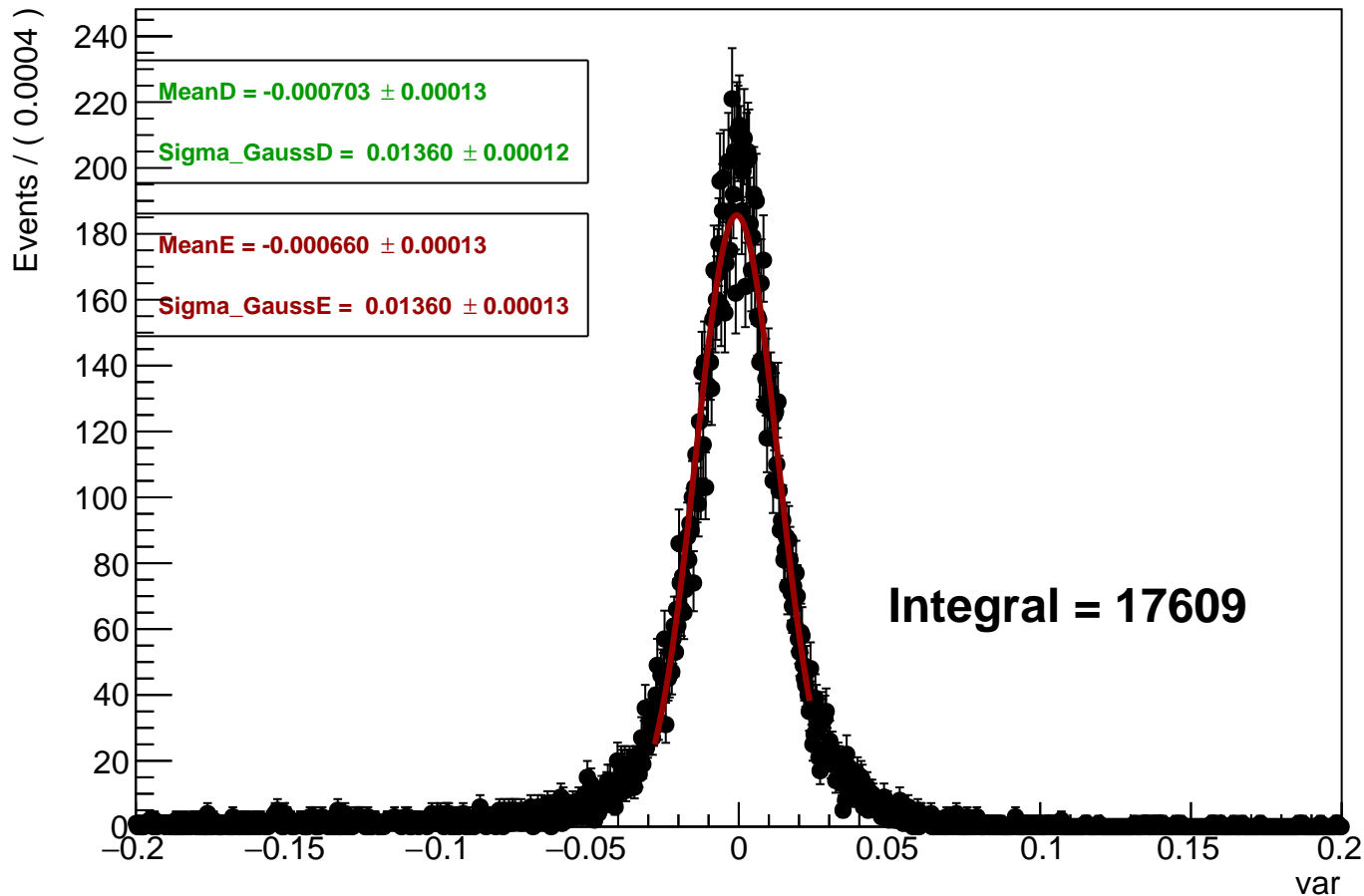


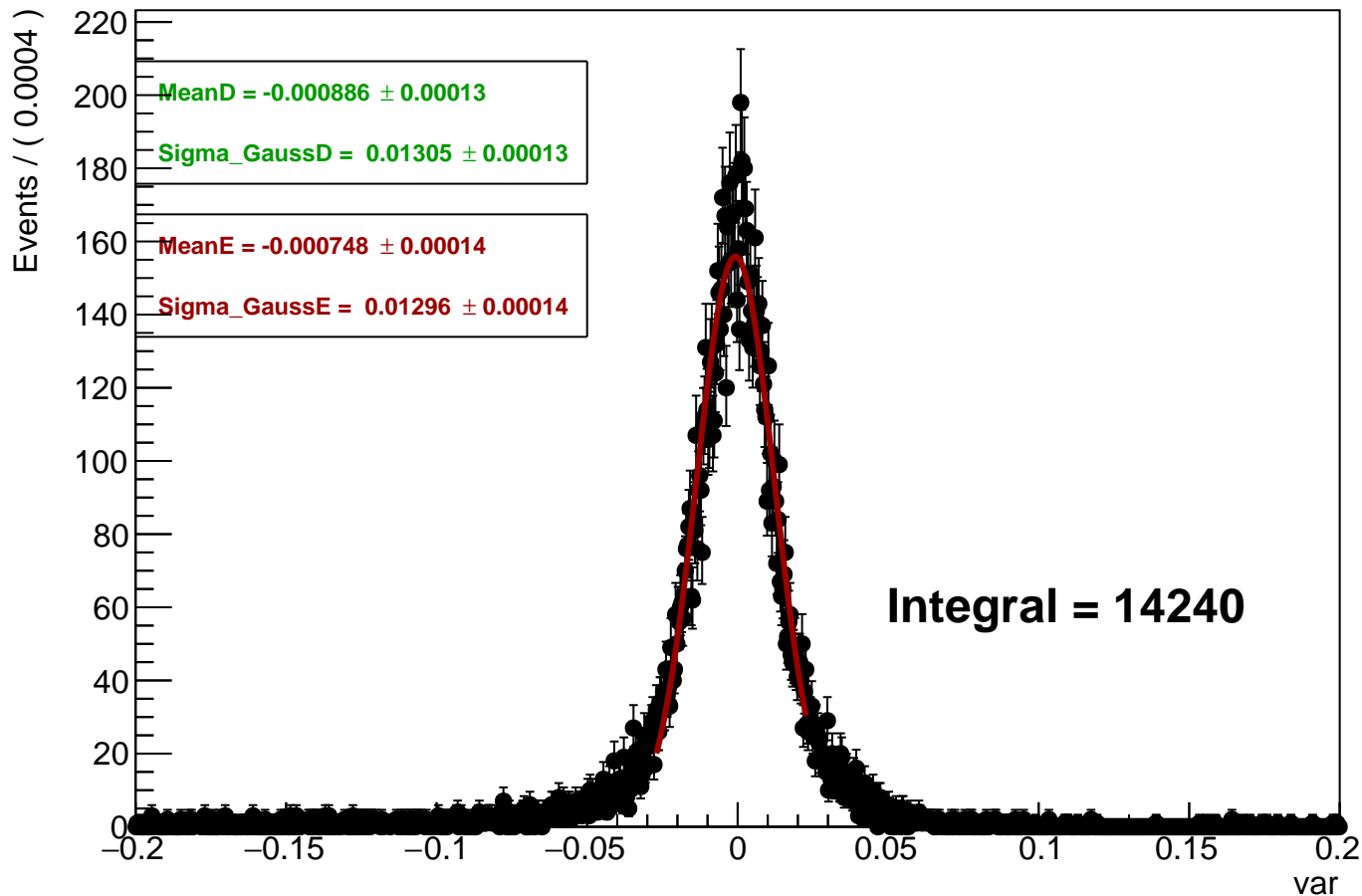
h2_Resolution_UL_Roch_VX+BS_Inclusive_d0InclusivePtRes_vsVtx_27_28

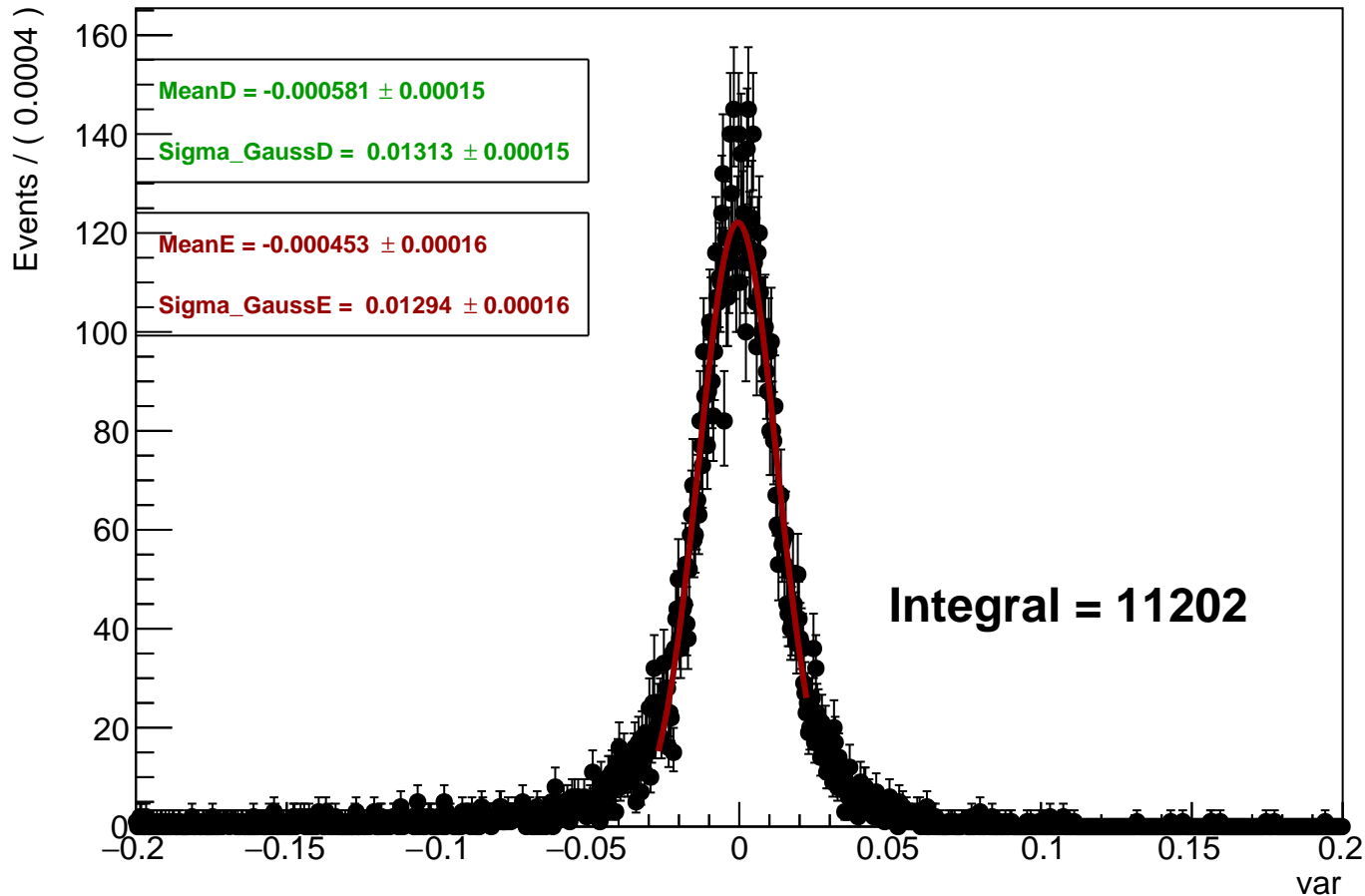


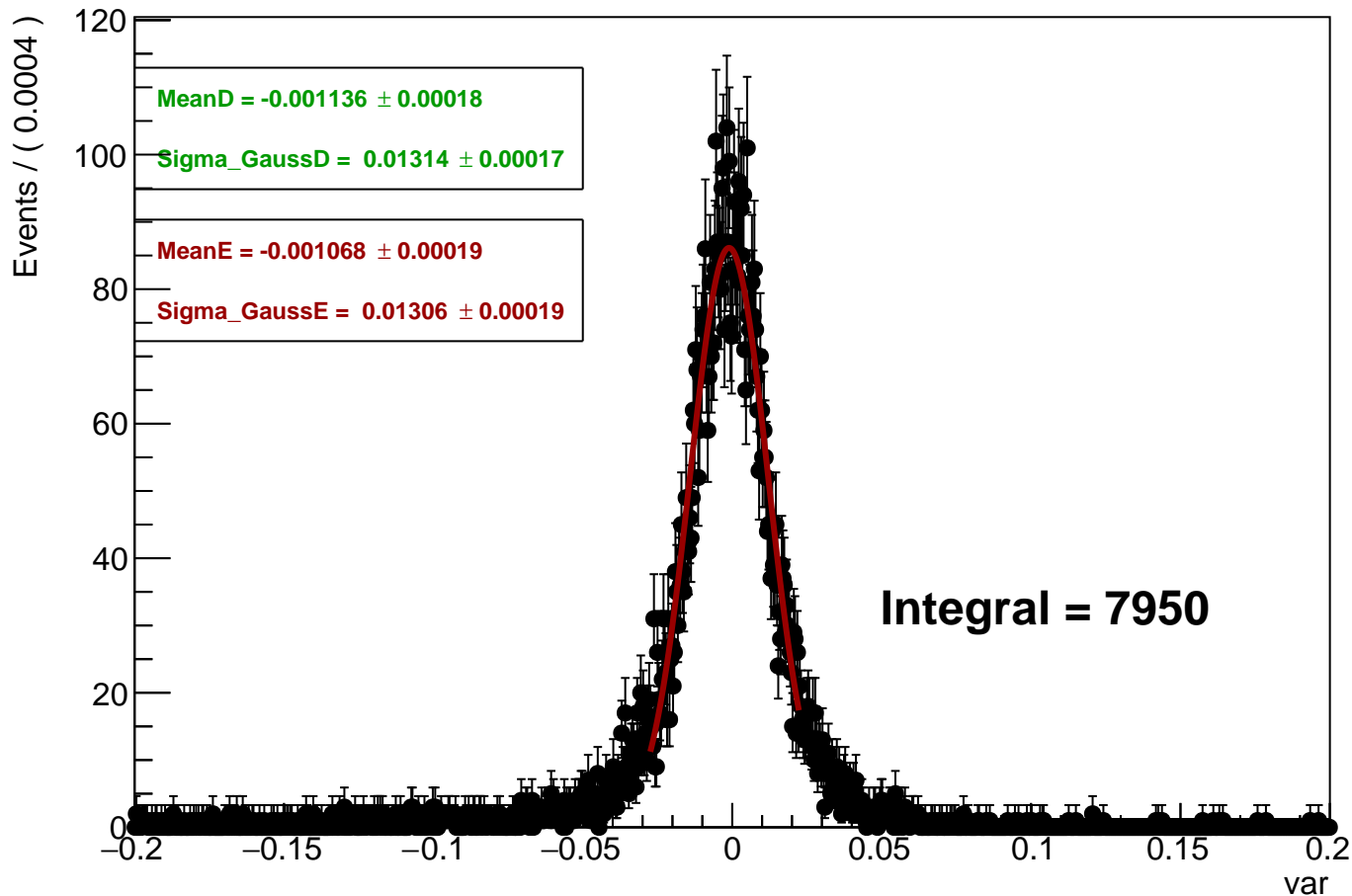


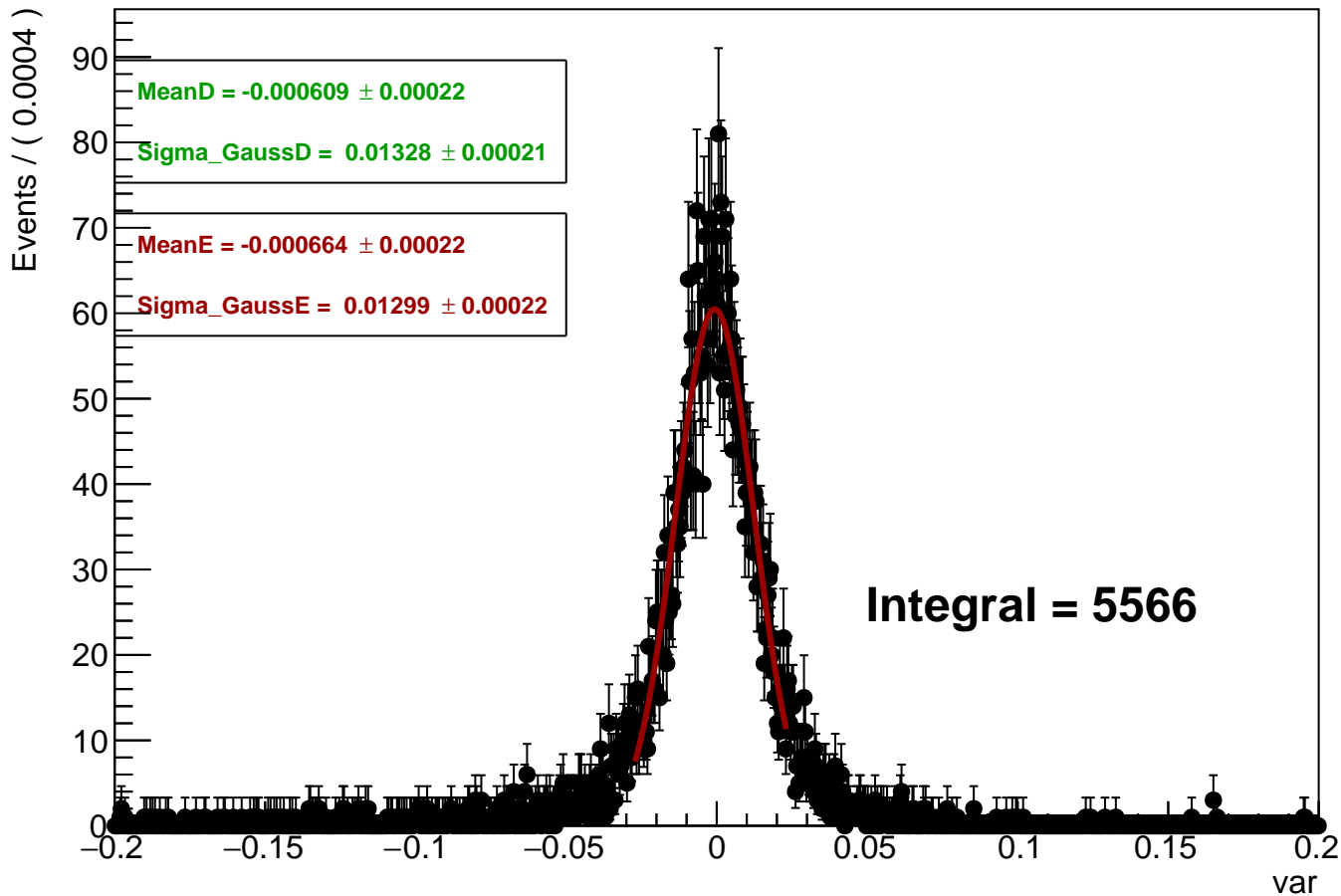


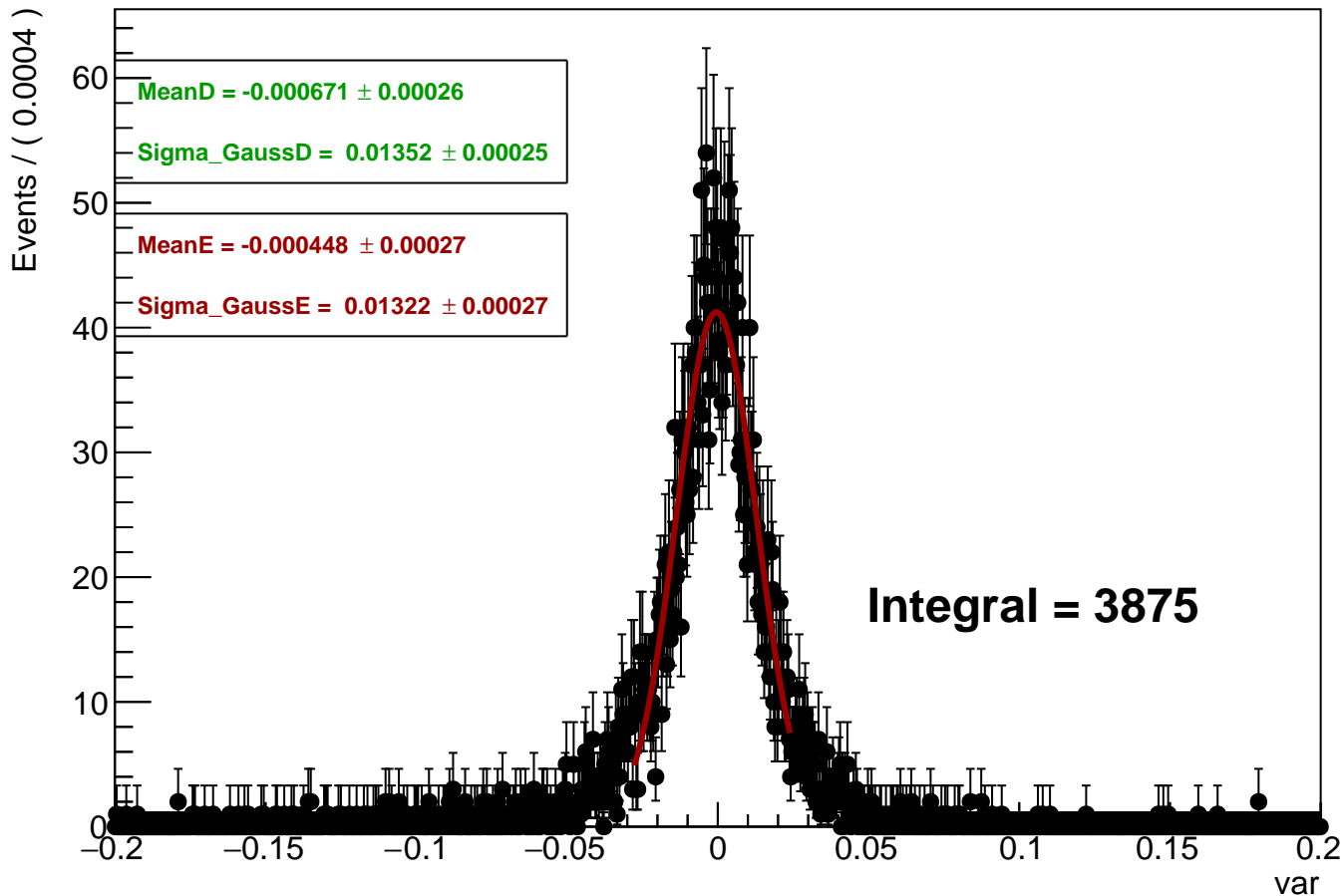


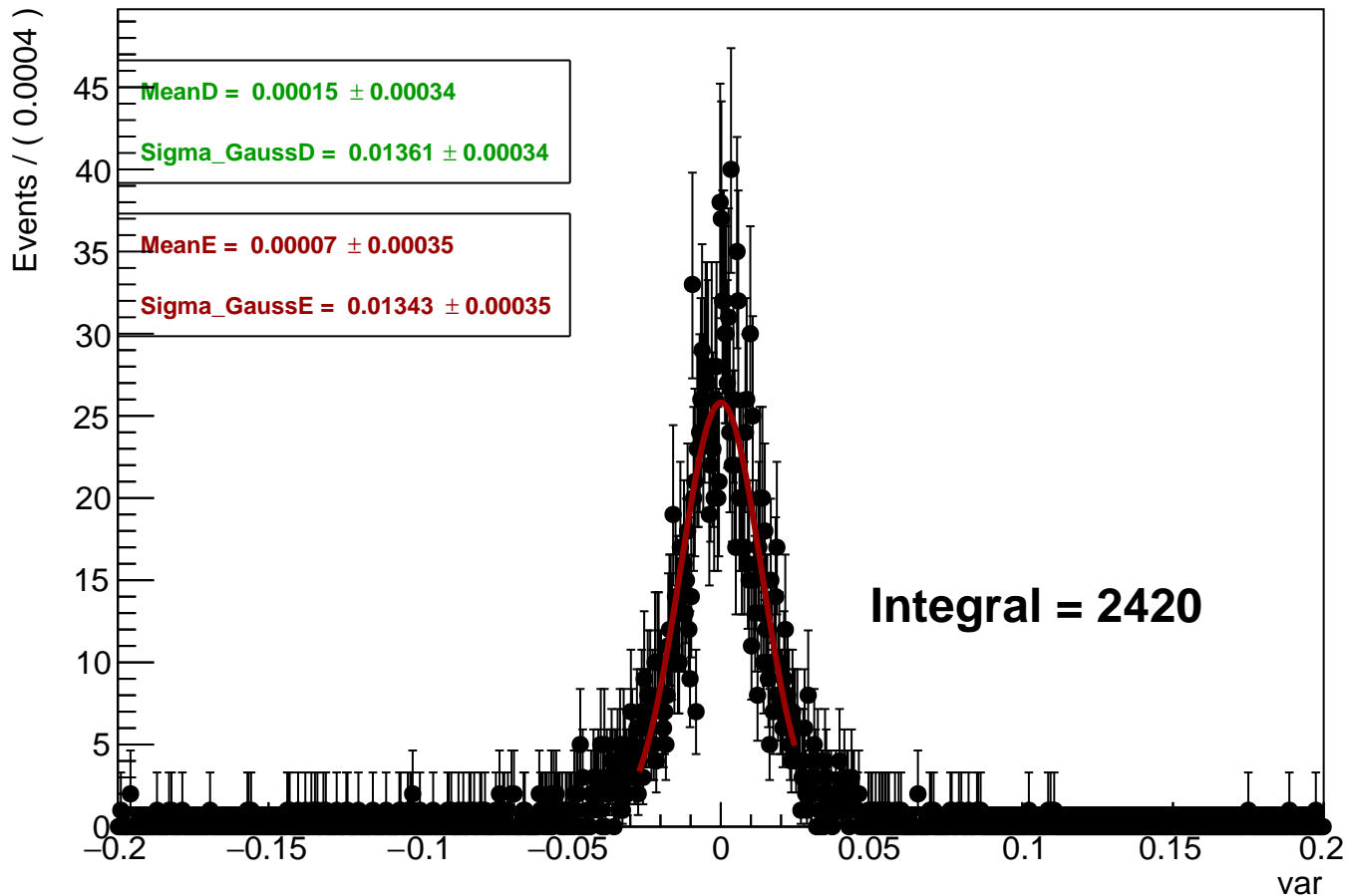


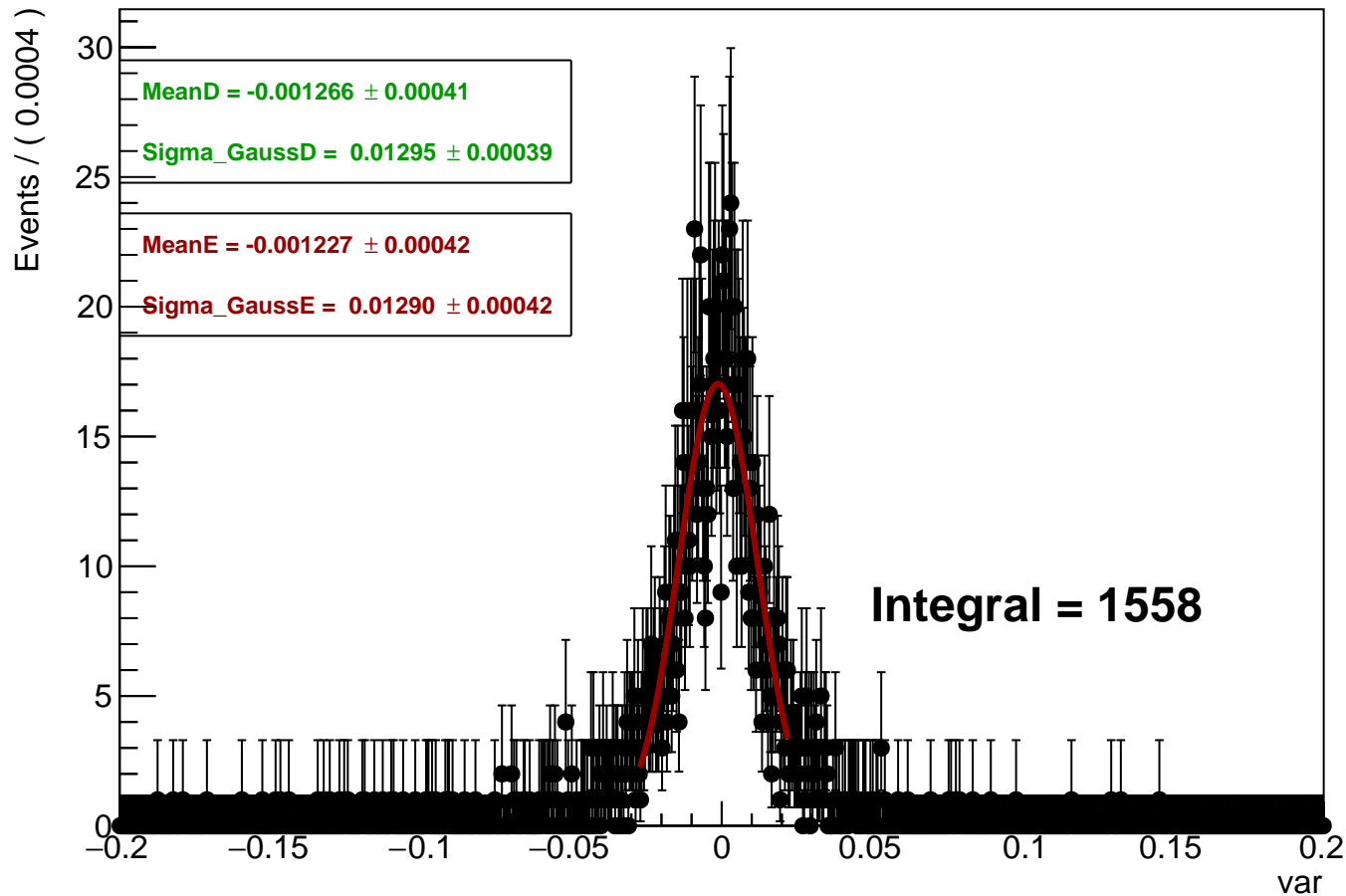




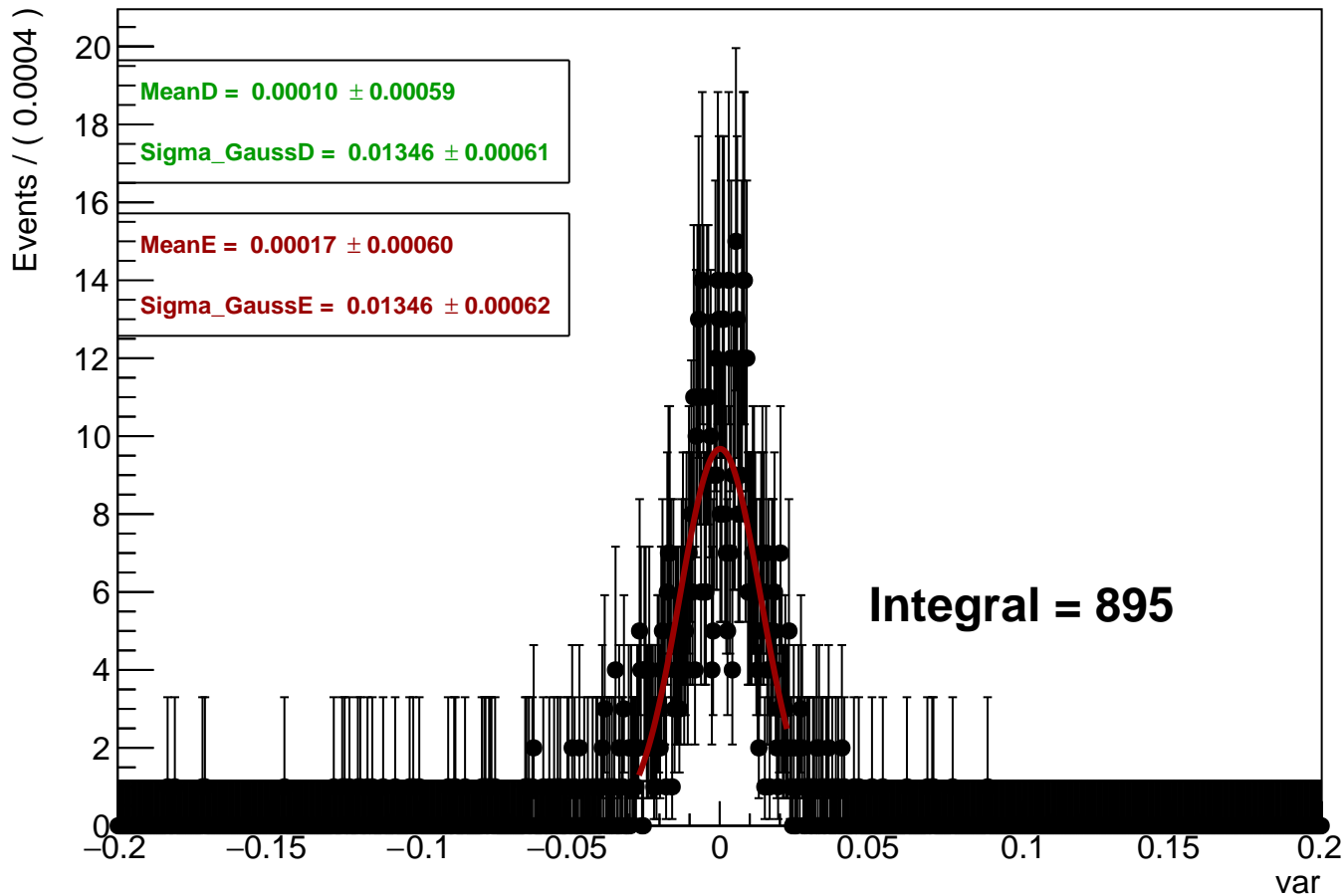








h2_Resolution_UL_Roch_VX+BS_Inclusive_d0InclusivePtRes_vsVtx_48_50



h2_Resolution_UL_Roch_VX+BS_Inclusive_d0InclusivePtRes_vsVtx_50_60

