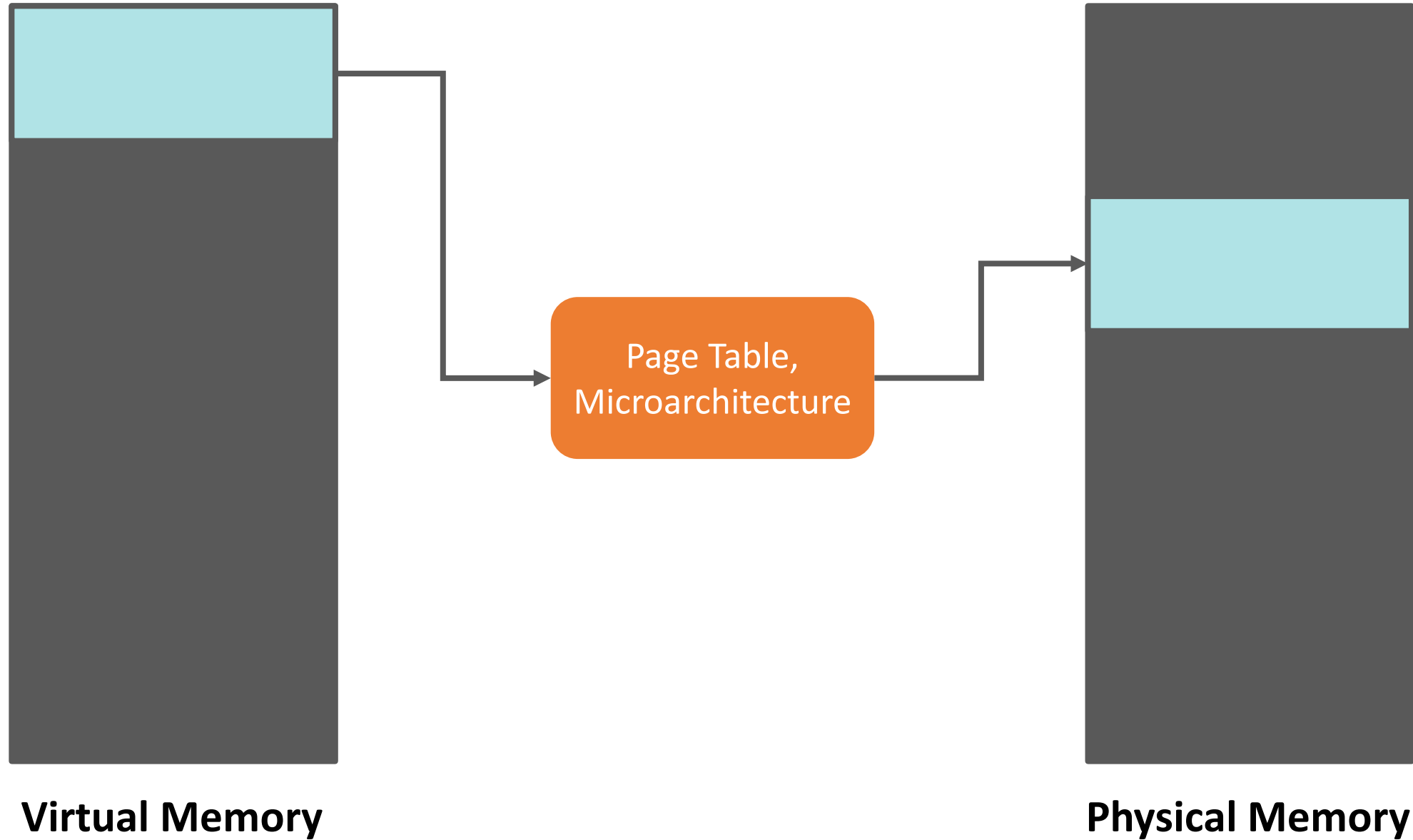
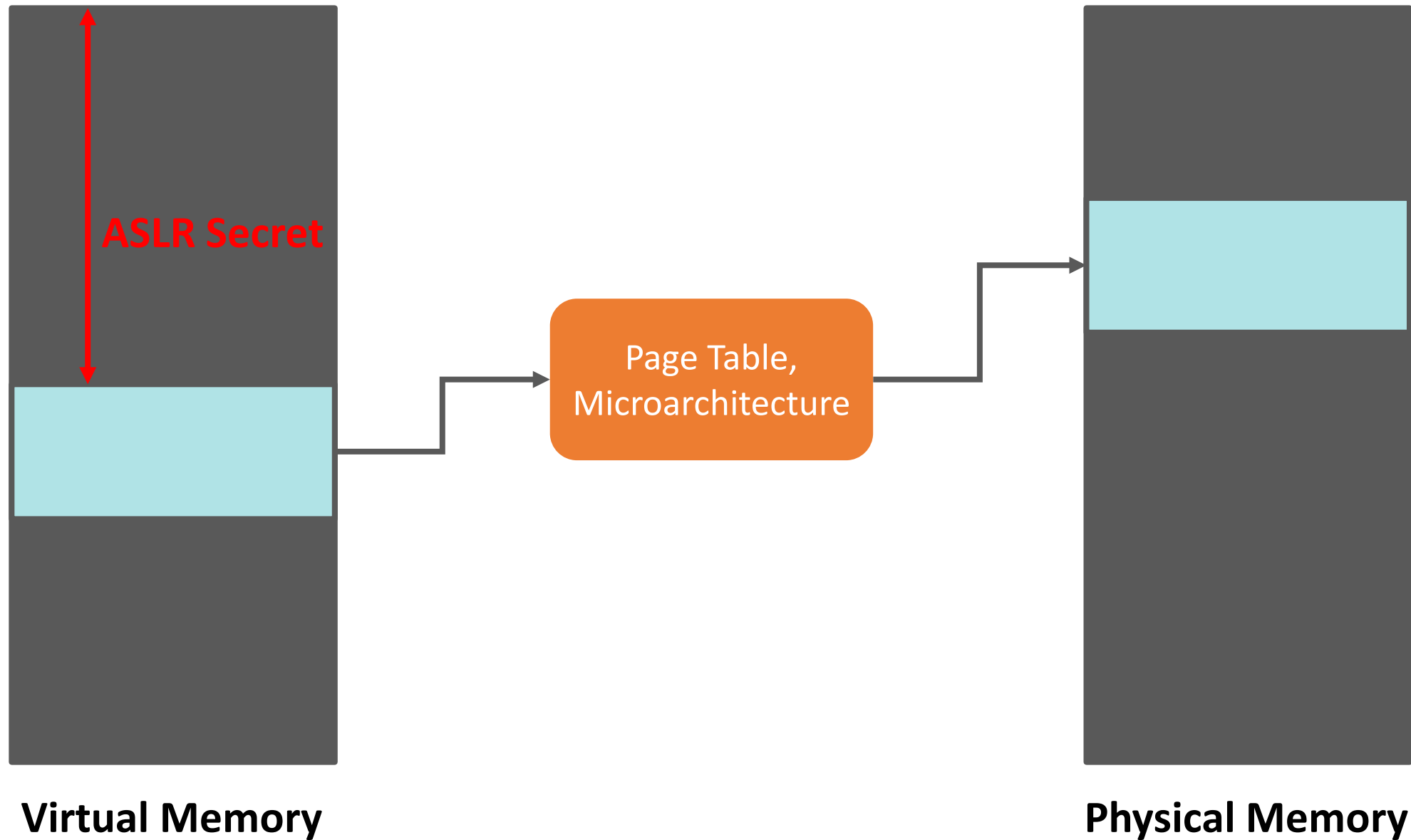


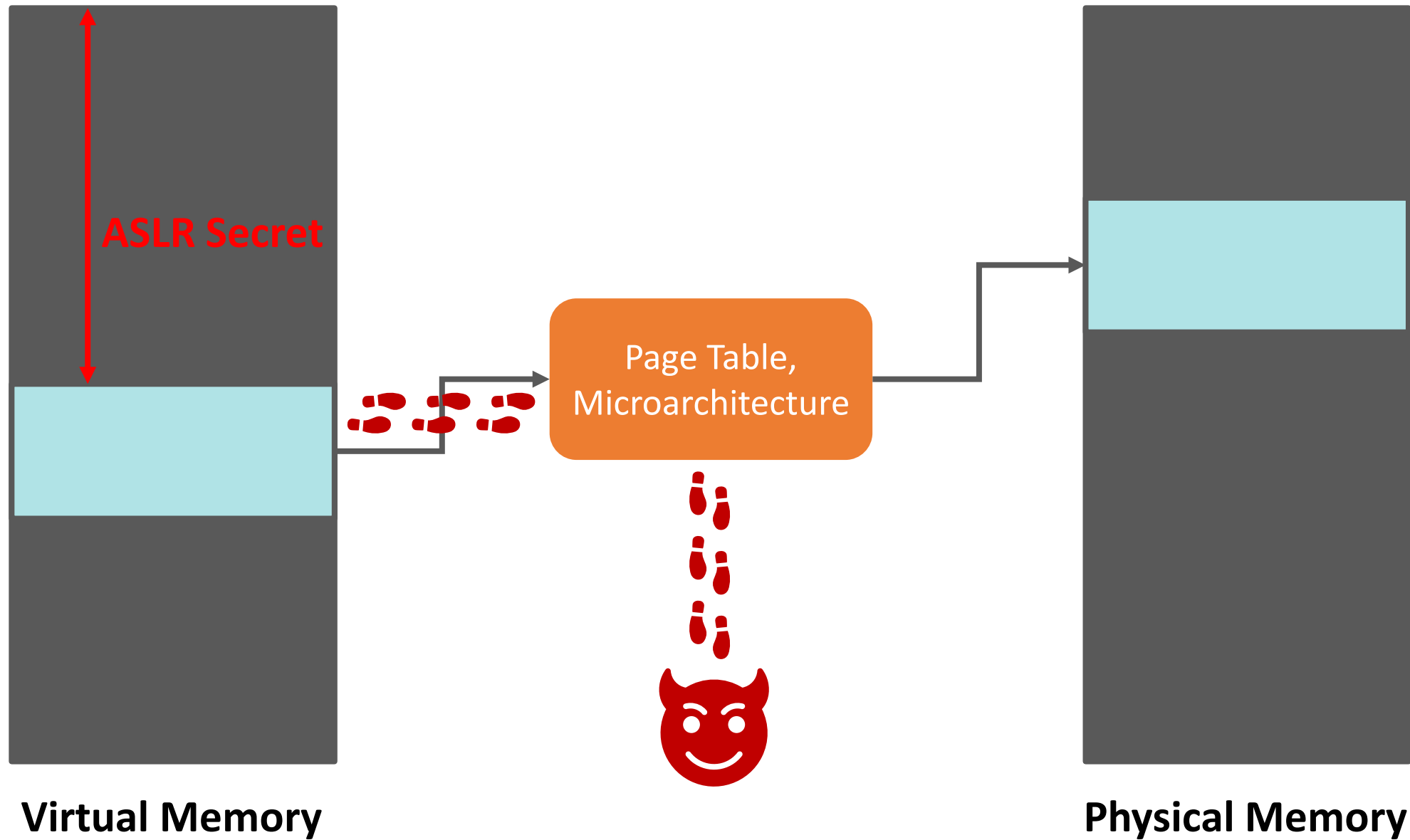
Oreo: Protecting ASLR Against Microarchitectural Attacks

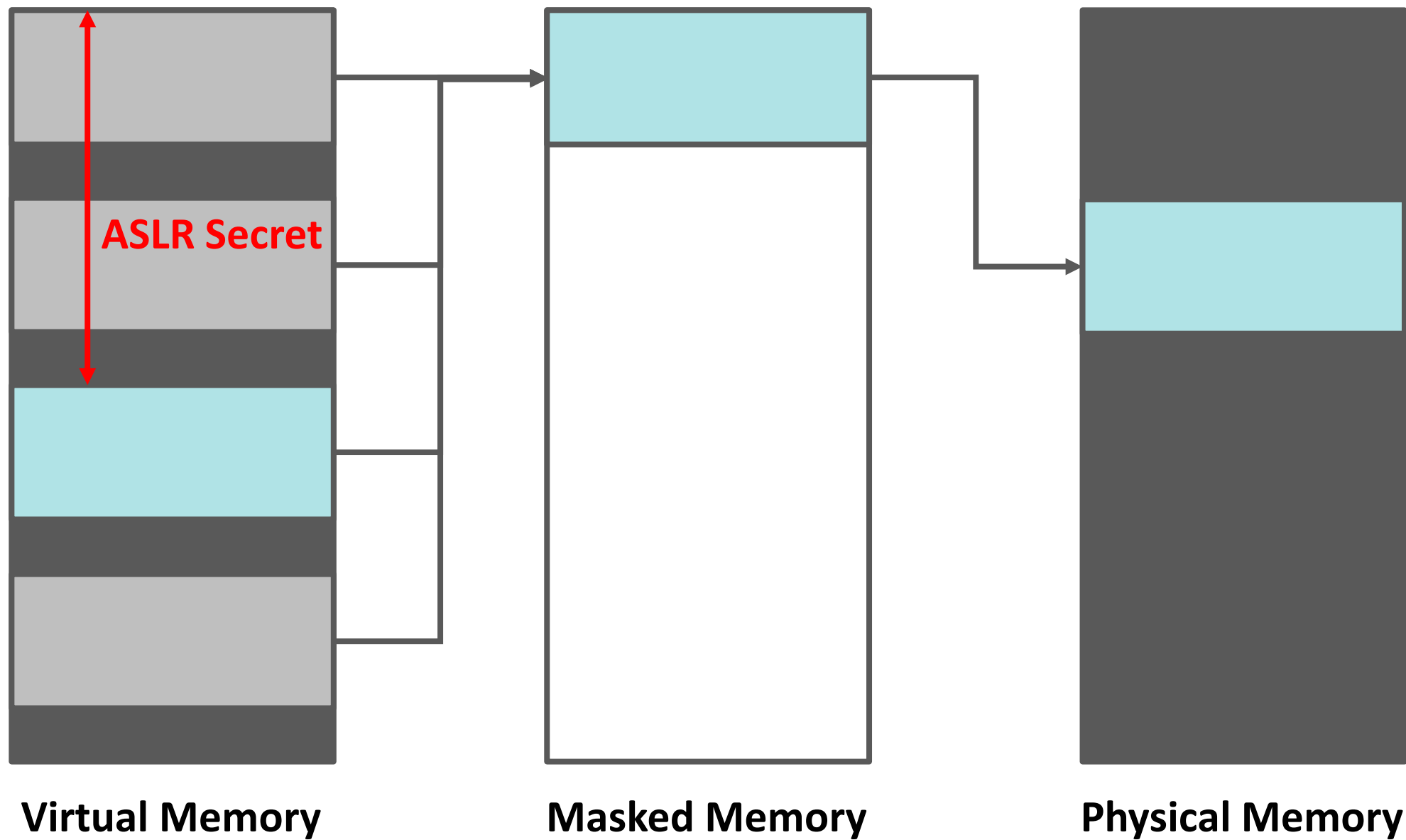
Shixin Song, Joseph Zhang, Mengjia Yan (MIT)

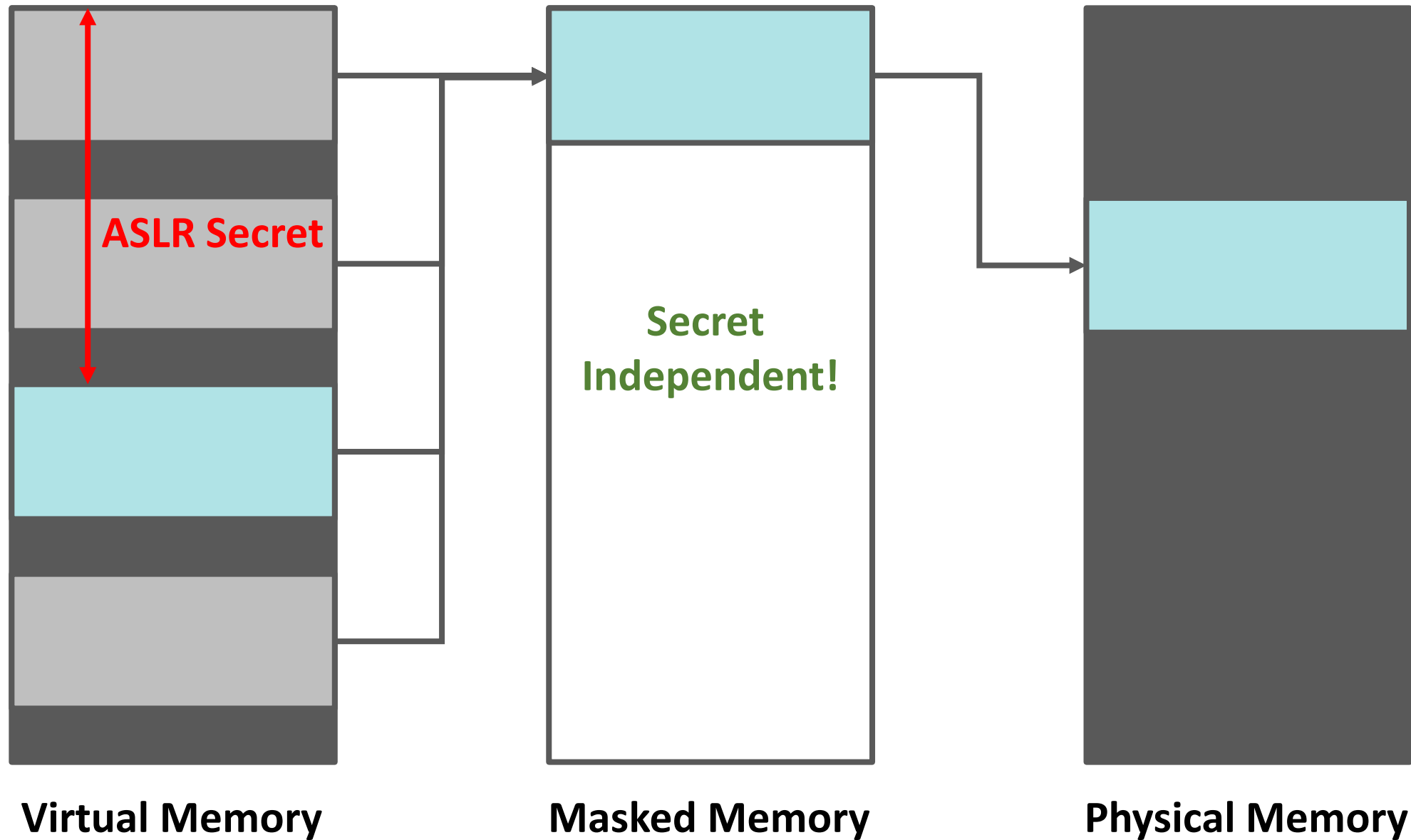




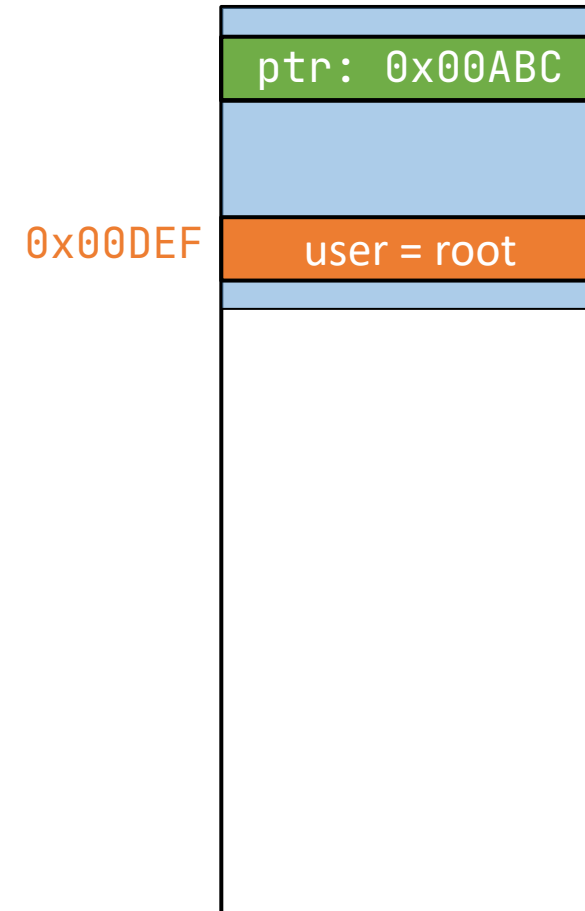




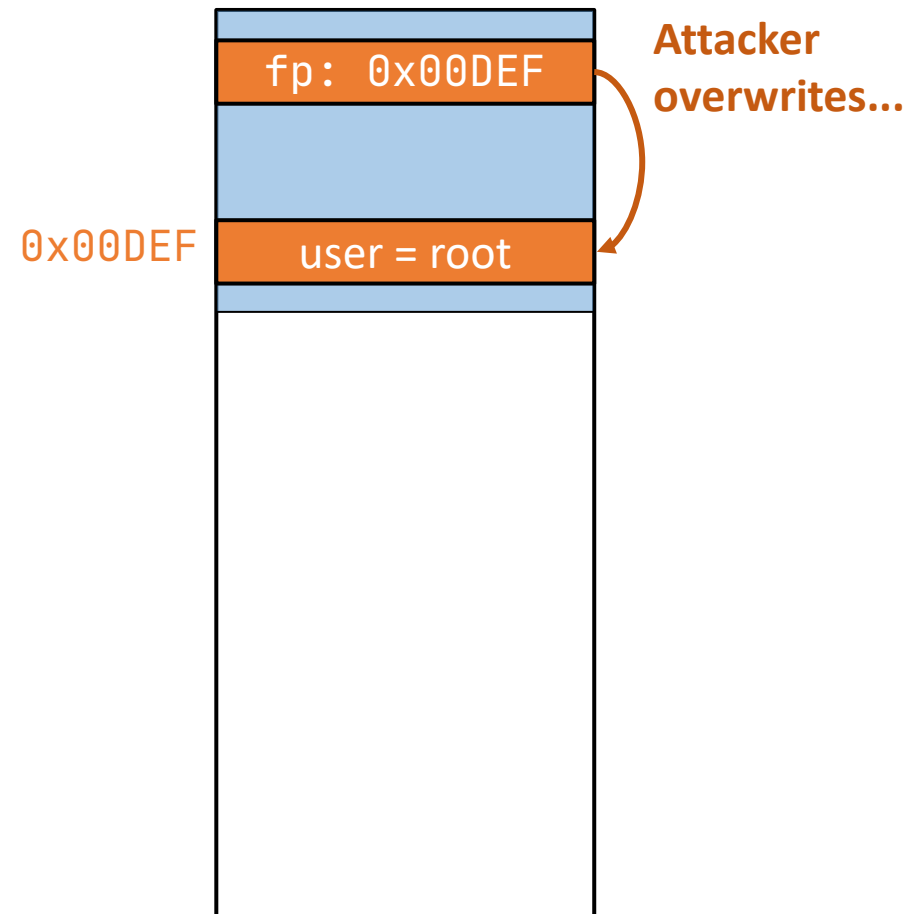




Address Space Layout Randomization (ASLR)

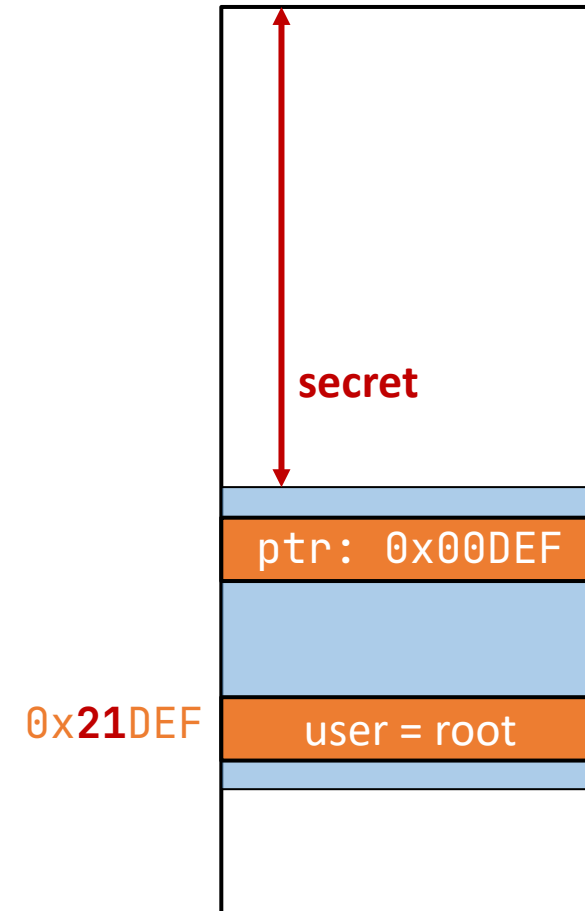


Address Space Layout Randomization (ASLR)



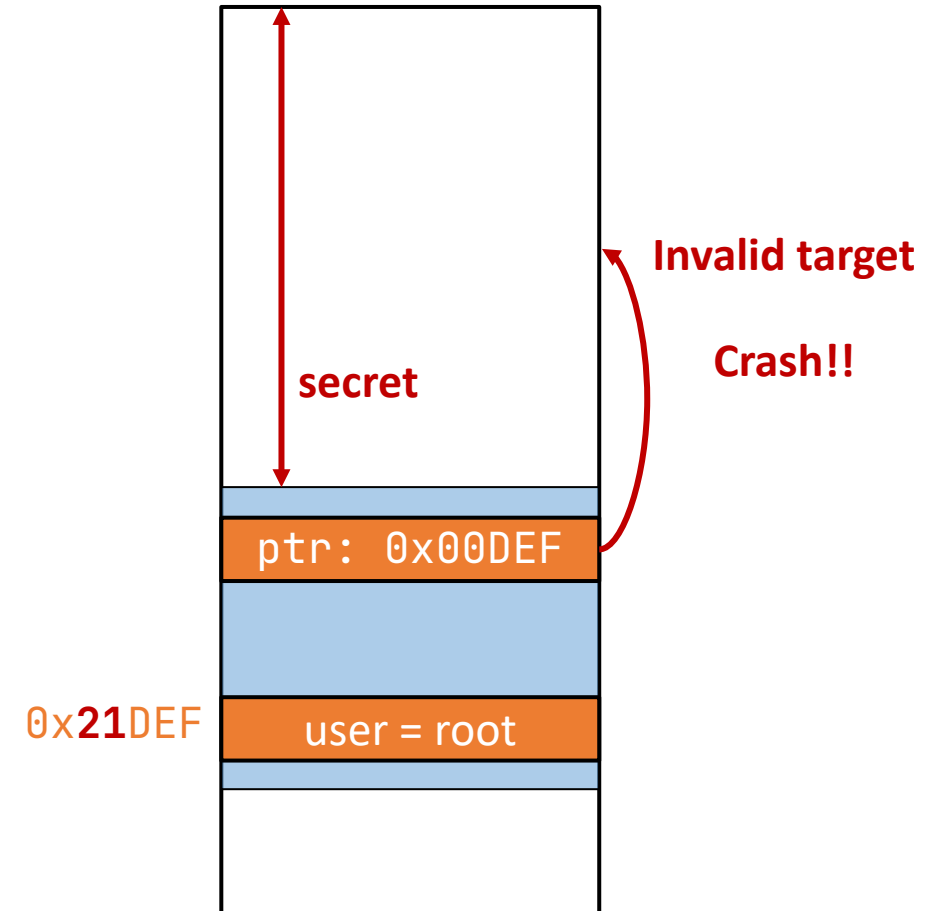
Address Space Layout Randomization (ASLR)

- ASLR is to relocate victim code with a randomized offset.



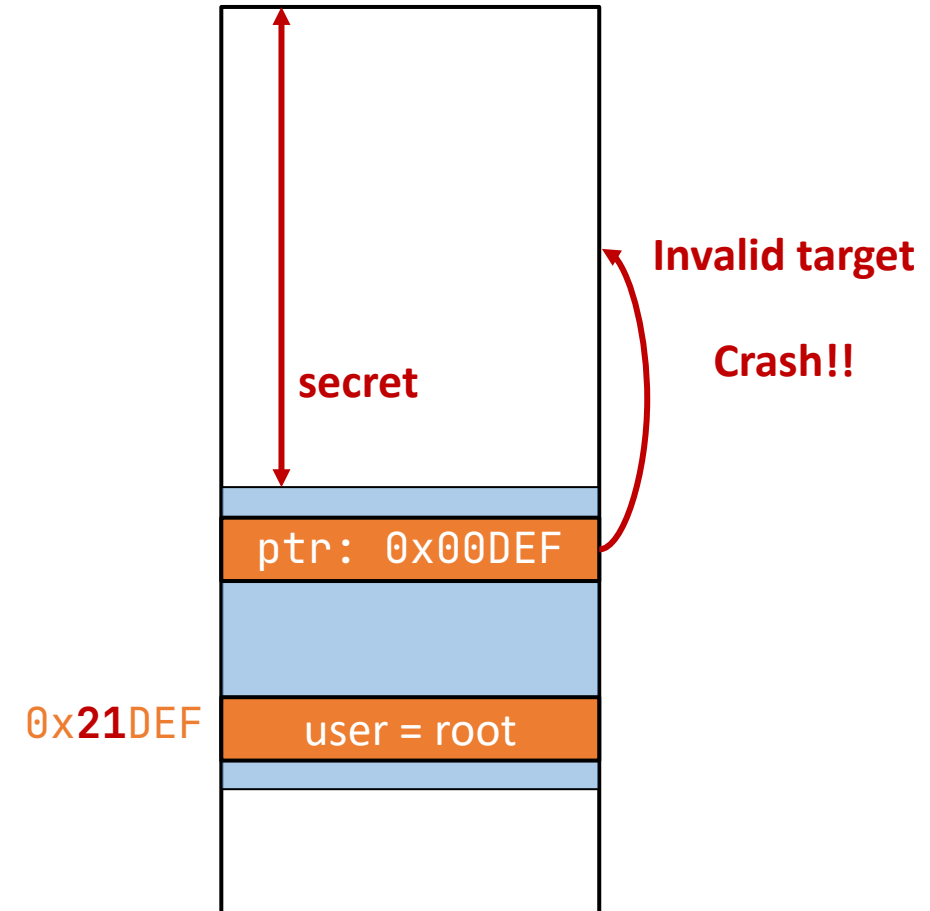
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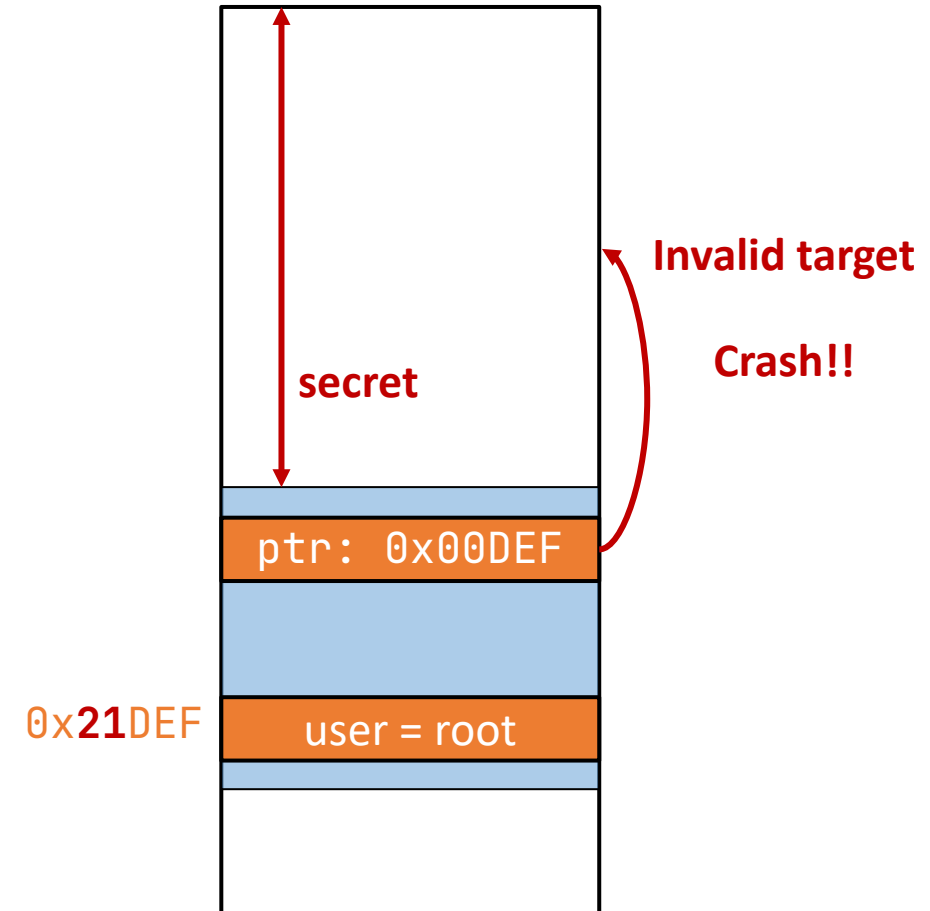
Address Space Layout Randomization (ASLR)

- ASLR is to relocate victim code with a randomized offset.
- Code reuse attacks need to perform an extra step to bypass ASLR.



Address Space Layout Randomization (ASLR)

- ASLR is to relocate victim code with a randomized offset.
- Code reuse attacks need to perform an extra step to bypass ASLR.
- ASLR is widely deployed in modern systems:
 - e.g., Linux, Windows, macOS



However, bypassing ASLR becomes extremely easy with microarchitectural attacks.

A word cloud featuring various microarchitectural attacks. The word 'Binoculars' is the largest and most central, written in a dark red, serif font. Other attacks are scattered around it in different colors and sizes, including blue, dark blue, and maroon. The attacks listed are: Osiris, Jump Over ASLR, Spectre Probing, Code Region Probing, Double Page Fault, Data Bounce, TagBleed, Prefetch Attack, EchoLoad, EntryBleed, Take A Way, Phantom, AnC, and Drk.

Osiris
Jump Over ASLR
Spectre Probing
Code Region Probing
Double Page Fault
Data Bounce
TagBleed
Prefetch Attack
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EntryBleed
Take A Way
Phantom
AnC
Drk
Binoculars

However, bypassing ASLR becomes extremely easy with microarchitectural attacks.

Exploiting CVE-2022-42703 - Bringing back the stack attack

Seth Jenkins, Project Zero

This prefetch code does indeed work to find the locations of the randomized CEA regions in Peter Ziljstra's proposed patch. However, the journey to that point results in code that demonstrates another deeply significant issue - KASLR is comprehensively compromised on x86 against local attackers, and has been for the past several years, and will be for the indefinite future. There are presently no plans in place to resolve the myriad microarchitectural issues that lead to side channels like this one. Future work is needed in this area in order to preserve the integrity of KASLR, or alternatively, it is probably time to accept that KASLR is no longer an effective mitigation against local attackers and to develop defensive code and mitigations that accept its limitations.

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Exploiting CVE-2022-42703 - Bringing back the stack attack

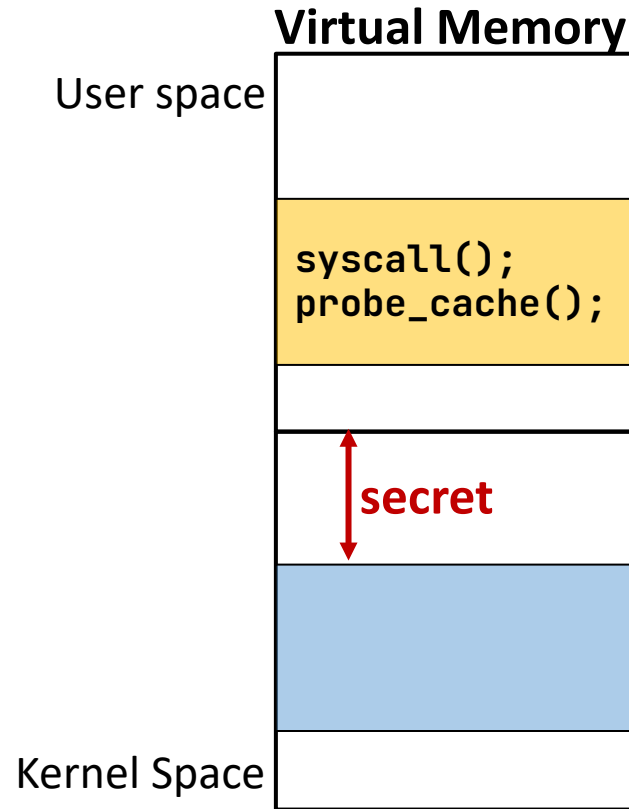
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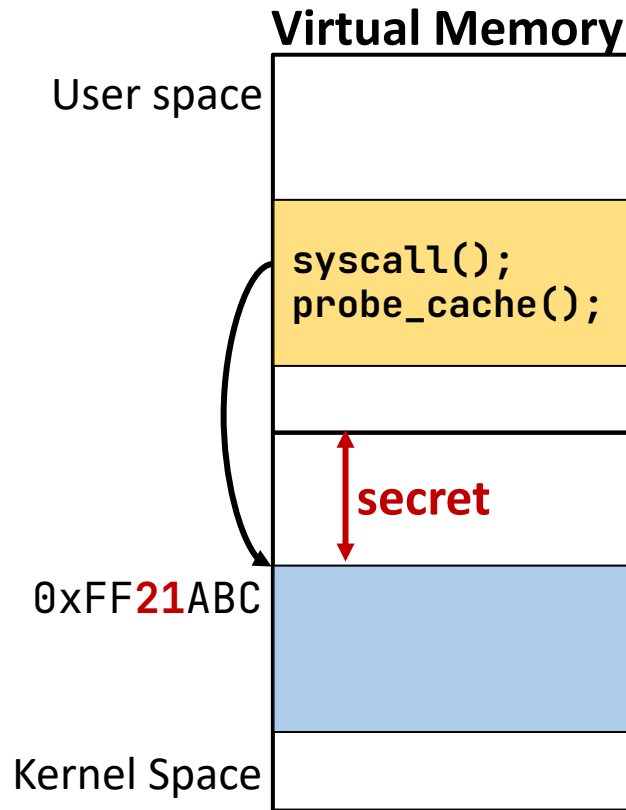
the myriad microarchitectural issues that lead to side channels like this one. Future work is needed in this area in order to preserve the integrity of KASLR, or alternatively, it is probably time to accept that KASLR is no longer an effective mitigation against local attackers and to develop defensive code and mitigations that accept its limitations.

Attack 1: ALSR Dependent Address Translation^[1]



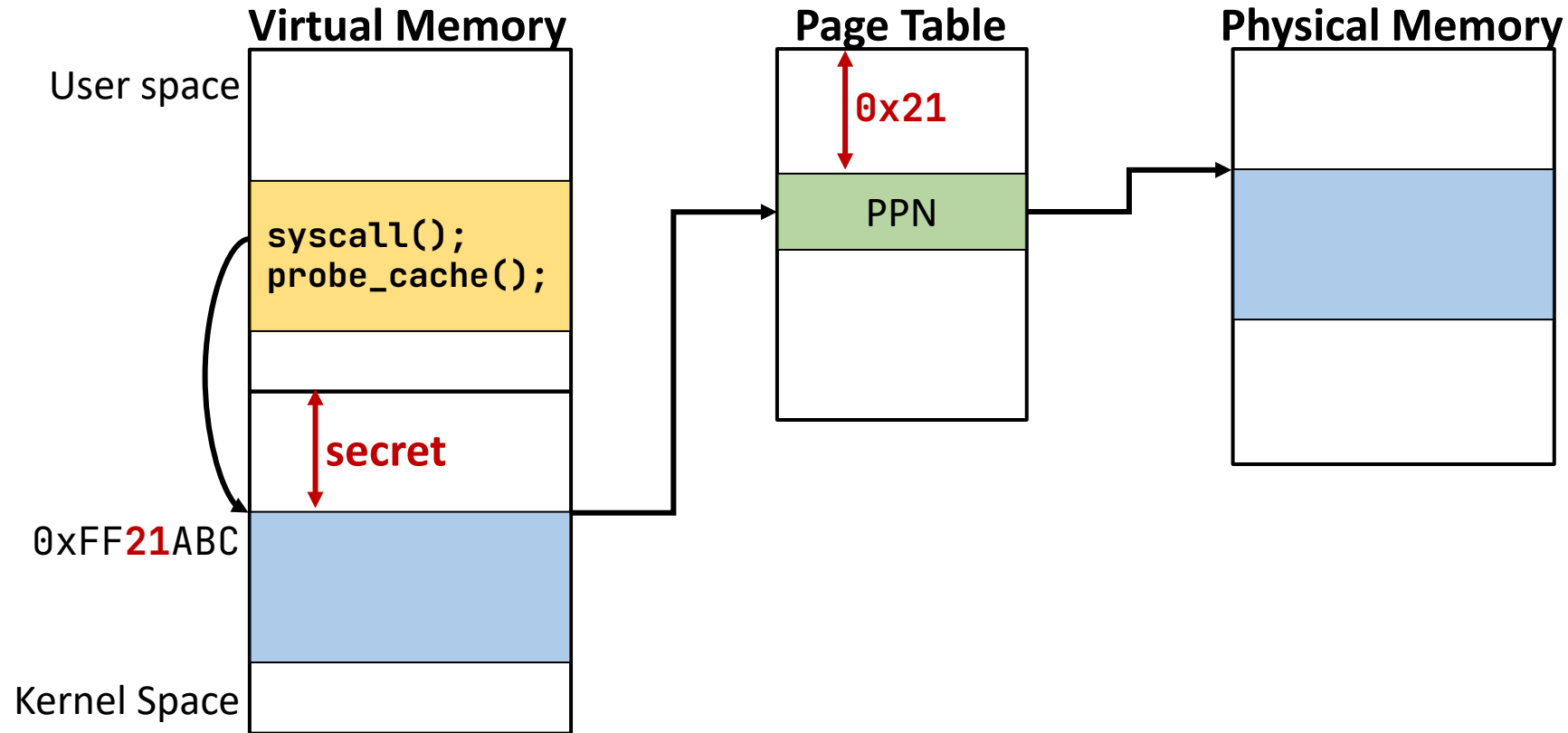
[1] Zhao, et al., “Binoculars: Contention-Based Side-Channel attacks exploiting the page walker,” in USENIX Security 2022.

Attack 1: ALSR Dependent Address Translation^[1]



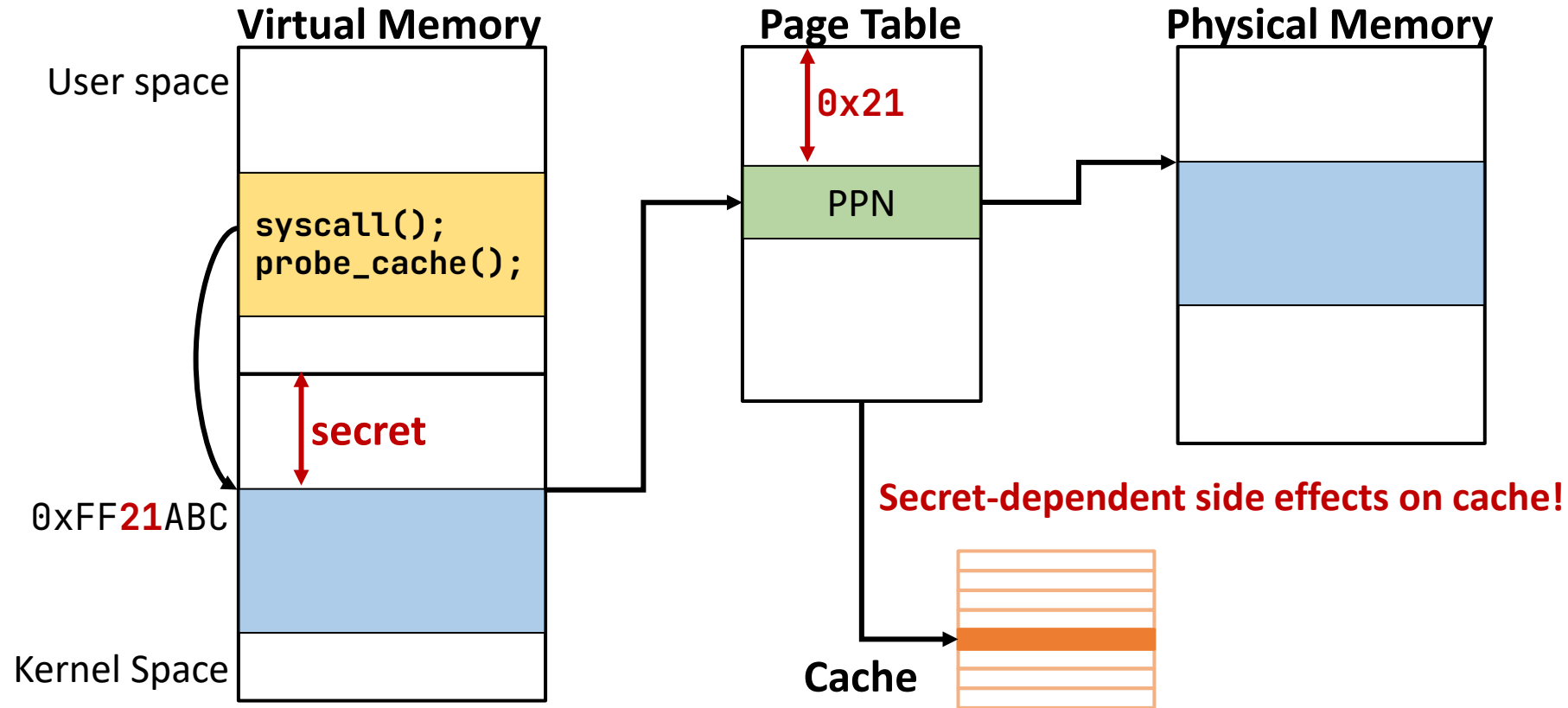
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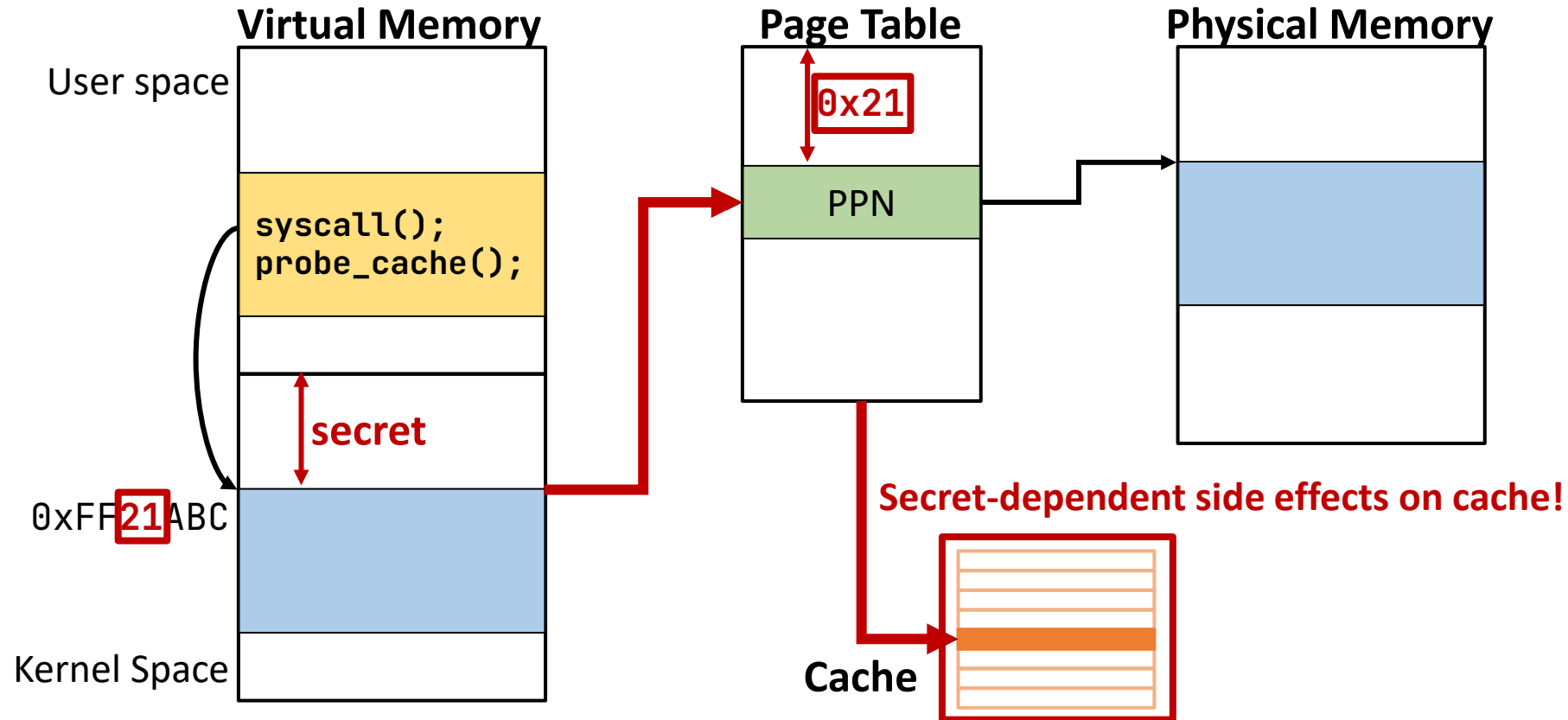
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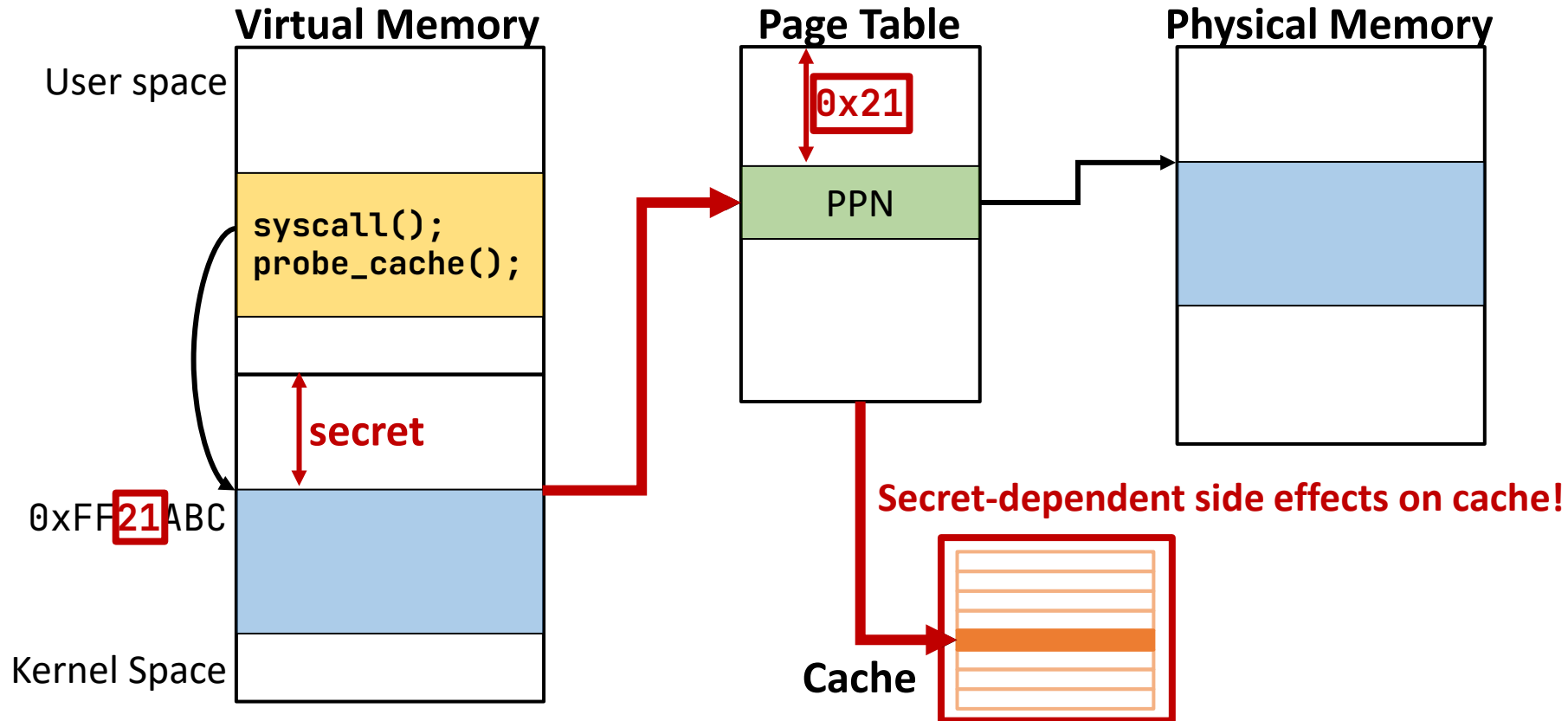
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Attack 2: Distinguishing Valid/Invalid Addresses^[1]



```
for guess_addr in
    [0xFF00ABC, 0xFF01ABC, ... 0xFF21ABC, ...]
{
    transient_probe(guess_addr);
    latency = transient_probe(guess_addr);
}
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(First Probe)

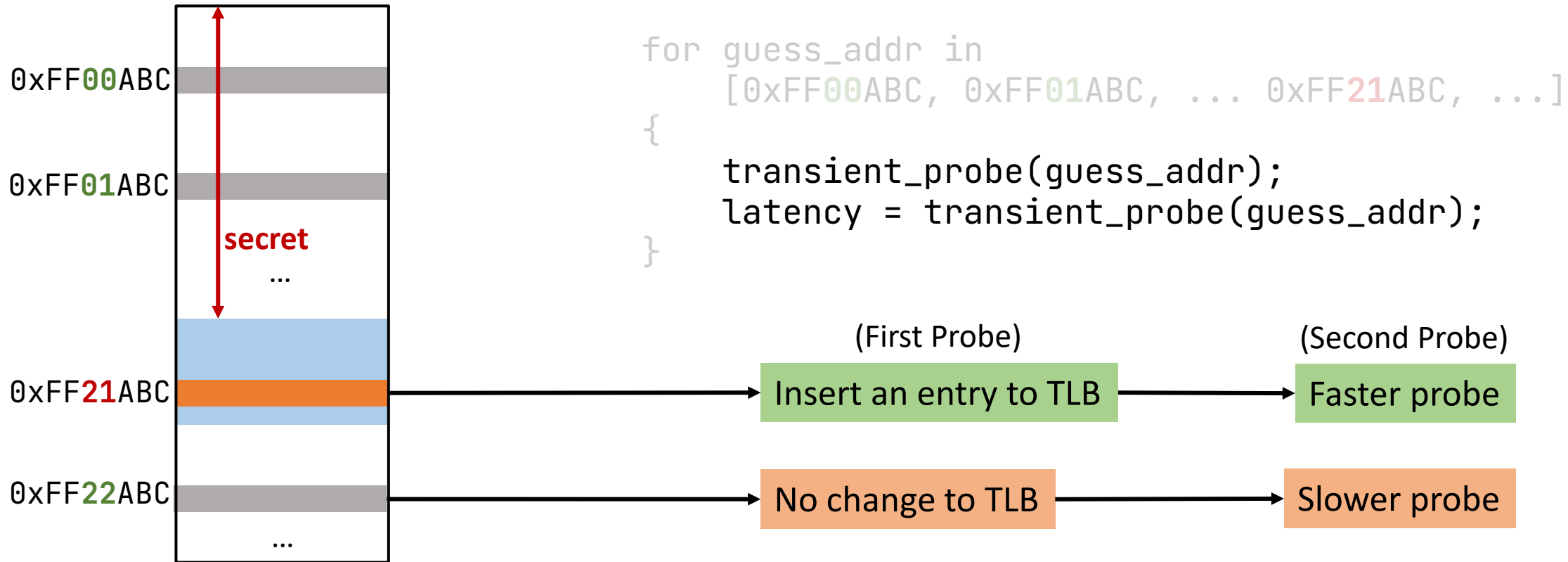
Insert an entry to TLB

(Second Probe)

Faster probe

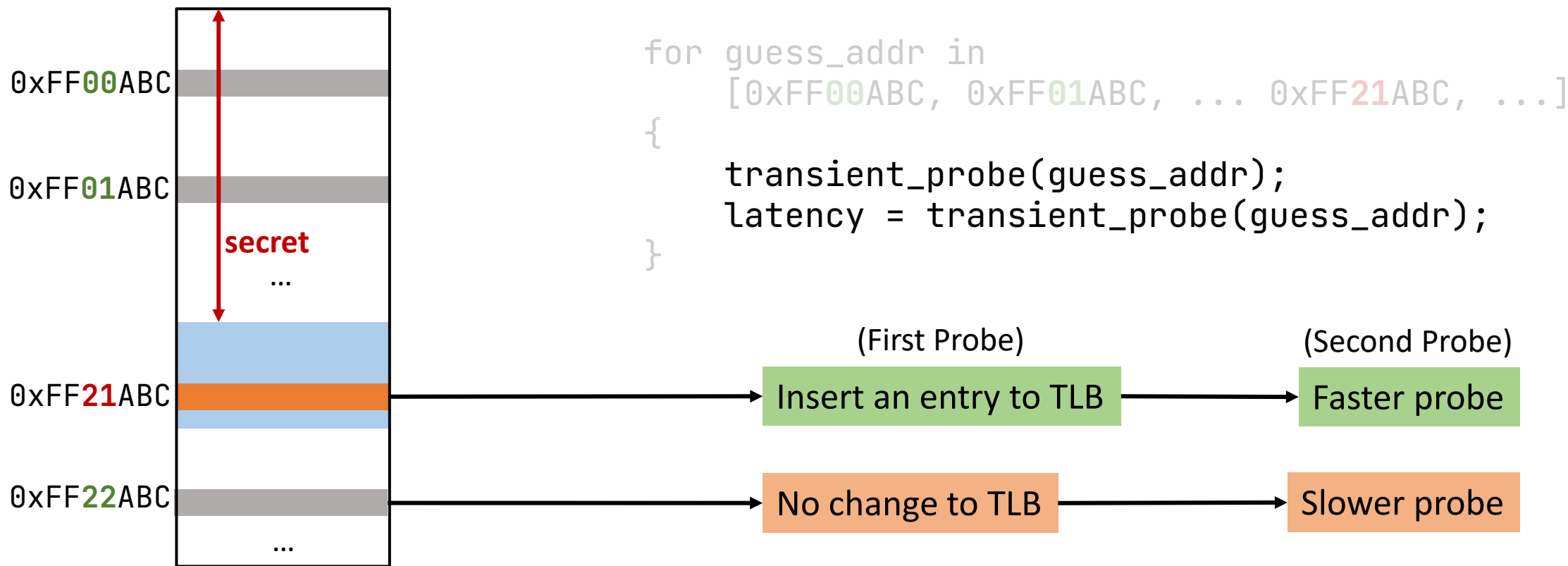
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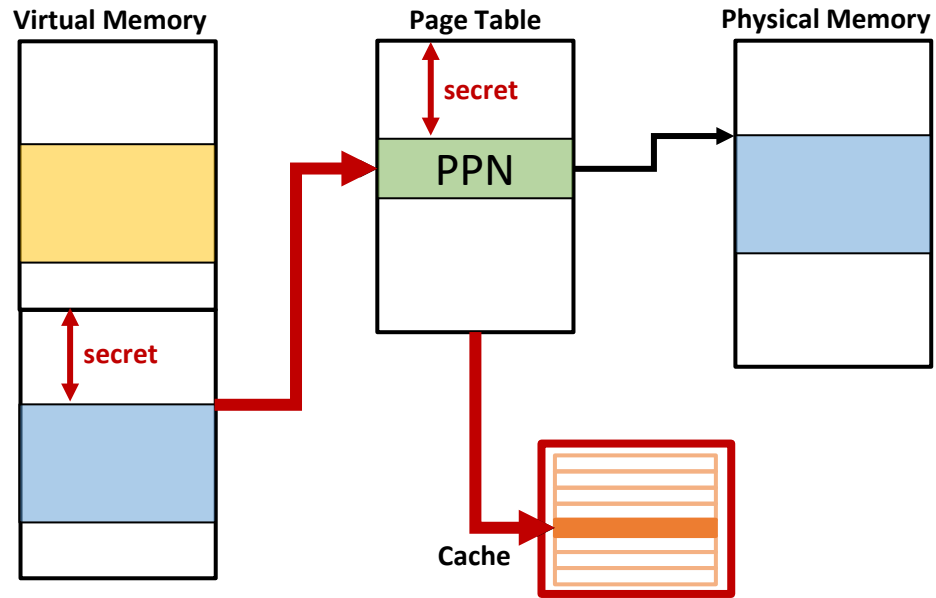
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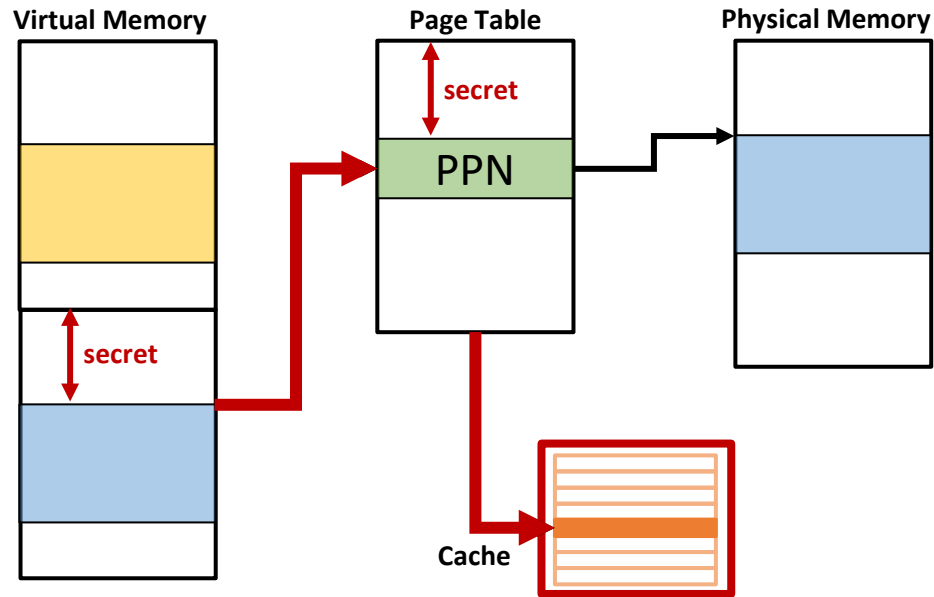


Accessing valid and invalid addresses has distinguishable microarchitectural side effects.

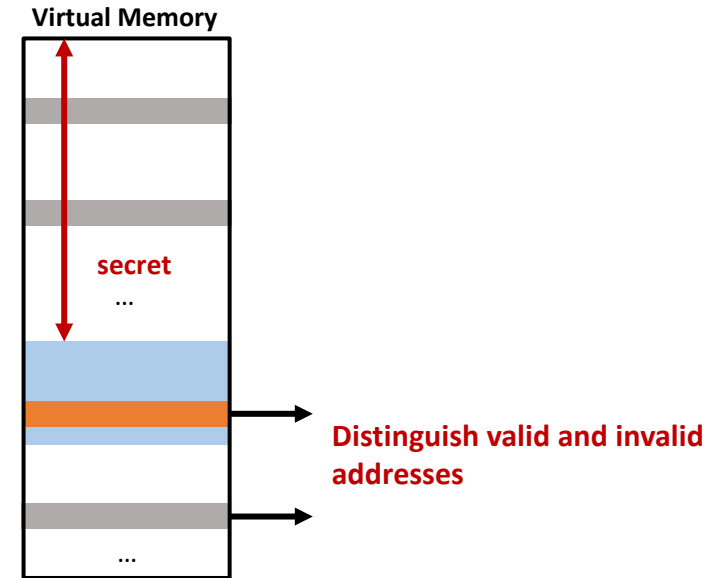
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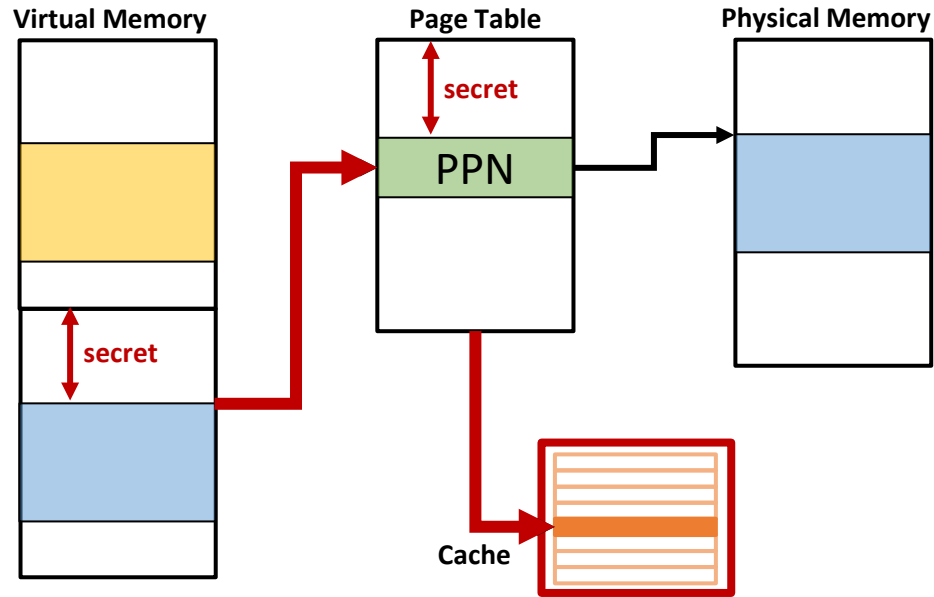
ASLR secret is used to index into page tables and microarchitecture structures.



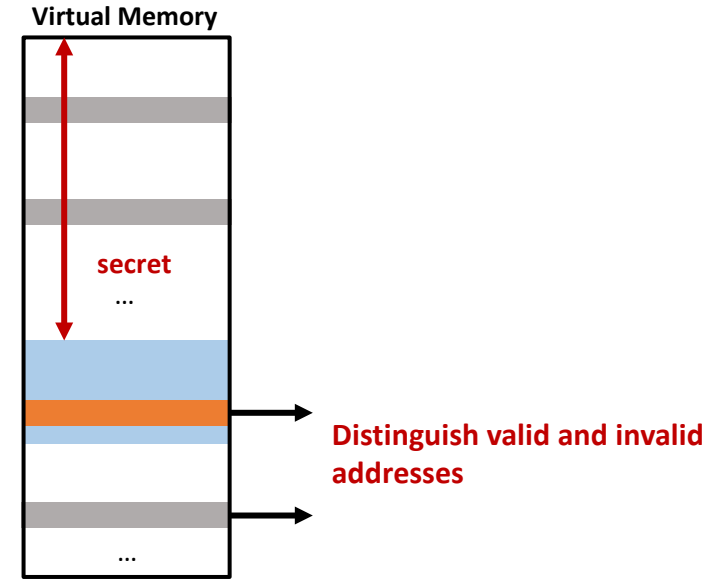
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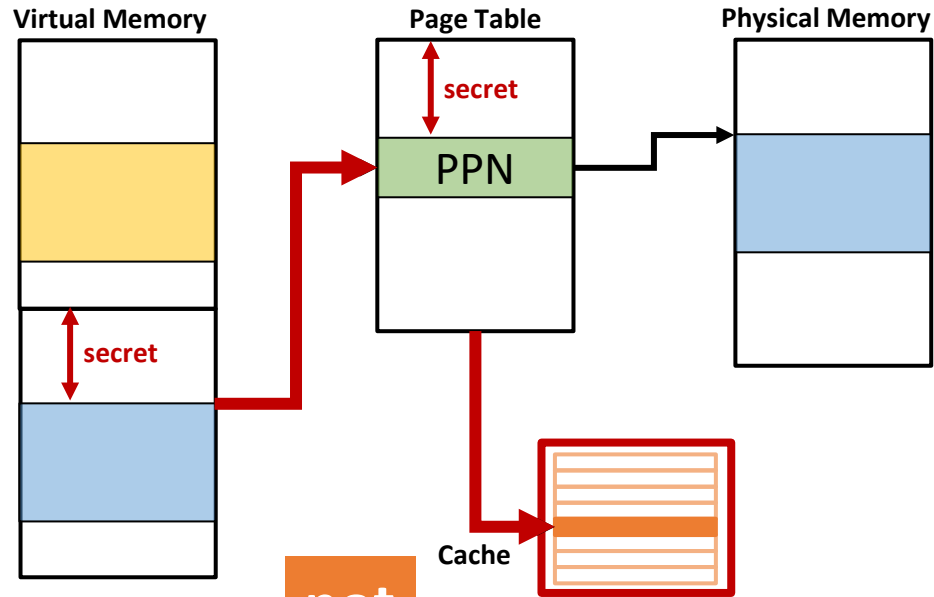
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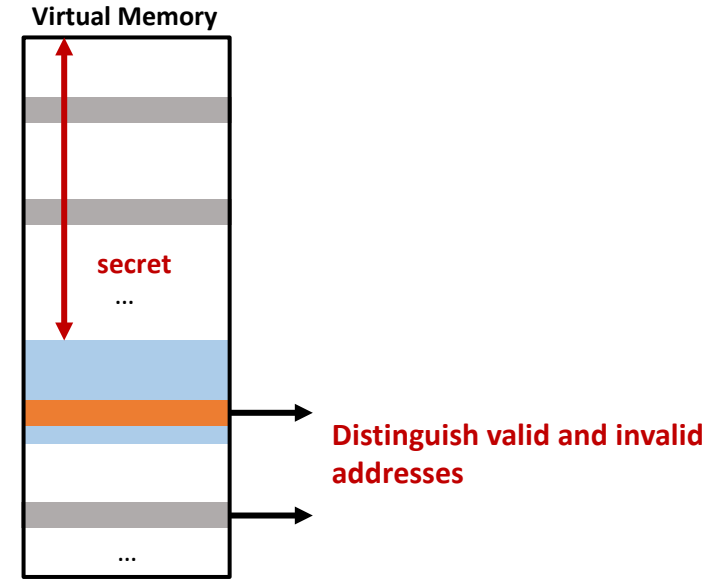


Our goal is to make ASLR bits microarchitectural independent.



not

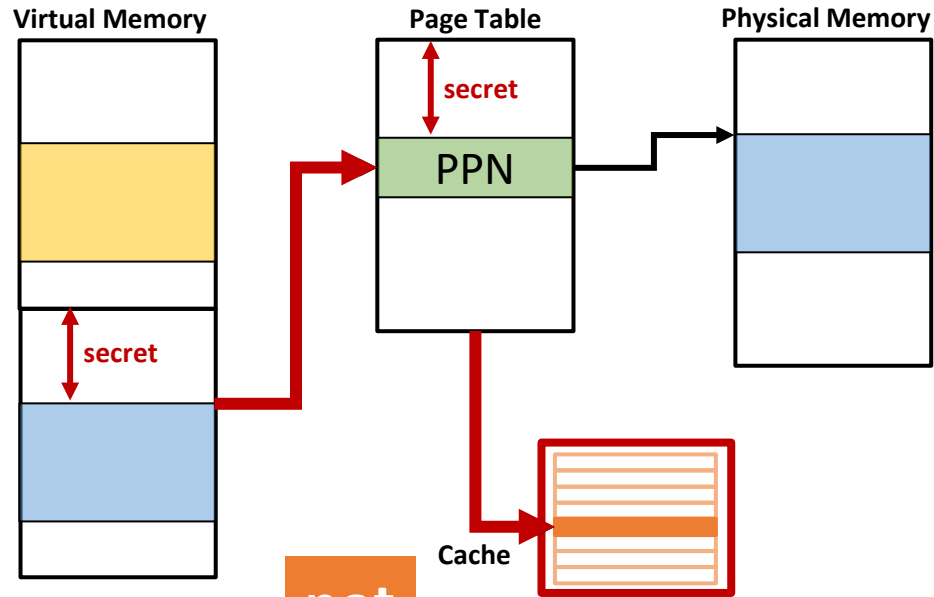
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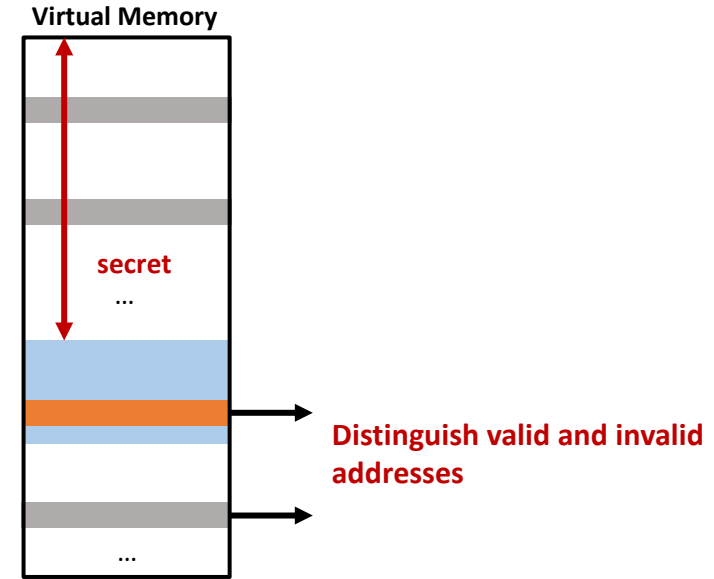


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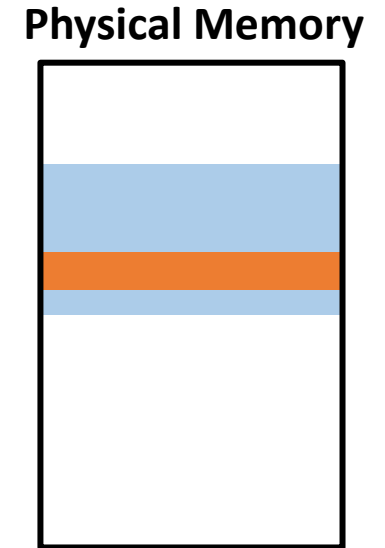
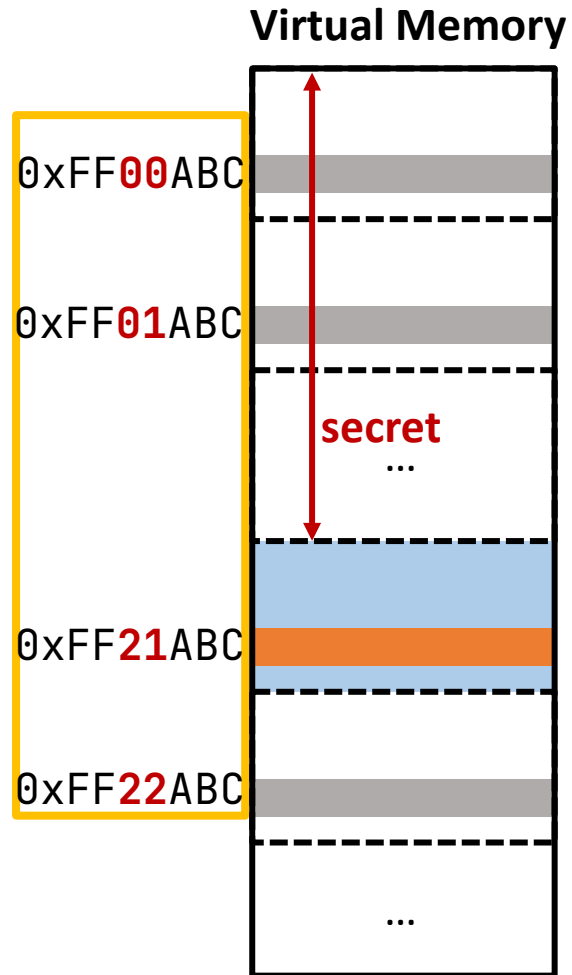
Accessing valid and invalid addresses has ~~distinguishable~~ microarchitectural side effects.

indistinguishable

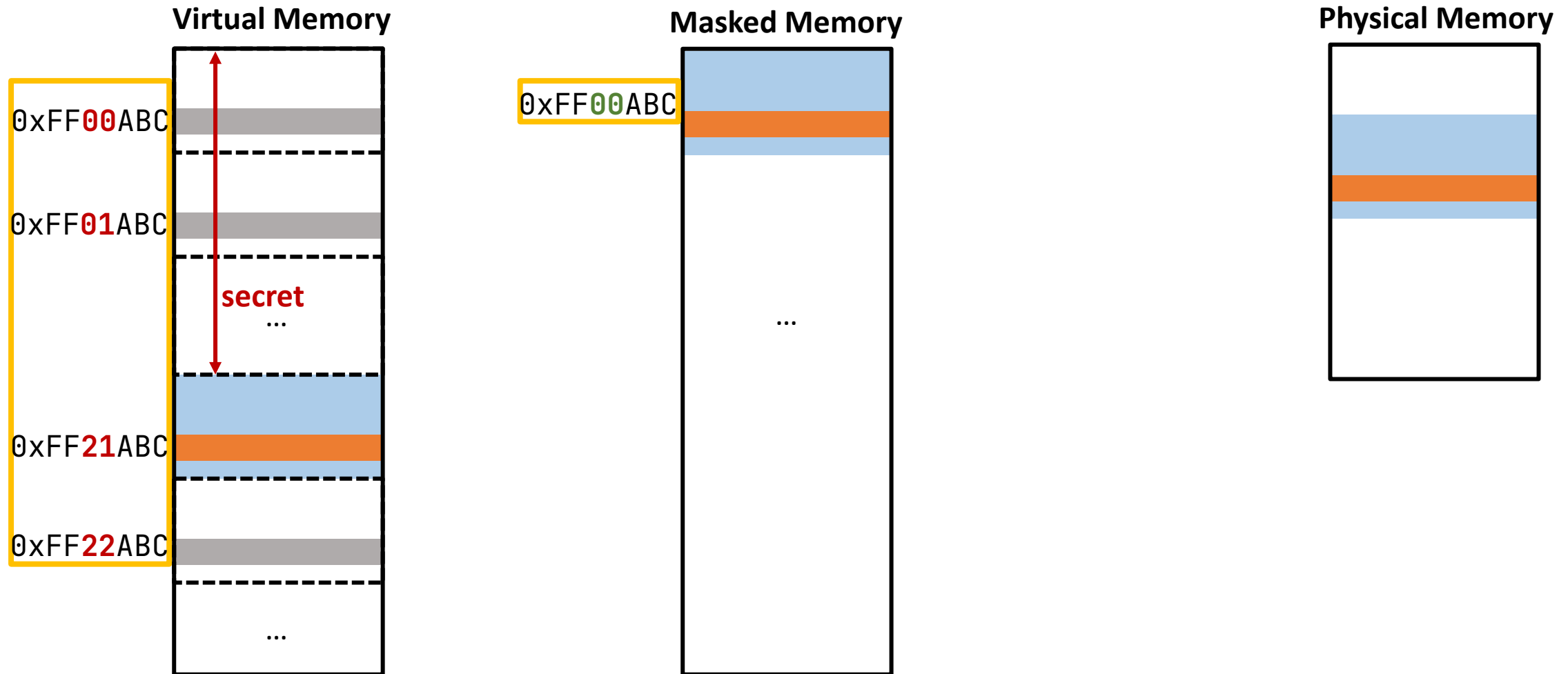


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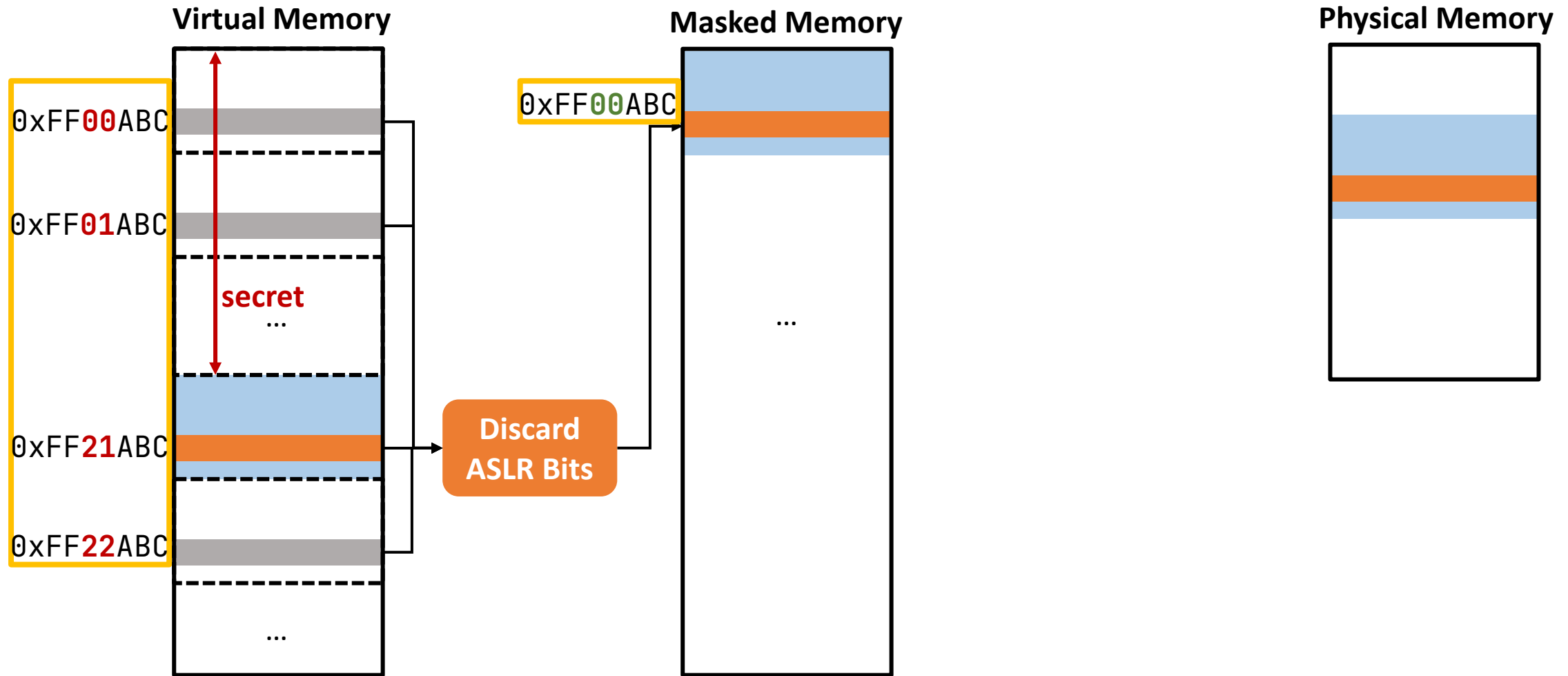
Oreo: New Memory Interface



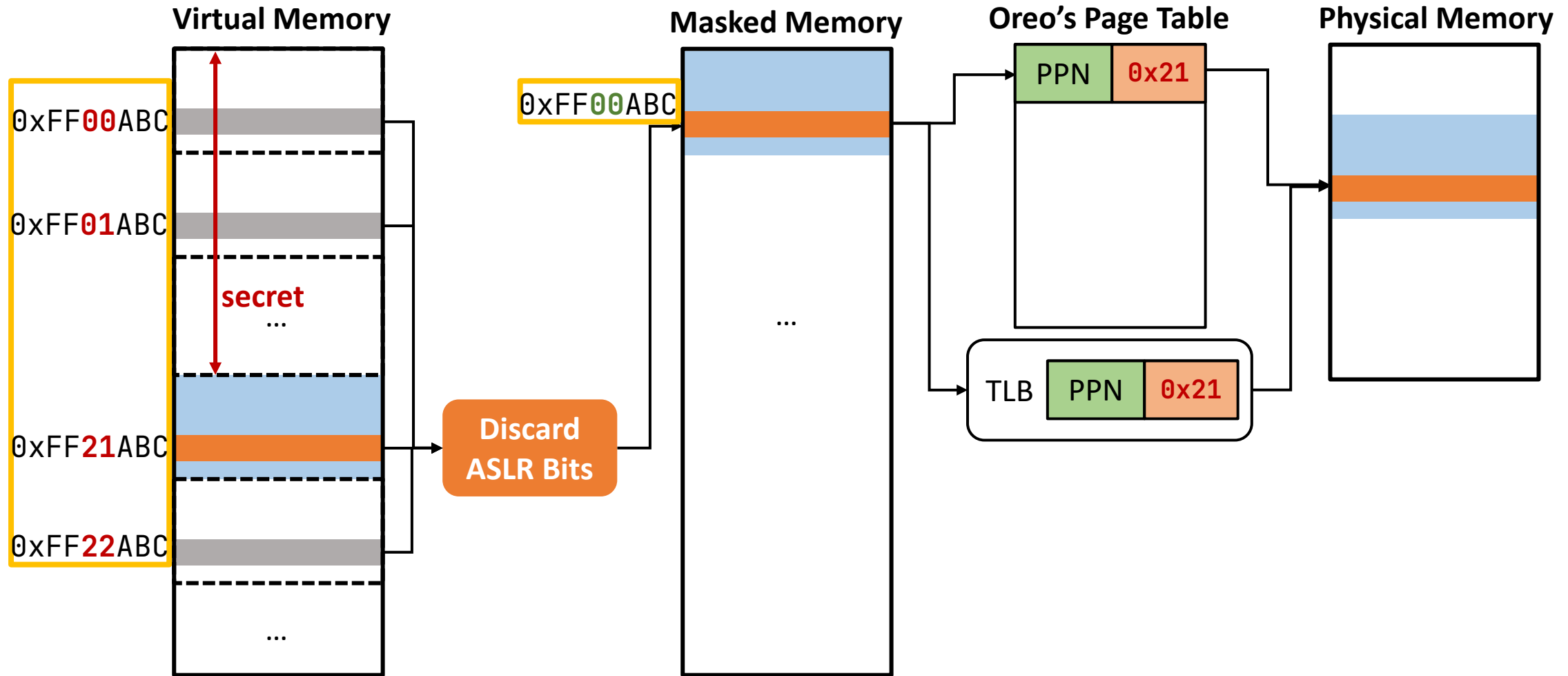
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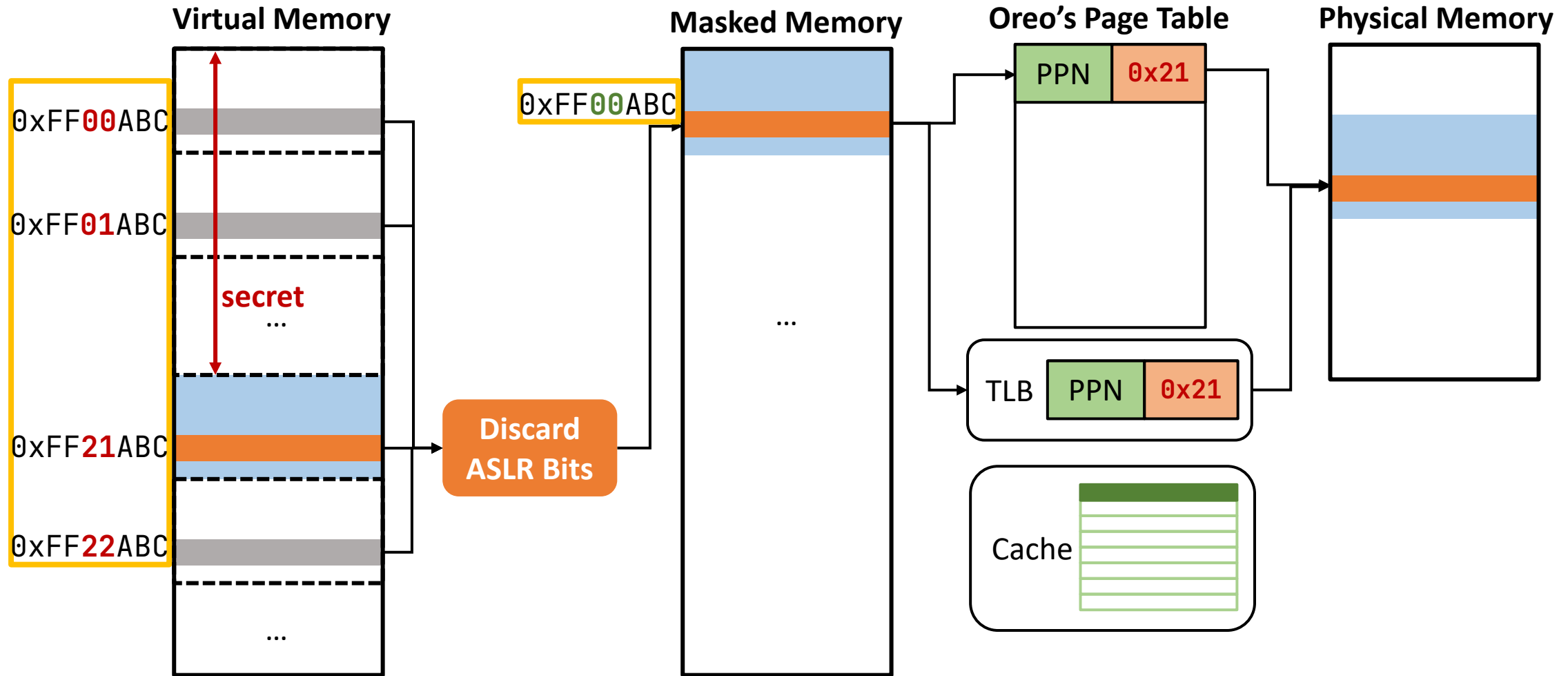
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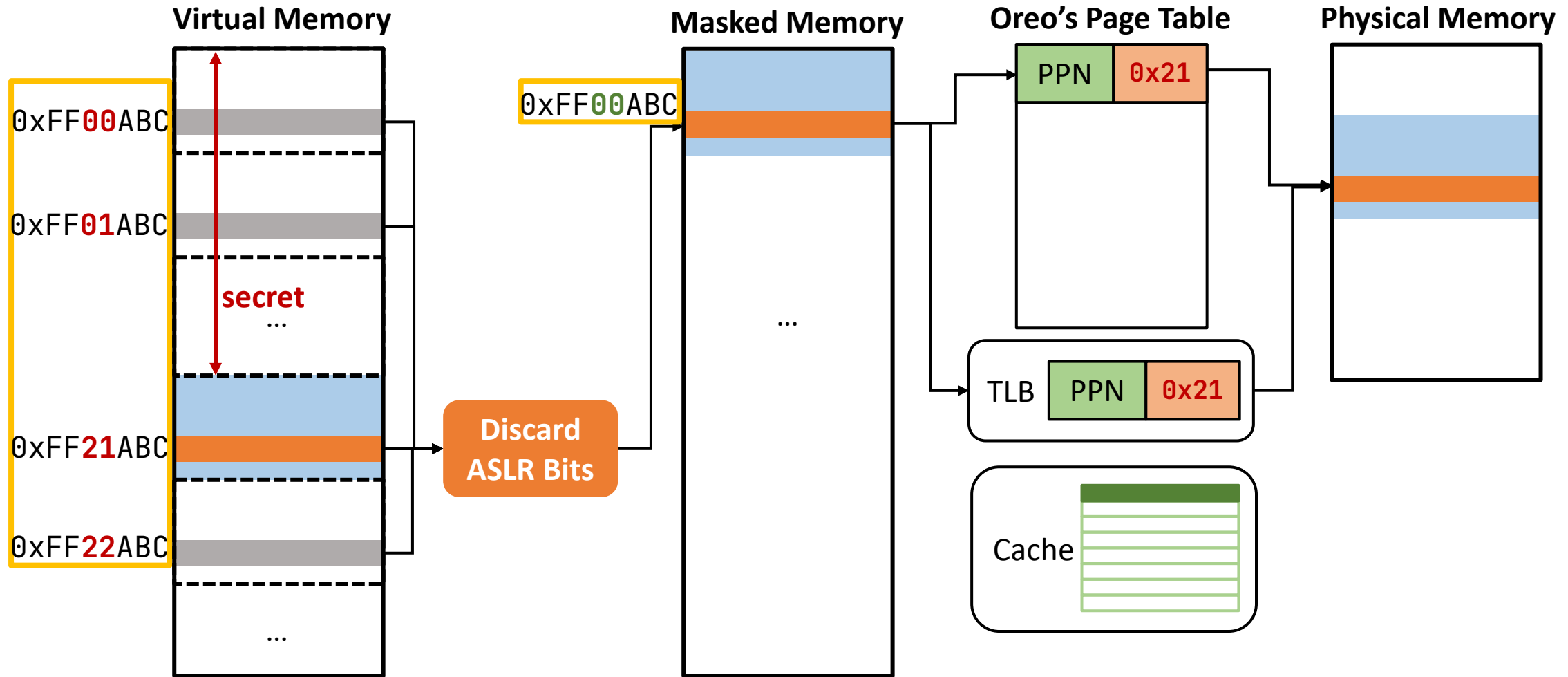
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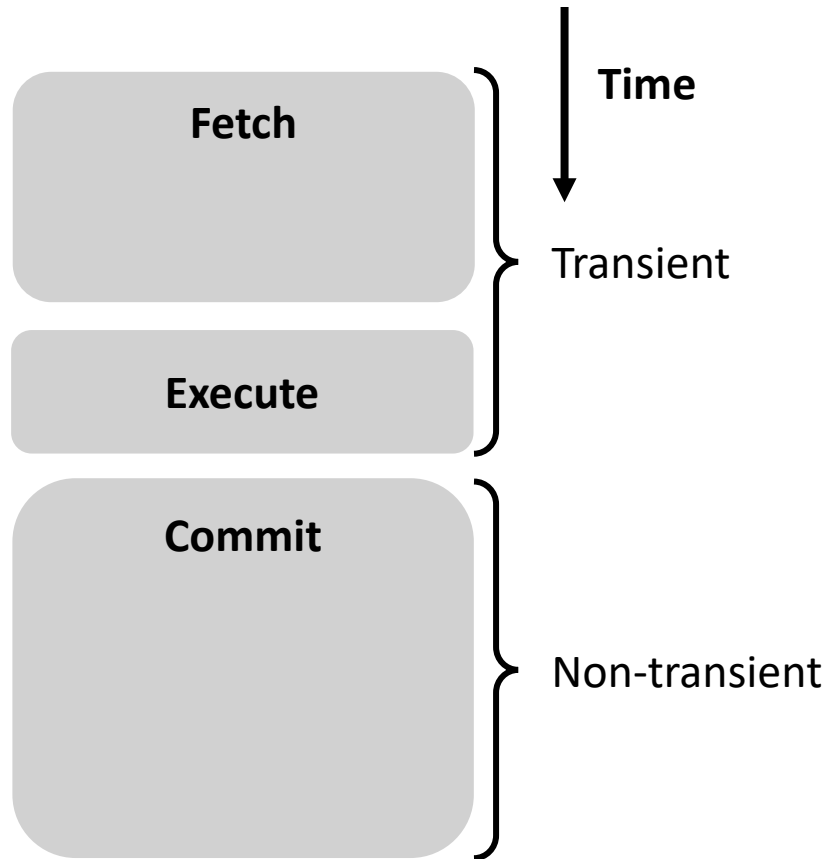


Oreo: New Memory Interface

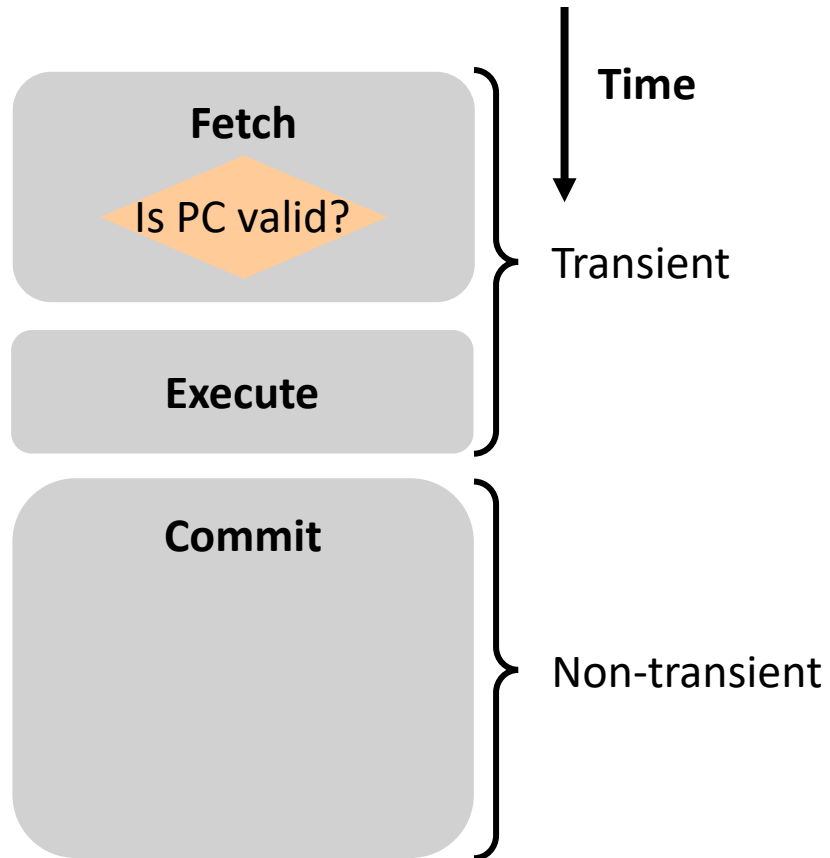


Oreo uses secret-independent masked addresses in page table walk.

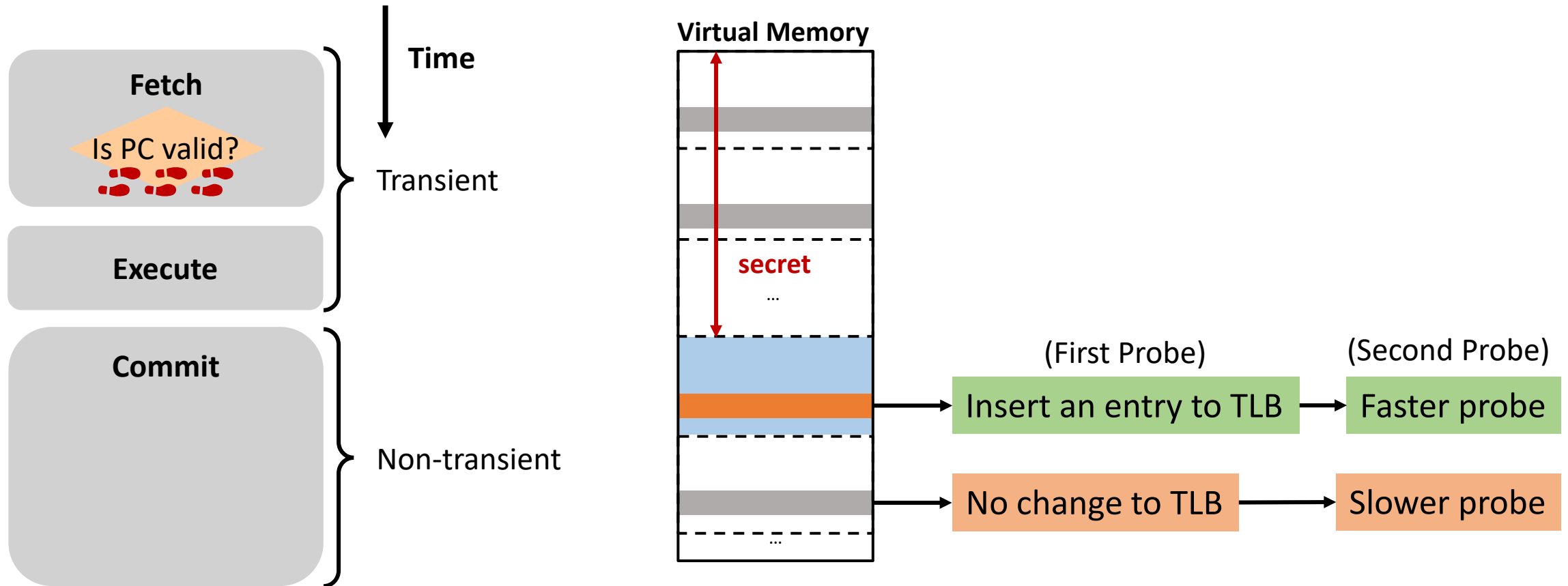
Oreo: ASLR Security Check



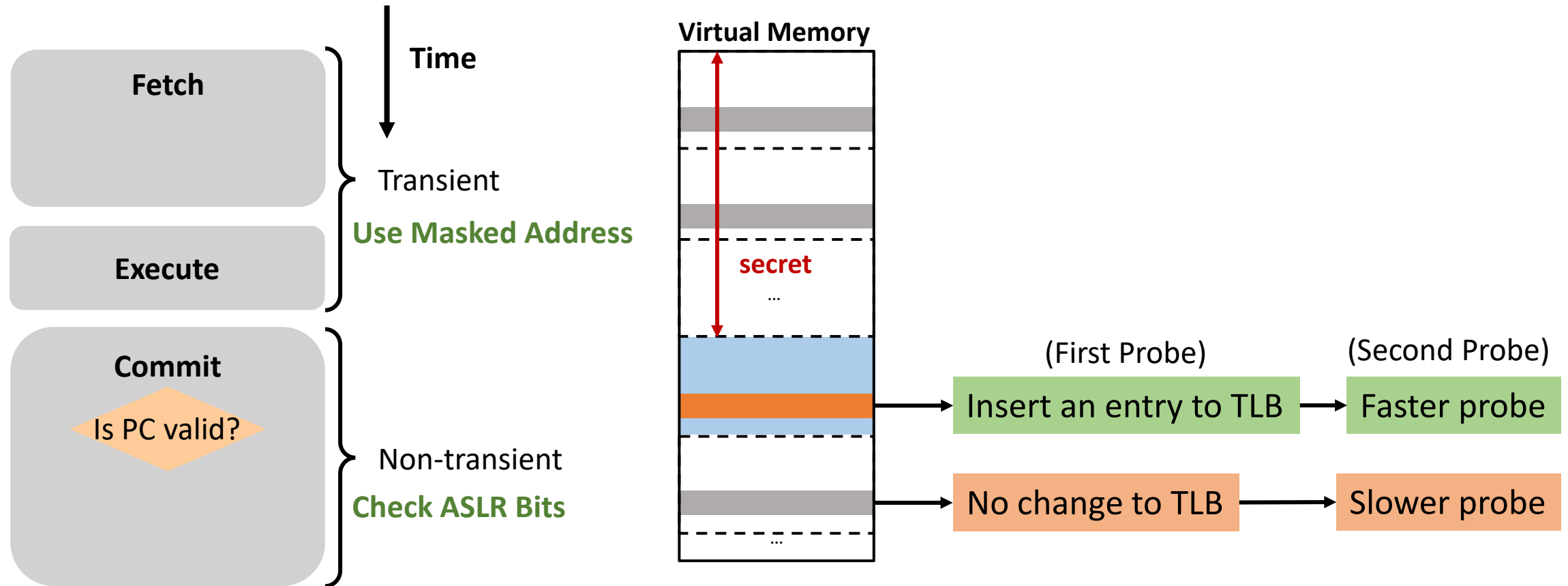
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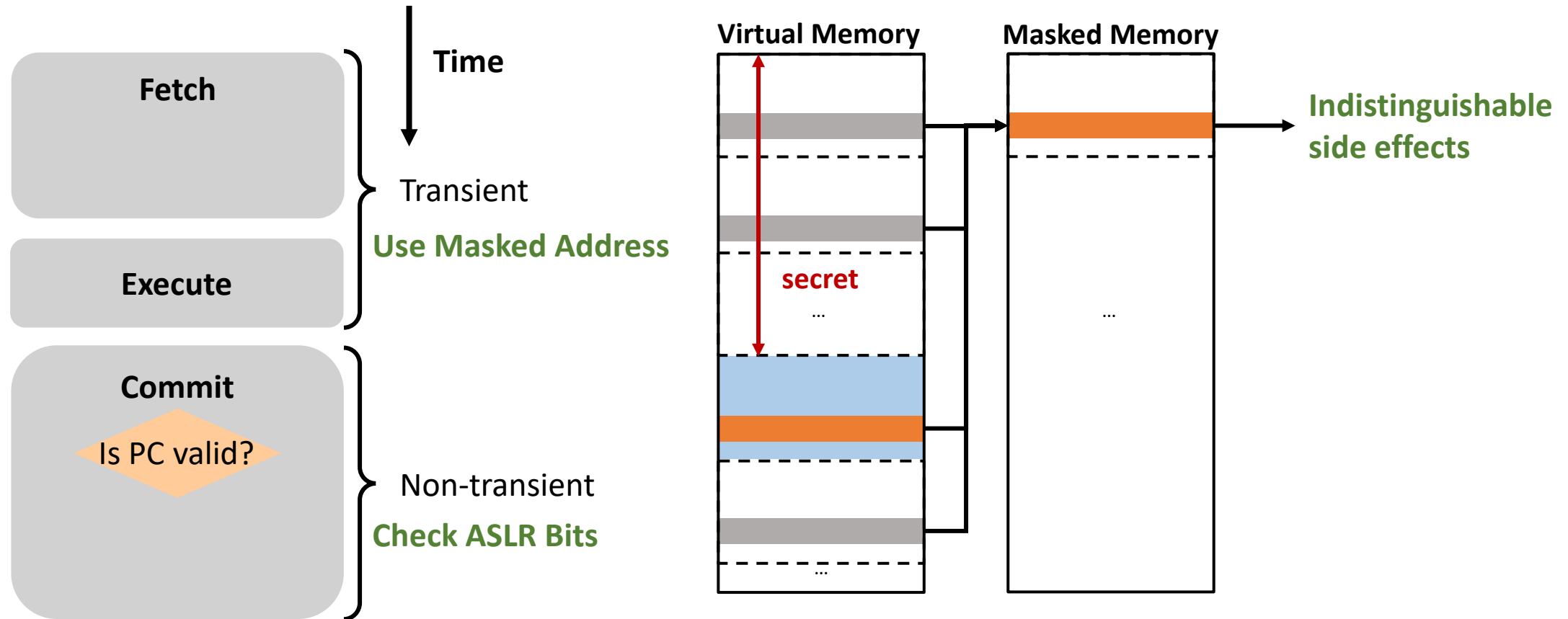
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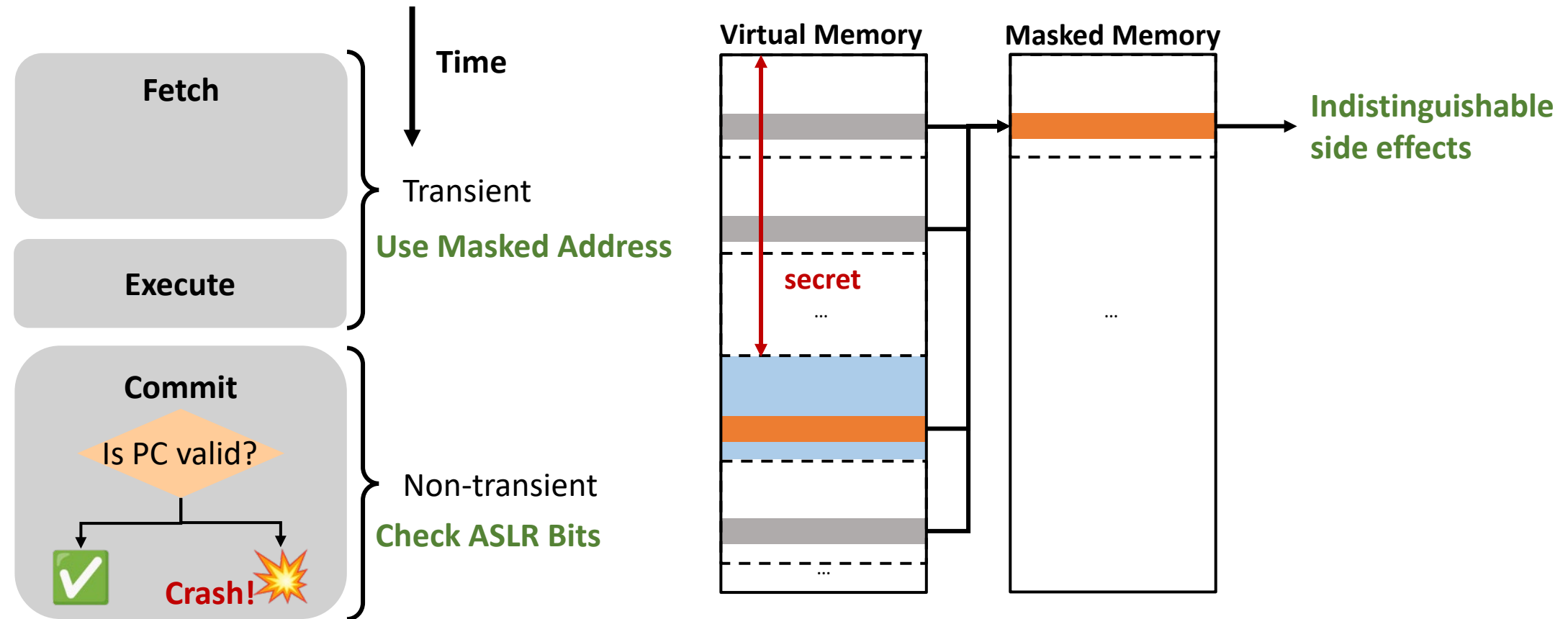
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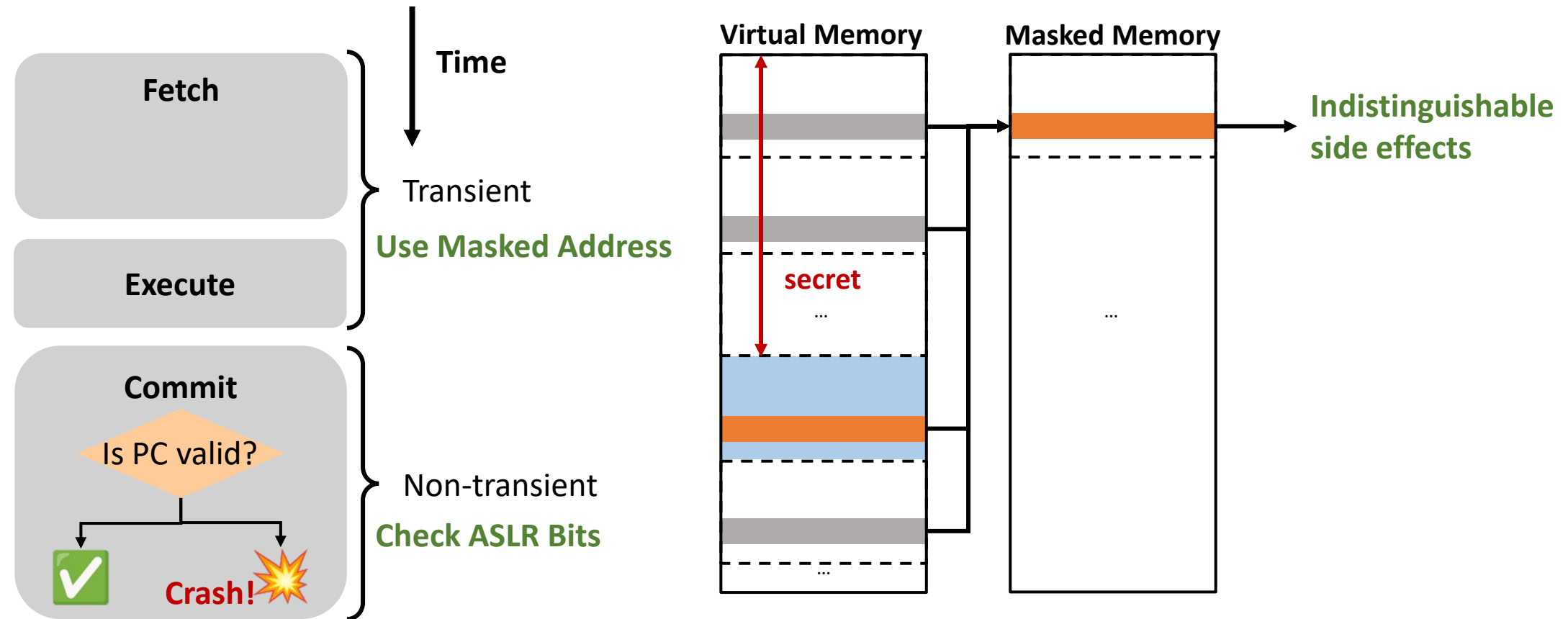


Oreo: ASLR Security Check



Attacker can only distinguish valid and invalid addresses through crashes.

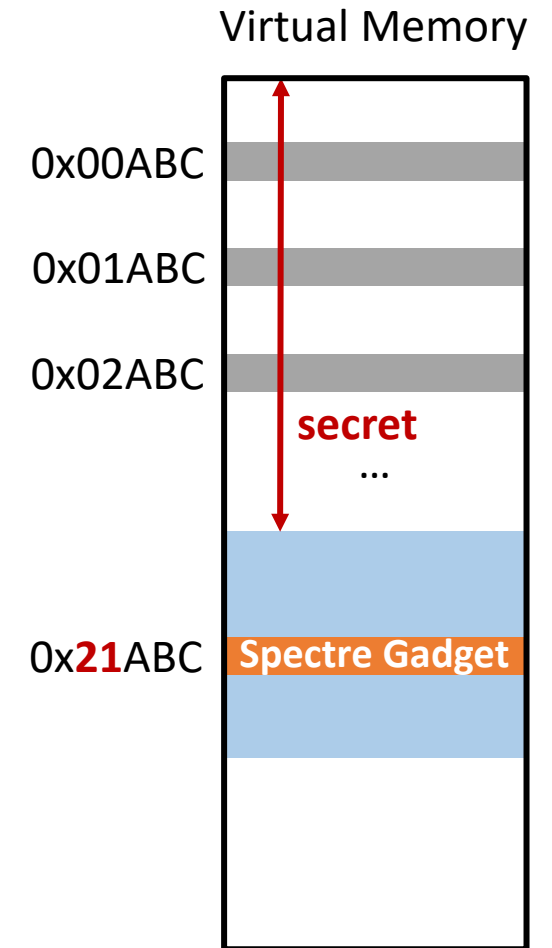
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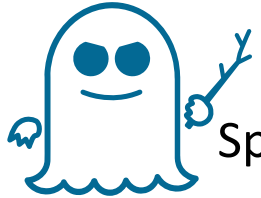
Delaying security check does not affect pipeline performance.

Security Dilemma



Security Dilemma

Without Oreo:



Spectre Attack needs:

Leak the secret **0x21**



```
fp = 0x21ABC;  
if (false) {  
    jmp fp;  
}
```

Virtual Memory

0x00ABC

0x01ABC

0x02ABC

secret

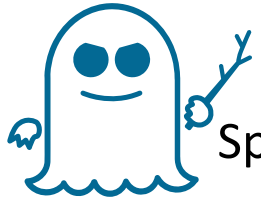
...

0x**21**ABC

Spectre Gadget

Security Dilemma

Without Oreo:



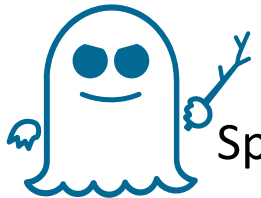
Spectre Attack needs:

Leak the secret **0x21**



```
fp = 0x21ABC;  
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With Oreo:



Spectre Attack needs:

~~Leak the secret **0x21**~~



```
fp = 0x00ABC;  
if (false) {  
    jmp fp;  
}
```

Virtual Memory

0x00ABC

0x01ABC

0x02ABC

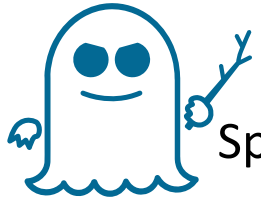
secret
...

0x**21**ABC

Spectre Gadget

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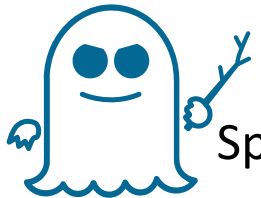
Spectre Attack needs:

Leak the secret **0x21**



```
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}
```

With Oreo:



Spectre Attack needs:

~~Leak the secret **0x21**~~



```
fp = 0x00ABC;  
if (false) {  
    jmp fp;  
}
```

Virtual Memory

0x00ABC

0x01ABC

0x02ABC

secret
...

0x21ABC

Spectre Gadget



Making valid addresses indistinguishable however makes Spectre attacks easier.

Dilemma Analysis

Virtual Address

Dilemma Analysis



Baseline Bits	Oreo Bits	Security Outcome

Dilemma Analysis



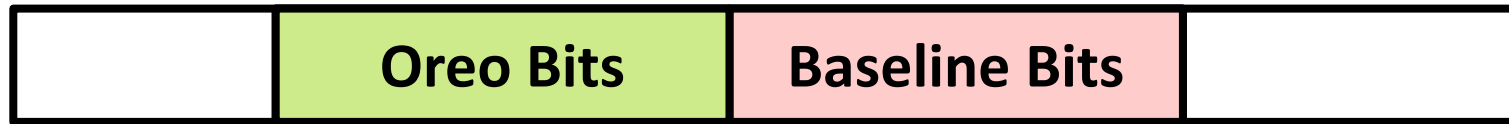
Baseline Bits	Oreo Bits	Security Outcome
All	None	✗ Vulnerable to ASLR bypasses




Dilemma Analysis



Baseline Bits	Oreo Bits	Security Outcome
All	None	✗ Vulnerable to ASLR bypasses
None	All	✗ Vulnerable to Spectre-like attacks

Dilemma Analysis

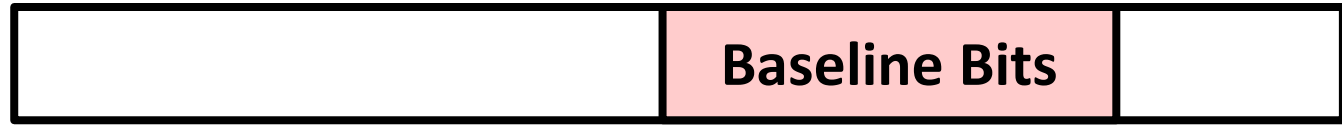


Baseline Bits	Oreo Bits	Security Outcome
All	None	 Vulnerable to ASLR bypasses
None	All	 Vulnerable to Spectre-like attacks
Some	Some	 Safe on both sides!

Oreo: Solution to Security Dilemma

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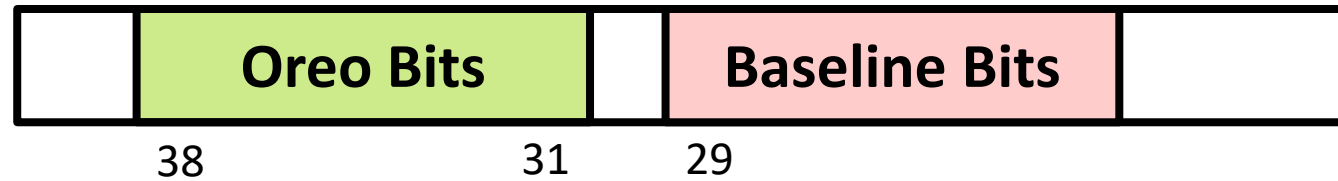
Kernel ASLR



29

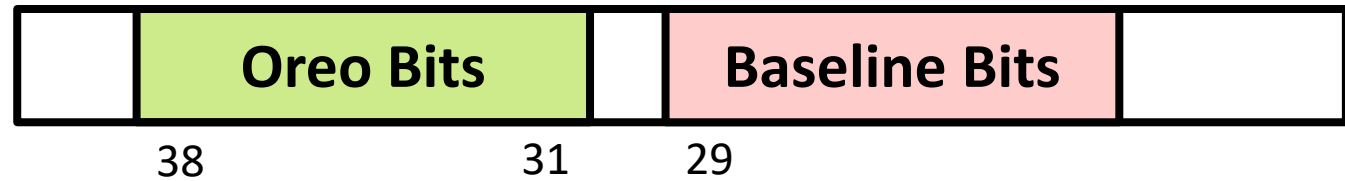
Oreo: Solution to Security Dilemma

Kernel ASLR

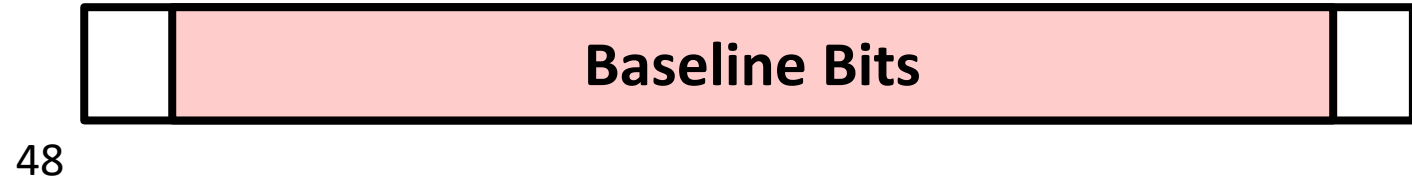


Oreo: Solution to Security Dilemma

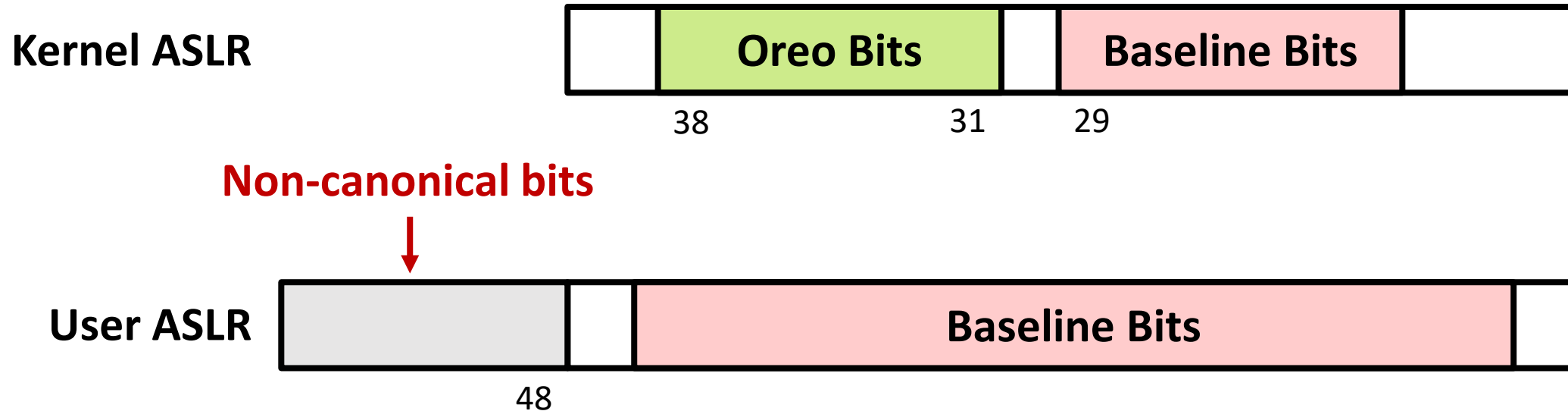
Kernel ASLR



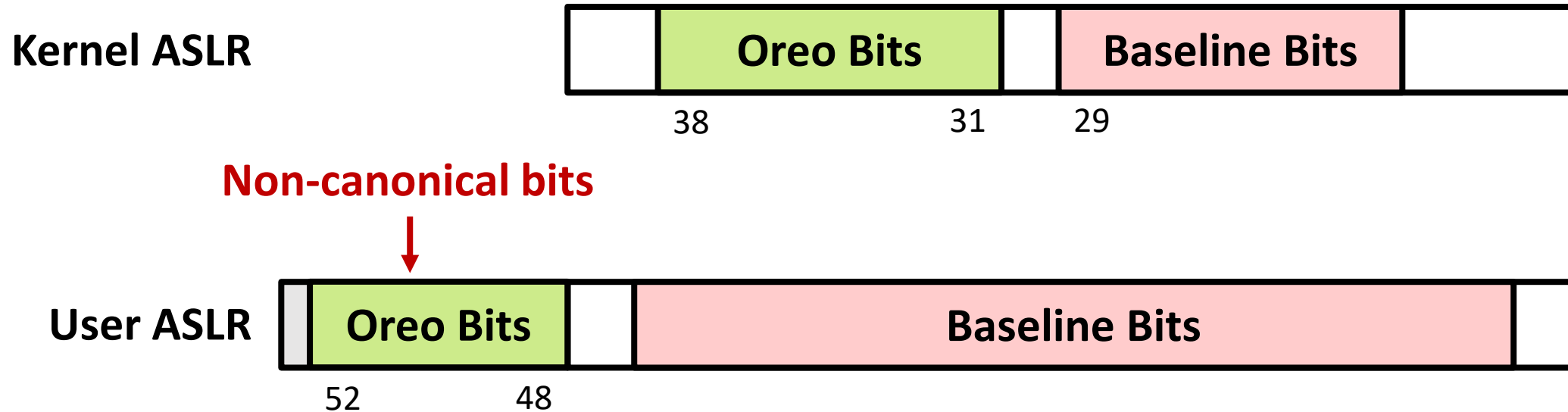
User ASLR



Oreo: Solution to Security Dilemma



Oreo: Solution to Security Dilemma



More in the Paper...

- Prototype
 - SW: Linux
 - HW: gem5 simulator

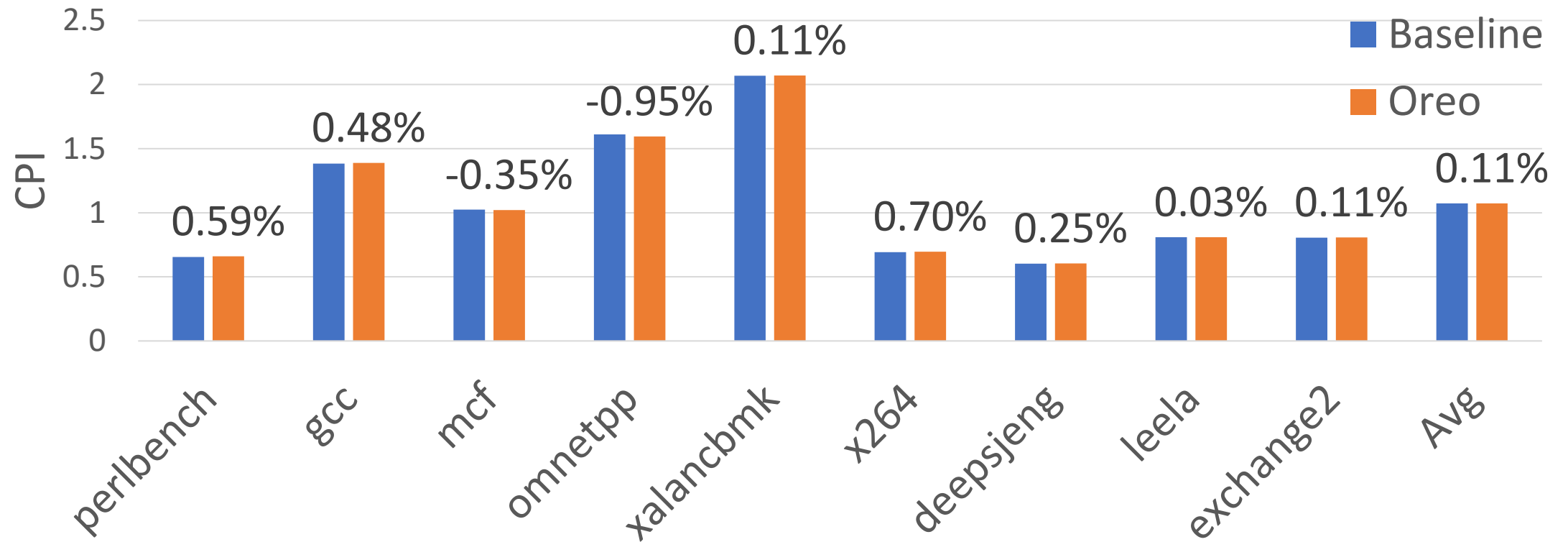
More in the Paper...

- Prototype
 - SW: Linux
 - HW: gem5 simulator
- Evaluation
 - Performance evaluation on SPEC and LEBench
 - Security evaluation on multiple leakage paths

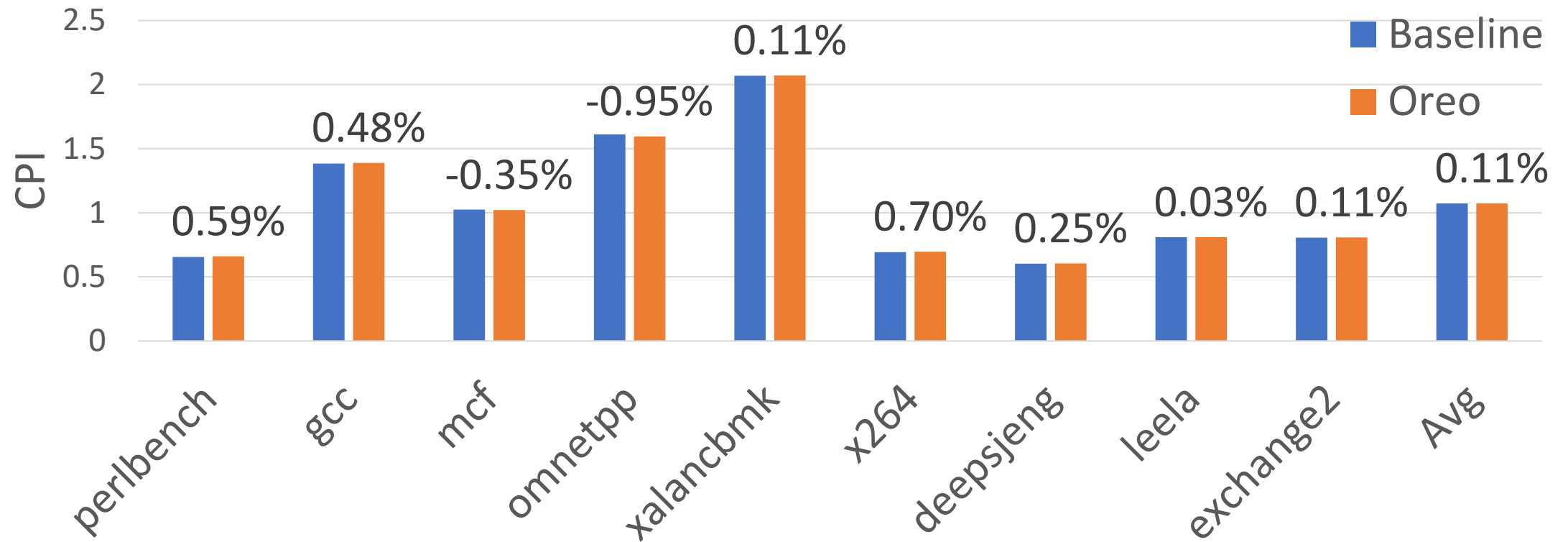
More in the Paper...

- Prototype
 - SW: Linux
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 - Performance evaluation on SPEC and LEBench
 - Security evaluation on multiple leakage paths
- Formal reasoning of Oreo's security property (in extended version)

Performance Overhead: SPEC

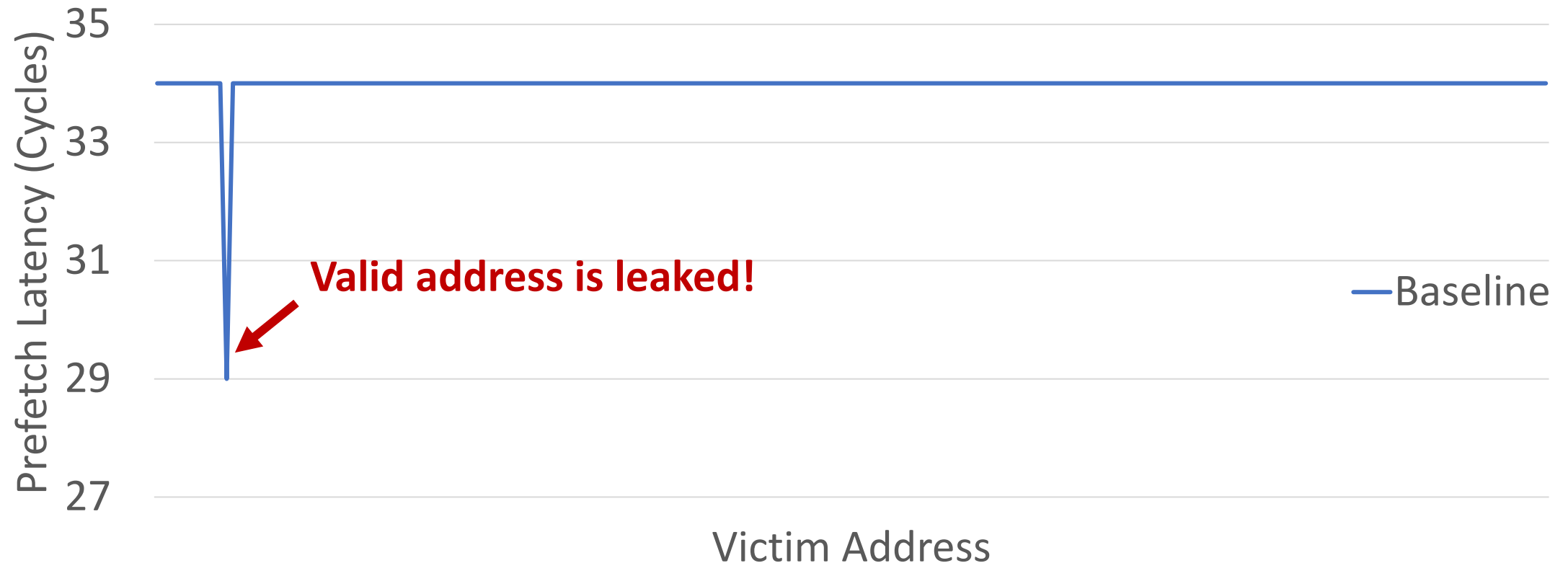


Performance Overhead: SPEC

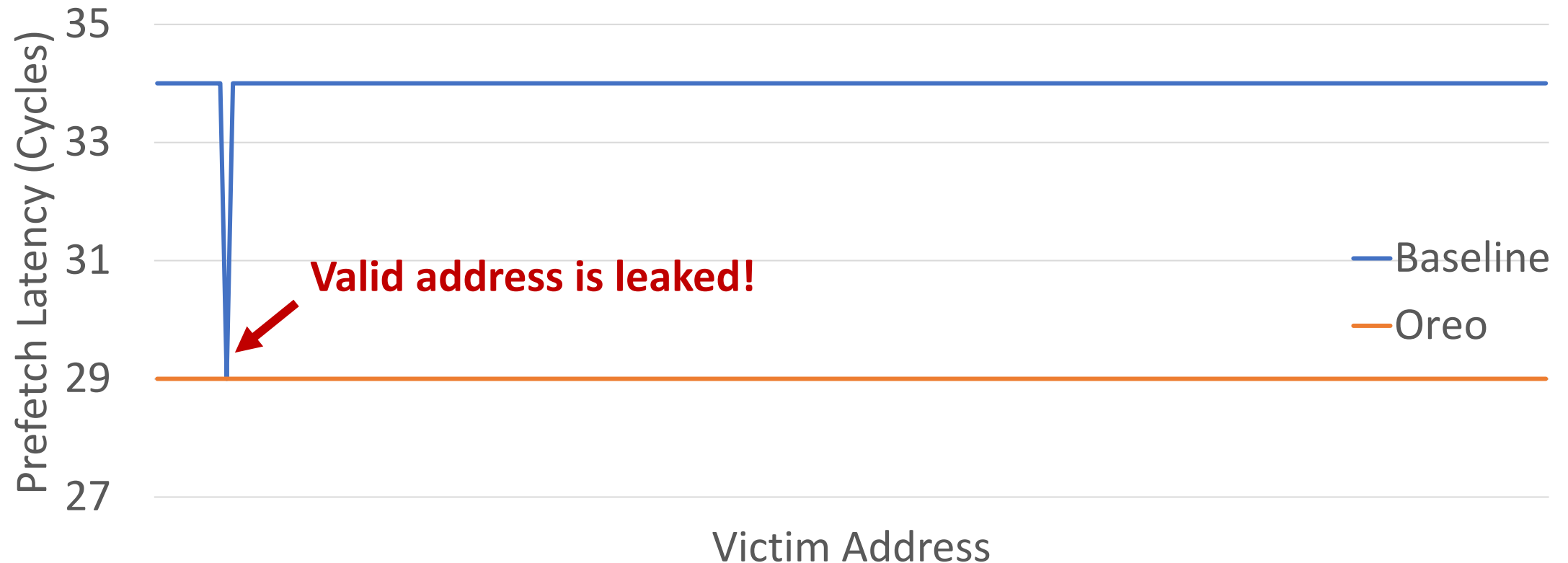


Oreo introduces negligible performance overhead.

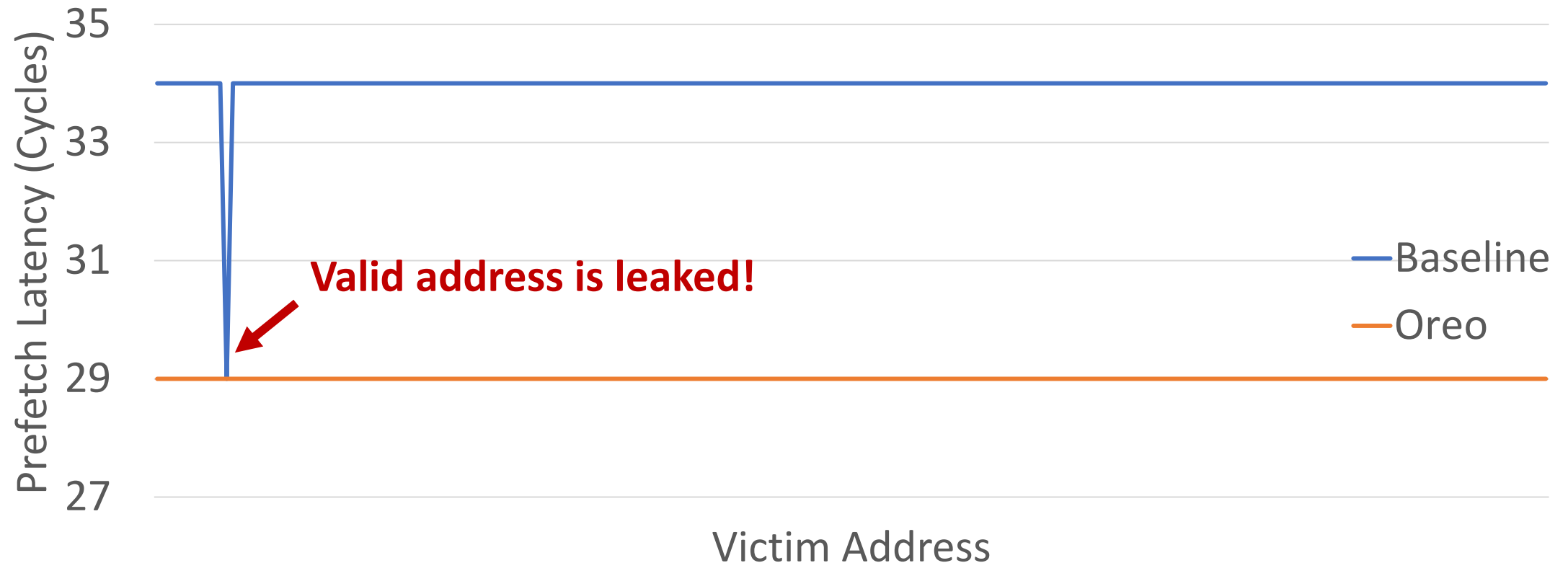
Security Evaluation: Prefetch Attack



Security Evaluation: Prefetch Attack



Security Evaluation: Prefetch Attack



The prefetch attack no longer works on Oreo.

