Readme

This build supports an environment defined as AT91SAM3XAZ

It has a similar structure to the Arduino DUE with the following changes (additions and then subtractions)

1. Supports an expanded TWI interface
   1. PCA9698 – port expander address?
   2. AT24Cxx EEPROM with IEEE MAC address
2. A software environement that can go to low power
3. Variable speed demands – EFC Data flash,
4. RTC with 32KHz xtal
5. Run to completion loop
6. Serial based control console with extensible runtime debug system
7. Asdas

The nomenclature used attempts the following – and keeps the files in a flat directory structure.

For drivers, follows the TinyOS.net three layer Hardware Abstraction Architecture “H!L” naming conventions standard

HPL – Hardware Presentation Layer – typically just the register level interface – no state information

HAL – Hardware Adaption Layer - most code is here.

HIL – Hardware Interface Layer - an interface layer that is where the upper layers access the hardware . required, other layers can be folded into the HIL if necessary.

<file naming conventions to be documented>