

Thus, we find

$$v_1 = \frac{\Delta_1}{\Delta} = \frac{48}{10} = 4.8 \text{ V}, \quad v_2 = \frac{\Delta_2}{\Delta} = \frac{24}{10} = 2.4 \text{ V}$$

$$v_3 = \frac{\Delta_3}{\Delta} = \frac{-24}{10} = -2.4 \text{ V}$$

as we obtained with Method 1.

■ **METHOD 3** We now use *MATLAB* to solve the matrix. Equation (3.2.6) can be written as

$$\mathbf{A}\mathbf{V} = \mathbf{B} \quad \Rightarrow \quad \mathbf{V} = \mathbf{A}^{-1}\mathbf{B}$$

where \mathbf{A} is the 3 by 3 square matrix, \mathbf{B} is the column vector, and \mathbf{V} is a column vector comprised of v_1 , v_2 , and v_3 that we want to determine. We use *MATLAB* to determine \mathbf{V} as follows:

```
>>A = [3  -2  -1;  -4  7  -1;  2  -3  1];
>>B = [12  0  0]';
>>V = inv(A) * B
      4.8000
V =    2.4000
     -2.4000
```

Thus, $v_1 = 4.8 \text{ V}$, $v_2 = 2.4 \text{ V}$, and $v_3 = -2.4 \text{ V}$, as obtained previously.

Practice Problem 3.2

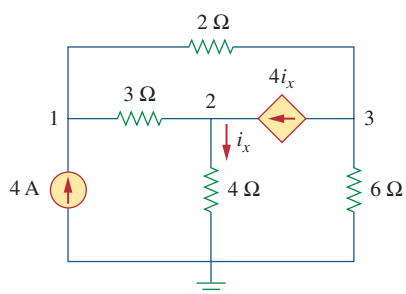


Figure 3.6
For Practice Prob. 3.2.

Find the voltages at the three nonreference nodes in the circuit of Fig. 3.6.

Answer: $v_1 = 32 \text{ V}$, $v_2 = -25.6 \text{ V}$, $v_3 = 62.4 \text{ V}$.

3.3 Nodal Analysis with Voltage Sources

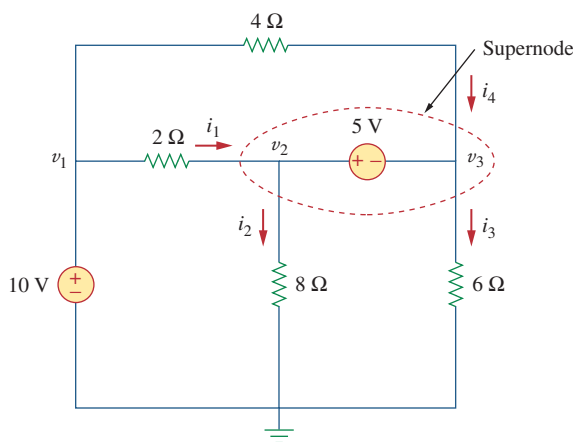
We now consider how voltage sources affect nodal analysis. We use the circuit in Fig. 3.7 for illustration. Consider the following two possibilities.

■ **CASE 1** If a voltage source is connected between the reference node and a nonreference node, we simply set the voltage at the nonreference node equal to the voltage of the voltage source. In Fig. 3.7, for example,

$$v_1 = 10 \text{ V} \quad (3.10)$$

Thus, our analysis is somewhat simplified by this knowledge of the voltage at this node.

■ **CASE 2** If the voltage source (dependent or independent) is connected between two nonreference nodes, the two nonreference nodes

**Figure 3.7**

A circuit with a supernode.

form a *generalized node* or *supernode*; we apply both KCL and KVL to determine the node voltages.

A **supernode** is formed by enclosing a (dependent or independent) voltage source connected between two nonreference nodes and any elements connected in parallel with it.

A supernode may be regarded as a closed surface enclosing the voltage source and its two nodes.

In Fig. 3.7, nodes 2 and 3 form a supernode. (We could have more than two nodes forming a single supernode. For example, see the circuit in Fig. 3.14.) We analyze a circuit with supernodes using the same three steps mentioned in the previous section except that the supernodes are treated differently. Why? Because an essential component of nodal analysis is applying KCL, which requires knowing the current through each element. There is no way of knowing the current through a voltage source in advance. However, KCL must be satisfied at a supernode like any other node. Hence, at the supernode in Fig. 3.7,

$$i_1 + i_4 = i_2 + i_3 \quad (3.11a)$$

or

$$\frac{v_1 - v_2}{2} + \frac{v_1 - v_3}{4} = \frac{v_2 - 0}{8} + \frac{v_3 - 0}{6} \quad (3.11b)$$

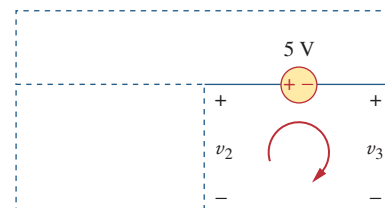
To apply Kirchhoff's voltage law to the supernode in Fig. 3.7, we redraw the circuit as shown in Fig. 3.8. Going around the loop in the clockwise direction gives

$$-v_2 + 5 + v_3 = 0 \quad \Rightarrow \quad v_2 - v_3 = 5 \quad (3.12)$$

From Eqs. (3.10), (3.11b), and (3.12), we obtain the node voltages.

Note the following properties of a supernode:

1. The voltage source inside the supernode provides a constraint equation needed to solve for the node voltages.
2. A supernode has no voltage of its own.
3. A supernode requires the application of both KCL and KVL.

**Figure 3.8**

Applying KVL to a supernode.

Example 3.3

For the circuit shown in Fig. 3.9, find the node voltages.

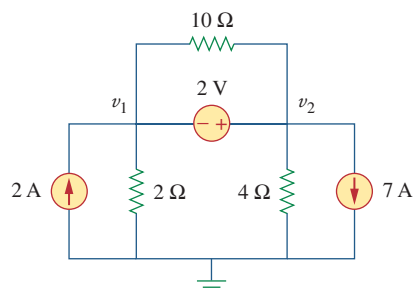


Figure 3.9
For Example 3.3.

Solution:

The supernode contains the 2-V source, nodes 1 and 2, and the 10-Ω resistor. Applying KCL to the supernode as shown in Fig. 3.10(a) gives

$$2 = i_1 + i_2 + 7$$

Expressing i_1 and i_2 in terms of the node voltages

$$2 = \frac{v_1 - 0}{2} + \frac{v_2 - 0}{4} + 7 \Rightarrow 8 = 2v_1 + v_2 + 28$$

or

$$v_2 = -20 - 2v_1 \quad (3.3.1)$$

To get the relationship between v_1 and v_2 , we apply KVL to the circuit in Fig. 3.10(b). Going around the loop, we obtain

$$-v_1 - 2 + v_2 = 0 \Rightarrow v_2 = v_1 + 2 \quad (3.3.2)$$

From Eqs. (3.3.1) and (3.3.2), we write

$$v_2 = v_1 + 2 = -20 - 2v_1$$

or

$$3v_1 = -22 \Rightarrow v_1 = -7.333 \text{ V}$$

and $v_2 = v_1 + 2 = -5.333 \text{ V}$. Note that the 10-Ω resistor does not make any difference because it is connected across the supernode.

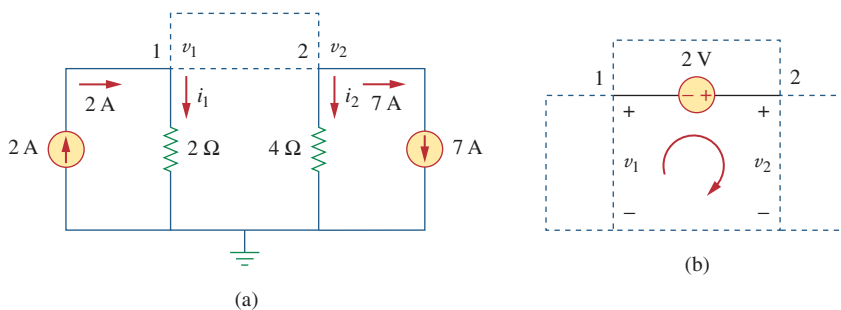


Figure 3.10

Applying: (a) KCL to the supernode, (b) KVL to the loop.

Practice Problem 3.3

Find v and i in the circuit of Fig. 3.11.

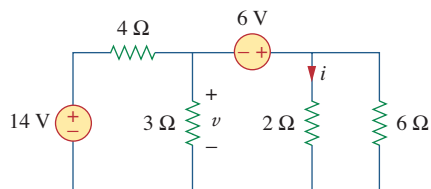


Figure 3.11

For Practice Prob. 3.3.

Answer: -400 mV , 2.8 A .

Find the node voltages in the circuit of Fig. 3.12.

Example 3.4

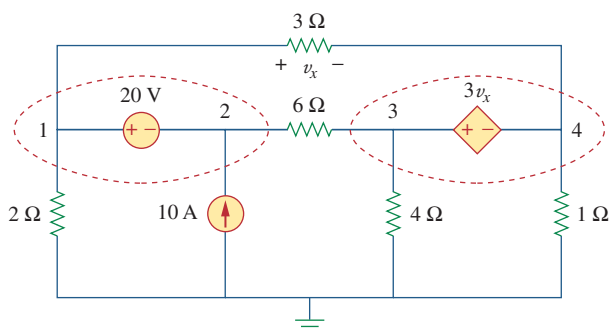


Figure 3.12
For Example 3.4.

Solution:

Nodes 1 and 2 form a supernode; so do nodes 3 and 4. We apply KCL to the two supernodes as in Fig. 3.13(a). At supernode 1-2,

$$i_3 + 10 = i_1 + i_2$$

Expressing this in terms of the node voltages,

$$\frac{v_3 - v_2}{6} + 10 = \frac{v_1 - v_4}{3} + \frac{v_1}{2}$$

or

$$5v_1 + v_2 - v_3 - 2v_4 = 60 \quad (3.4.1)$$

At supernode 3-4,

$$i_1 = i_3 + i_4 + i_5 \Rightarrow \frac{v_1 - v_4}{3} = \frac{v_3 - v_2}{6} + \frac{v_4}{1} + \frac{v_3}{4}$$

or

$$4v_1 + 2v_2 - 5v_3 - 16v_4 = 0 \quad (3.4.2)$$

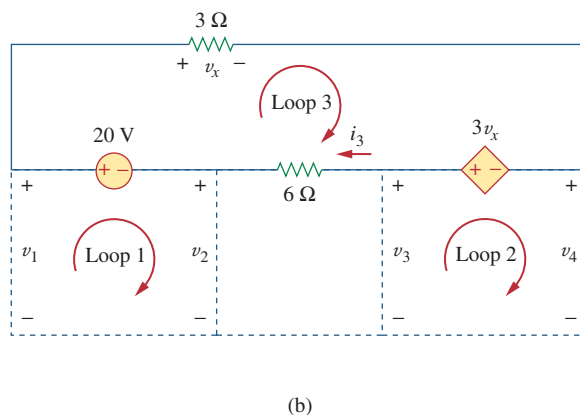
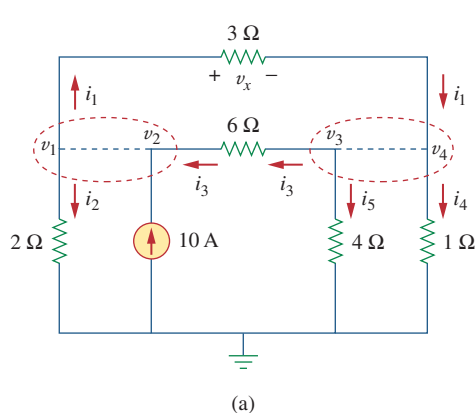


Figure 3.13

Applying: (a) KCL to the two supernodes, (b) KVL to the loops.

We now apply KVL to the branches involving the voltage sources as shown in Fig. 3.13(b). For loop 1,

$$-v_1 + 20 + v_2 = 0 \quad \Rightarrow \quad v_1 - v_2 = 20 \quad (3.4.3)$$

For loop 2,

$$-v_3 + 3v_x + v_4 = 0$$

But $v_x = v_1 - v_4$ so that

$$3v_1 - v_3 - 2v_4 = 0 \quad (3.4.4)$$

For loop 3,

$$v_x - 3v_x + 6i_3 - 20 = 0$$

But $6i_3 = v_3 - v_2$ and $v_x = v_1 - v_4$. Hence,

$$-2v_1 - v_2 + v_3 + 2v_4 = 20 \quad (3.4.5)$$

We need four node voltages, v_1, v_2, v_3 , and v_4 , and it requires only four out of the five Eqs. (3.4.1) to (3.4.5) to find them. Although the fifth equation is redundant, it can be used to check results. We can solve Eqs. (3.4.1) to (3.4.4) directly using *MATLAB*. We can eliminate one node voltage so that we solve three simultaneous equations instead of four. From Eq. (3.4.3), $v_2 = v_1 - 20$. Substituting this into Eqs. (3.4.1) and (3.4.2), respectively, gives

$$6v_1 - v_3 - 2v_4 = 80 \quad (3.4.6)$$

and

$$6v_1 - 5v_3 - 16v_4 = 40 \quad (3.4.7)$$

Equations (3.4.4), (3.4.6), and (3.4.7) can be cast in matrix form as

$$\begin{bmatrix} 3 & -1 & -2 \\ 6 & -1 & -2 \\ 6 & -5 & -16 \end{bmatrix} \begin{bmatrix} v_1 \\ v_3 \\ v_4 \end{bmatrix} = \begin{bmatrix} 0 \\ 80 \\ 40 \end{bmatrix}$$

Using Cramer's rule gives

$$\Delta = \begin{vmatrix} 3 & -1 & -2 \\ 6 & -1 & -2 \\ 6 & -5 & -16 \end{vmatrix} = -18, \quad \Delta_1 = \begin{vmatrix} 0 & -1 & -2 \\ 80 & -1 & -2 \\ 40 & -5 & -16 \end{vmatrix} = -480,$$

$$\Delta_3 = \begin{vmatrix} 3 & 0 & -2 \\ 6 & 80 & -2 \\ 6 & 40 & -16 \end{vmatrix} = -3120, \quad \Delta_4 = \begin{vmatrix} 3 & -1 & 0 \\ 6 & -1 & 80 \\ 6 & -5 & 40 \end{vmatrix} = 840$$

Thus, we arrive at the node voltages as

$$v_1 = \frac{\Delta_1}{\Delta} = \frac{-480}{-18} = 26.67 \text{ V}, \quad v_3 = \frac{\Delta_3}{\Delta} = \frac{-3120}{-18} = 173.33 \text{ V},$$

$$v_4 = \frac{\Delta_4}{\Delta} = \frac{840}{-18} = -46.67 \text{ V}$$

and $v_2 = v_1 - 20 = 6.667 \text{ V}$. We have not used Eq. (3.4.5); it can be used to cross check results.

Find v_1 , v_2 , and v_3 in the circuit of Fig. 3.14 using nodal analysis.

Answer: $v_1 = 7.608 \text{ V}$, $v_2 = -17.39 \text{ V}$, $v_3 = 1.6305 \text{ V}$.

3.4 Mesh Analysis

Mesh analysis provides another general procedure for analyzing circuits, using mesh currents as the circuit variables. Using mesh currents instead of element currents as circuit variables is convenient and reduces the number of equations that must be solved simultaneously. Recall that a loop is a closed path with no node passed more than once. A mesh is a loop that does not contain any other loop within it.

Nodal analysis applies KCL to find unknown voltages in a given circuit, while mesh analysis applies KVL to find unknown currents. Mesh analysis is not quite as general as nodal analysis because it is only applicable to a circuit that is *planar*. A planar circuit is one that can be drawn in a plane with no branches crossing one another; otherwise it is *nonplanar*. A circuit may have crossing branches and still be planar if it can be redrawn such that it has no crossing branches. For example, the circuit in Fig. 3.15(a) has two crossing branches, but it can be redrawn as in Fig. 3.15(b). Hence, the circuit in Fig. 3.15(a) is planar. However, the circuit in Fig. 3.16 is nonplanar, because there is no way to redraw it and avoid the branches crossing. Nonplanar circuits can be handled using nodal analysis, but they will not be considered in this text.

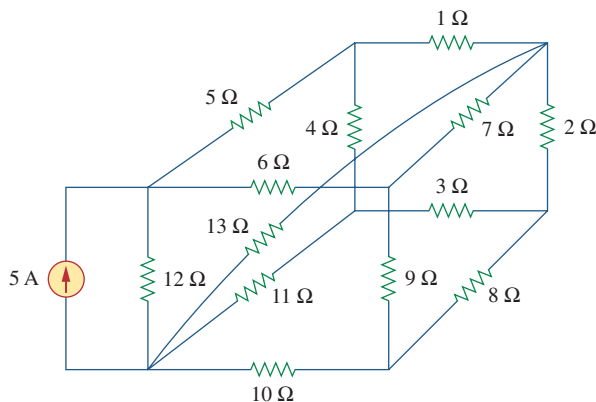


Figure 3.16
A nonplanar circuit.

To understand mesh analysis, we should first explain more about what we mean by a mesh.

A **mesh** is a loop which does not contain any other loops within it.

Practice Problem 3.4

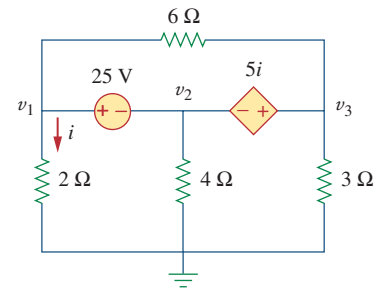


Figure 3.14
For Practice Prob. 3.4.

Mesh analysis is also known as *loop analysis* or the *mesh-current method*.

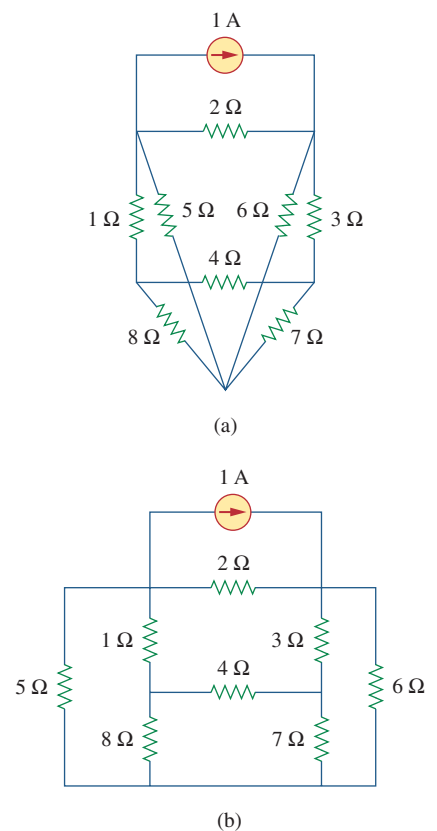


Figure 3.15
(a) A planar circuit with crossing branches,
(b) the same circuit redrawn with no crossing branches.