LoRa32u4II V1.2 Pinout Diagram

Notes:

Max 20 mA per IO recommended 10 mA 200 mA for all IOs max
VBAt is the positive battery terminal 3.7 - 4.2V
EN msut be tied to GND to disable 3.3V LDO regulator
LDO regulator max output 600mA

LiPo battery voltage
D9 (ADC12)

RST					
3V3					
AREF					
GND					
ADC7	TDI	PF7	18 A1	36	A0
ADC6	TDO	PF6	19 A1	37	A1
ADC5	TMS	PF5	20 A2	38	A2
ADC4	TCK	PF4	21 A3	39	A3
ADC1	PF1	22 A4	40	A4	
ADC0	PF0	23 A5	41	A5	
SCLK	PCINT1	PB1	15	9	SCK
MOSI	PCINT2	PDI	PB2	16	10
MISO	PCINT3	PDO	PB3	14	11
RXD1	INT2	PD2	0	20	
TXD2	INT3	PD3	1	21	
	INT8	PC0	14	23	

BAT
EN
USB

13	32	13	PC7	ICP3	CLK0	OC4A
12	26	12	PD6	T1	OC4D	ADC9
11	12	11	PB7	PCINT7	OC1C	OC0A	RTS
10	30	10	PB6	PCINT6	OC1B	OC4B	ADC13
9	29	9	PB5	PCINT5	OC1A	OC4B	ADC12
6	27	6	PD7	TO	OC4D	ADC10
5	31	5	PC6	OC3A	OC4A	
3	18	3	PD0	INT0	OC4B	SCL
2	19	2	PD1	INT1	SDA	

Legend

■	GND	■	Int	■	Analog Pin
■	Power	■	Port pin	⌇	PWM/1Wire/I2C
■	Control	■	Pin function	■	Serial Pin
■	Interrupt	■	Arduino pin	■	Physical Pin

LoRa pinout

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RST   -   D4 (RST)
NSS   -   D8 (CS)
MOSI  -   D16(MOSI)
MISO  -   D14(MISO)
SCK   -   D15(SCK)
DIO0  -   D7 (IRQ)
DIO1  -   D1*
DIO2  -   D2*
DIO3  -   D3*
DIO5  -   NC
DIO4  -   NC
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*solder jumper on back