






IGEPv2 - RC version (2011-March-21)

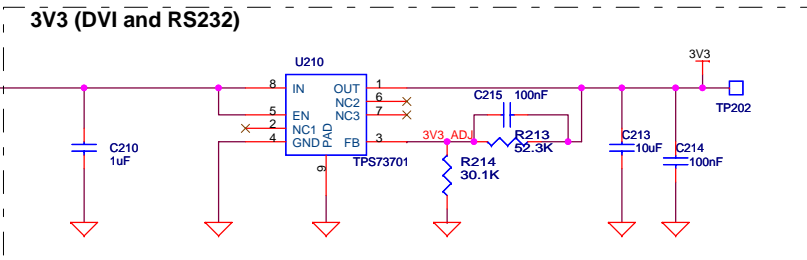
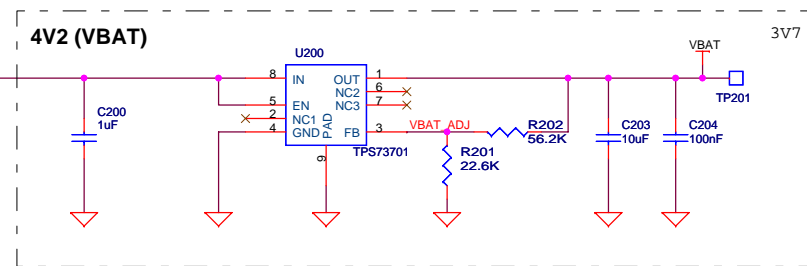
PAGE NO.	SCHEMATIC PAGE
1	COVER PAGE
2	POWER
3	OMAP3 1 OF 3
4	OMAP3 2 OF 3, JTAG, SWITCHES, LEDS, SVIDEO
5	OMAP3 3 OF 3
6	TWL4030 1 of 2, AUDIO JACKS, LED, 26MHZ, 32KHZ
7	TWL4030 2 of 2
8	USB HOST & OTG
9	SD/MMC, SERIAL DEBUG PORT, BB EXP., KEYB EXP.
10	DVI-D & LCD EXP.
11	ETHERNET
12	CAM + ADC
13	COMBO WIFI BLUETOOTH



This schematic is ***NOT SUPPORTED***. Only "community" support is allowed via resources at:

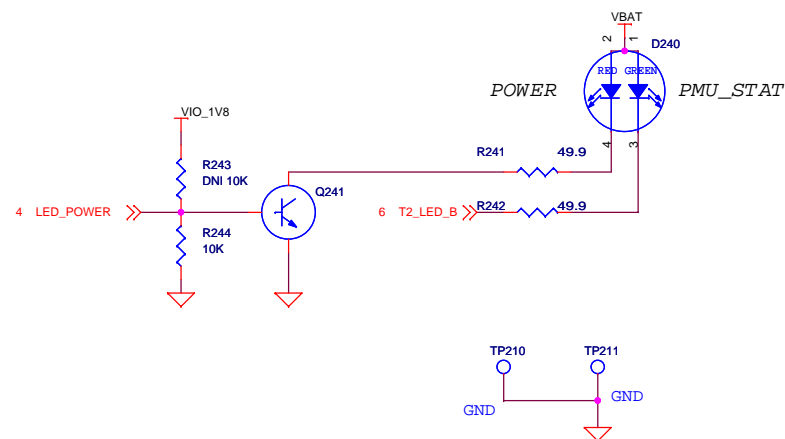
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 ISEE		Wednesday, March 23, 2011				
01 - COVER		A INIT EDIT	AFG	22/05/09	MCT	21/03/14
ISEPv2-RC		REV. MODIF.	DES.	DATE	REV.	DATE
		SCALE 1/1				
		 			C	SHEET 1 / 13



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	R243	R244
IGEPv2 - RB version	10K	Empty
IGEPv2 - RC version	Empty	10K



These need to extend barely past the edge of the processor. These leads need to be as short as possible.

TP300 SDC_CLK

TP301 SDC_nCS0

10 DSS_D0
10 DSS_D1
10 DSS_D2
10 DSS_D3
10 DSS_D4
10 DSS_D5
10 DSS_D6
10 DSS_D7
10 DSS_D8
10 DSS_D9
10 DSS_D10
10 DSS_D11
10 DSS_D12
10 DSS_D13
10 DSS_D14
10 DSS_D15
10 DSS_D16
10 DSS_D17
10 DSS_D18
10 DSS_D19
10 DSS_D20
10 DSS_D21
10 DSS_D22
10 DSS_D23
10 DSS_PCLK
10 DSS_HSYNC
10 DSS_VSYNC
10 DSS_ACBIA5

R300 33 MMC_CLK

9 MMC1_CLK0
9 MMC1_CMD
9 MMC1_DAT0
9 MMC1_DAT1
9 MMC1_DAT2
9 MMC1_DAT3
9 MMC1_DAT4
9 MMC1_DAT5
9 MMC1_DAT6
9 MMC1_DAT7

R301 33 MMC2_CLK

9.13 MMC2_CLK0
9.13 MMC2_CMD
9.13 MMC2_DAT0
9.13 MMC2_DAT1
9.13 MMC2_DAT2
9.13 MMC2_DAT3
9.13 MMC2_DAT4
9.13 MMC2_DAT5
9.13 MMC2_DAT6
9.13 MMC2_DAT7

WLAN (pg13) - EXP (pg9)

OMAP3530

H10 SDCR_BA1
H9 SDCR_BA0

E1 SDCR_A14
E2 SDCR_A13
D1 SDCR_A12
D2 SDCR_A11
D3 SDCR_A10
D4 SDCR_A9
C1 SDCR_A8
C2 SDCR_A7
C3 SDCR_A6
D5 SDCR_A5
C4 SDCR_A4
C5 SDCR_A3
B3 SDCR_A2
B4 SDCR_A1
A4 SDCR_A0

H14 SDCR_nRAS
H13 SDCR_nCAS
H15 SDCR_nWE
A13 SDCR_CLK
H17 SDCR_nCLK
H16 SDCR_CKE1
H12 SDCR_CKE0
H11 SDCR_nCS1
C20 SDCR_nCS0
B11 SDCR_DM3
A16 SDCR_DM2
B7 SDCR_DM1
A20 SDCR_DM0
A10 SDCR_DQS3
A17 SDCR_DQS2
A6 SDCR_DQS1

AG22 DSS_D0/DX0/UART1_CTS/DSSVENC656_DATA0/GPIO_70
AH22 DSS_D1/DX0/UART1_RTS/DSSVENC656_DATA1/GPIO_71
AG23 DSS_D2/DX1/DSSVENC656_DATA2/GPIO_72
AH23 DSS_D3/DX1/DSSVENC656_DATA3/GPIO_73
AG24 DSS_D4/DX2/UART3_RX_IRRX/DSSVENC656_DATA4/GPIO_74
AH24 DSS_D5/DX2/UART3_TX_IRTX/DSSVENC656_DATA5/GPIO_75
E28 DSS_D6/UART1_TX/DSSVENC656_DATA6/GPIO_76/HW_DBG14
E27 DSS_D7/UART1_RX/DSSVENC656_DATA7/GPIO_77/HW_DBG15
G26 DSS_D8/GPIO_78/HW_DBG16
G25 DSS_D9/GPIO_79/HW_DBG17
AD28 DSS_D10/SDI_DAT1N/GPIO_80
AD27 DSS_D11/SDI_DAT1P/GPIO_81
AB28 DSS_D12/SDI_DAT2N/GPIO_82
AG27 DSS_D13/SDI_DAT2P/GPIO_83
AA28 DSS_D14/SDI_DAT3N/GPIO_84
AA27 DSS_D15/SDI_DAT3P/GPIO_85
G25 DSS_D16/GPIO_86
H27 DSS_D17/GPIO_87
H26 DSS_D18/SDI_VSYNC/McSPI3_CLK/DSS_D0/GPIO_88
H25 DSS_D19/SDI_HSYNC/McSPI3_SIMO/DSS_D1/GPIO_89
E28 DSS_D20/SDI_DEN/McSPI3_SOMI/DSS_D2/GPIO_90
J26 DSS_D21/SDI_STP/McSPI3_CS0/DSS_D3/GPIO_91
AC27 DSS_D22/SDI_CLKP/McSPI3_CS1/DSS_D4/GPIO_92
AC28 DSS_D23/SDI_CLKN/DSS_D5/GPIO_93
D28 DSS_PCLK/GPIO_66/HW_DBG12
D26 DSS_HSYNC/GPIO_67/HW_DBG13
D27 DSS_VSYNC/GPIO_68
E27 DSS_ACBIA5/GPIO_69

N28 MMC1_CLK/MS_CLK/GPIO_120
M27 MMC1_CMD/MS_BS/GPIO_121
N27 MMC1_DAT0/MS_DAT0/GPIO_122
N26 MMC1_DAT1/MS_DAT1/GPIO_123
P28 MMC1_DAT2/MS_DAT2/GPIO_124
P27 MMC1_DAT3/MS_DAT3/GPIO_125
M27 MMC1_DAT4/SIM_IO/GPIO_126
P26 MMC1_DAT5/SIM_CLK/GPIO_127
R27 MMC1_DAT6/SIM_PWRCTRL/GPIO_128
R25 MMC1_DAT7/SIM_RST/GPIO_129

AE2 MMC2_CLK/McSPI3_CLK/GPIO_130
AG5 MMC2_CMD/McSPI3_SIMO/GPIO_131
AH5 MMC2_DAT0/McSPI3_SOMI/GPIO_132
AH4 MMC2_DAT1/GPIO_133
AG4 MMC2_DAT2/McSPI3_CS1/GPIO_134
AF4 MMC2_DAT3/McSPI3_CS0/GPIO_135
AF3 MMC2_DAT4/MMC2_DIR_DAT0/MMC3_DAT0/GPIO_136
AH3 MMC2_DAT5/MMC2_DIR_DAT1/CAM_GLOBAL_RESET/MMC3_DAT1/GPIO_137/HSUSB3_TLL_STP/MM3_RXDP
AF3 MMC2_DAT6/MMC2_DIR_CMD/CAM_SHUTTER/MMC3_DAT2/GPIO_138/HSUSB3_TLL_DIR
AF3 MMC2_DAT7/MMC2_CLKIN/MMC3_DAT3/GPIO_139/HSUSB3_TLL_NXT/MM3_RXDM

U300A

C21 SDCR_D31
B21 SDCR_D30
A21 SDCR_D29
D20 SDCR_D28
B20 SDCR_D27
B19 SDCR_D26
A19 SDCR_D25
C18 SDCR_D24
D14 SDCR_D23
B13 SDCR_D22
A11 SDCR_D21
C12 SDCR_D20
C11 SDCR_D19
B10 SDCR_D18
D11 SDCR_D17
D18 SDCR_D16
C17 SDCR_D15
B16 SDCR_D14
C17 SDCR_D13
B16 SDCR_D12
C15 SDCR_D11
B14 SDCR_D10
C14 SDCR_D9
B9 SDCR_D8
A9 SDCR_D7
A7 SDCR_D6
C9 SDCR_D5
C8 SDCR_D4
B6 SDCR_D3
C6 SDCR_D2
C6 SDCR_D1
D6 SDCR_D0

K3 GPMC_A10/SYS_nDMAREQ3/GPIO_43
L3 GPMC_A9/SYS_nDMAREQ2/GPIO_42
M3 GPMC_A8/GPIO_41
N3 GPMC_A7/GPIO_40
T3 GPMC_A6/GPIO_39
K4 GPMC_A5/GPIO_38
L4 GPMC_A4/GPIO_37
M4 GPMC_A3/GPIO_36
N4 GPMC_A2/GPIO_35
GPMC_A1/GPIO_34

Y1 GPMC_D15/GPIO_51
W1 GPMC_D14/GPIO_50
T2 GPMC_D13/GPIO_49
R2 GPMC_D12/GPIO_48
P1 GPMC_D11/GPIO_47
H2 GPMC_D10/GPIO_46
W2 GPMC_D9/GPIO_45
V2 GPMC_D8/GPIO_44
GPMC_D7
GPMC_D6
GPMC_D5
GPMC_D4
GPMC_D3
GPMC_D2
GPMC_D1
GPMC_D0

G4 GPMC_nCS0
H3 GPMC_nCS1
V8 GPMC_nCS2/GPIO_52
U8 GPMC_nCS3
T8 GPMC_nCS4
R8 GPMC_nCS5
P8 GPMC_nCS6
N8 GPMC_nCS7
T4 GPMC_CLK
F4 GPMC_nWE
G2 GPMC_nOE
E3 GPMC_nADV
G3 GPMC_nBE0_CLE
U3 GPMC_nBE1/GPIO_61
H1 GPMC_nWP/GPIO_62
M8 GPMC_WAIT0
GPMC_WAIT1/GPIO_63
GPMC_WAIT2/GPIO_64
GPMC_WAIT3/SYS_nDMAREQ1/GPIO_65

GPMC_nCS4/SYS_nDMAREQ1/McBSP4_CLK/GPT9_PWM_EVT/GPIO_55
GPMC_nCS5/SYS_nDMAREQ2/McBSP4_DR/GPT10_PWM_EVT/GPIO_56
GPMC_nCS6/SYS_nDMAREQ3/McBSP4_DX/GPT11_PWM_EVT/GPIO_57
GPMC_nCS7/GPMC_IODIR/McBSP4_FSX/GPT8_PWM_EVT/GPIO_58

AB26 UART2_CTS 13
AB25 UART2_RTS 13
AA25 UART2_TX 9.13
AD25 UART2_RX 9.13
H18 TP316
H19 TP317
H20 UART3_RX 9
H21 UART3_TX 9

UART2 / BT (pg13)

Serial
Debug
(pg9)

GMPC_Ax

GMPC_Dx

VIO_1V8

R302 10K

TP302

TP303

TP304

TP305

TP306

TP307

TP308

TP309

TP310

TP311

TP312

TP313

TP314

TP315

TP318



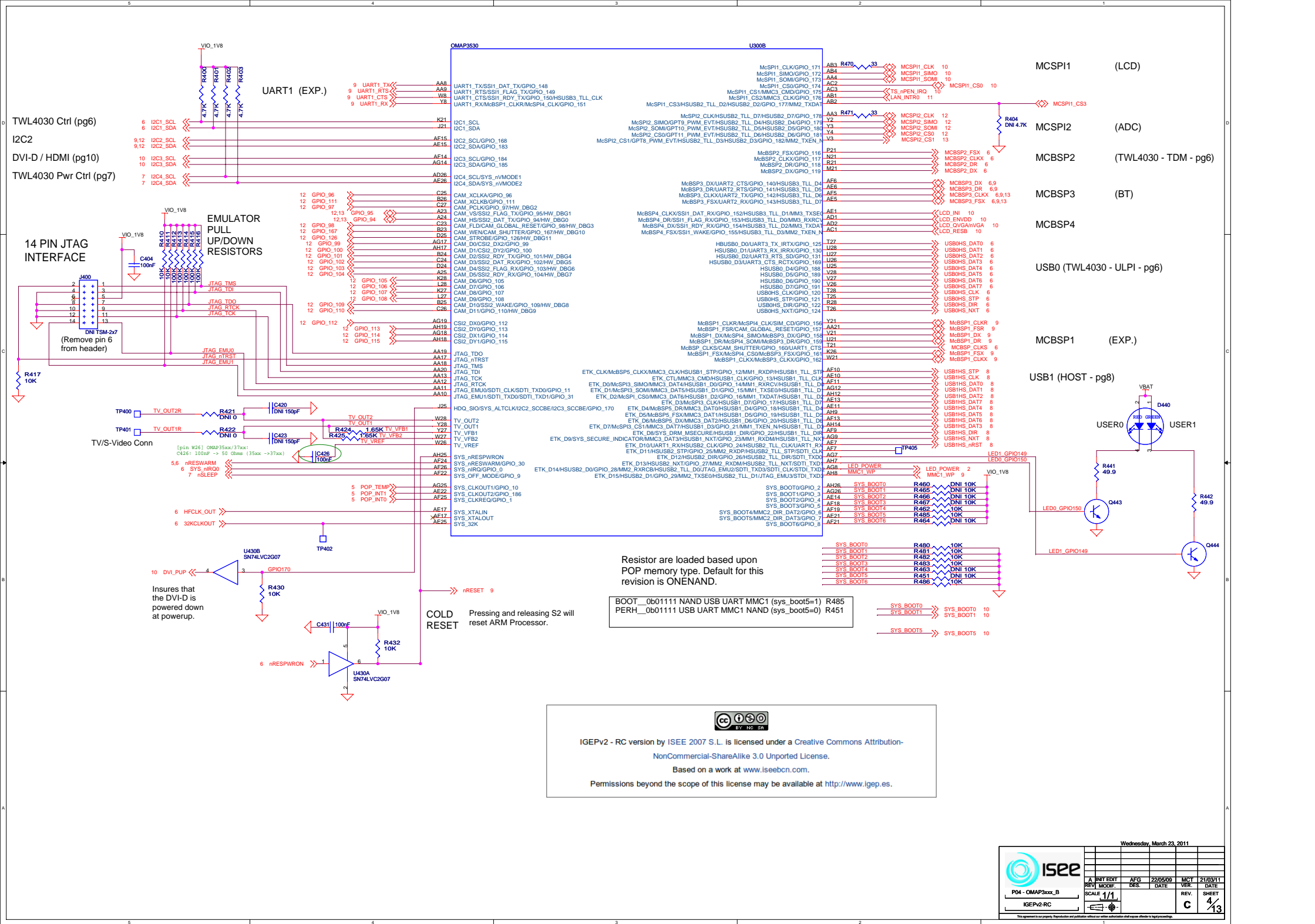
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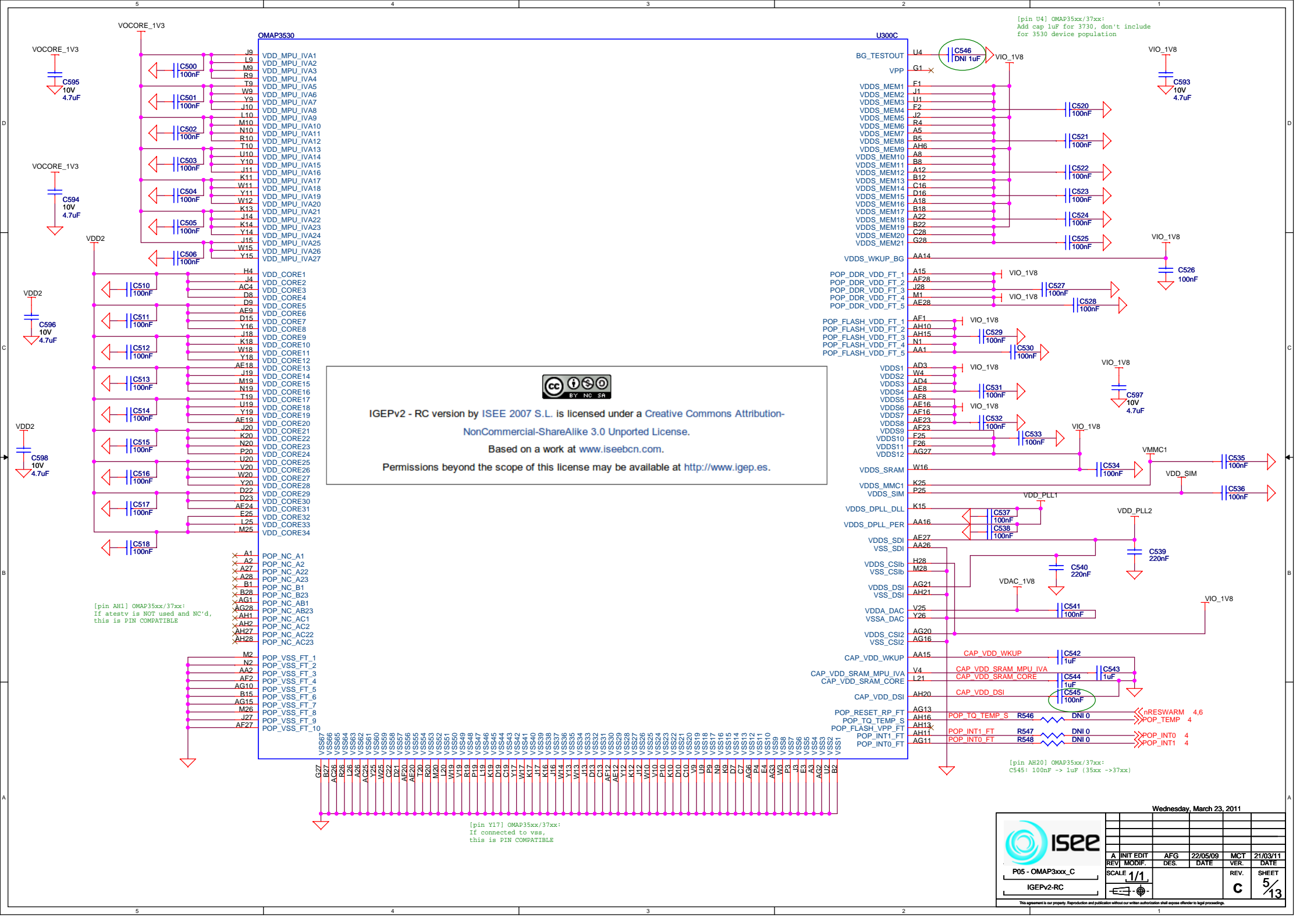
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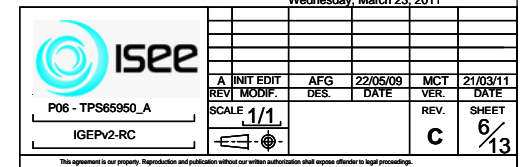
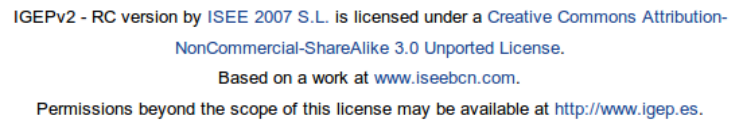
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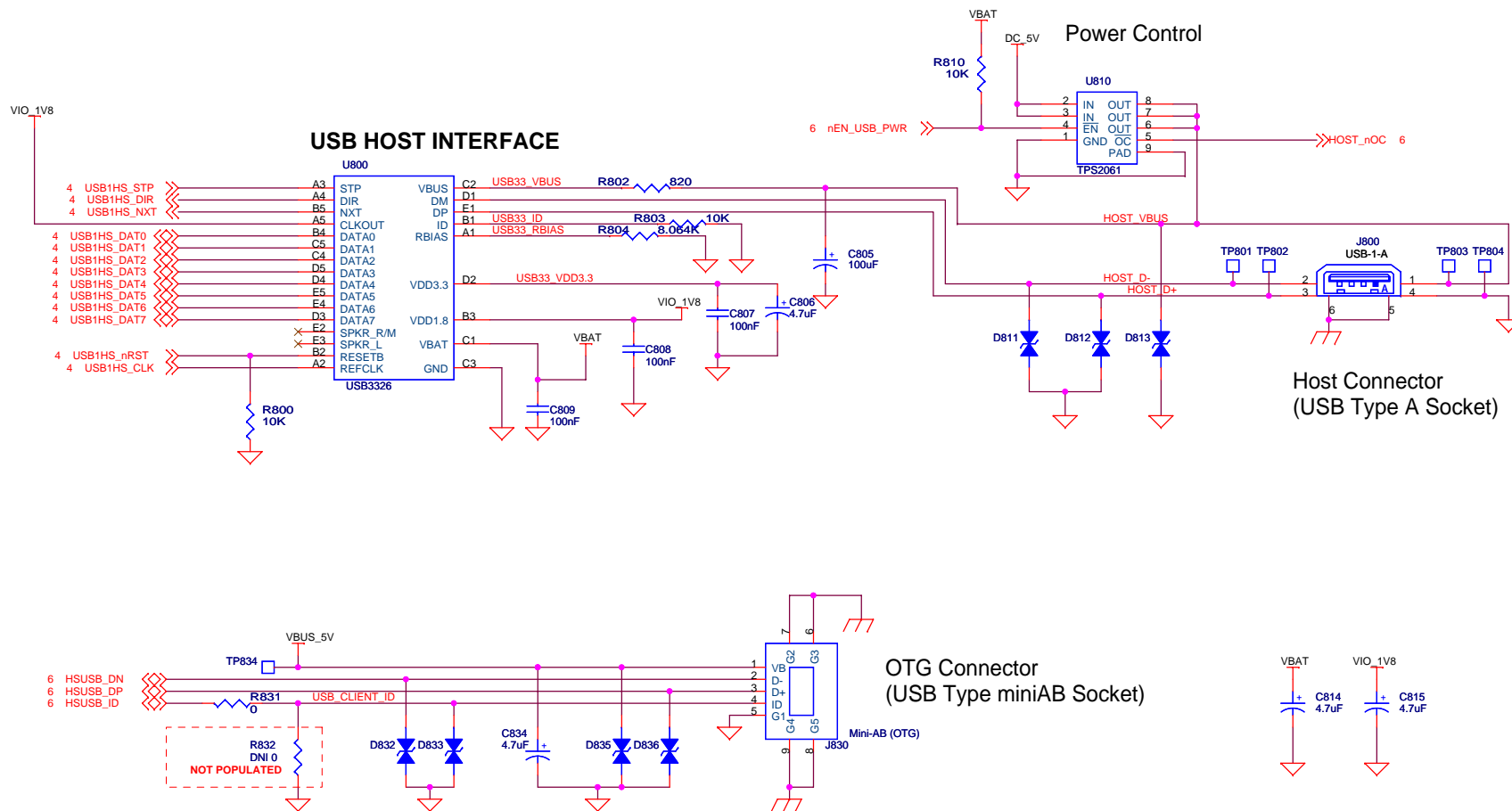
Wednesday, March 23, 2011

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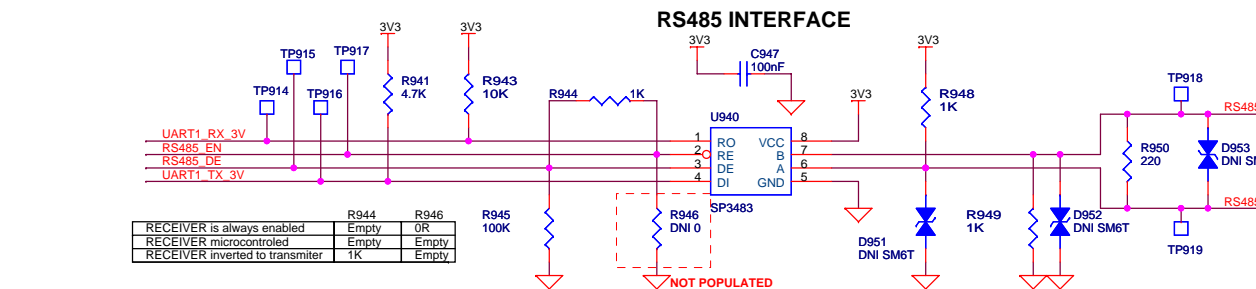
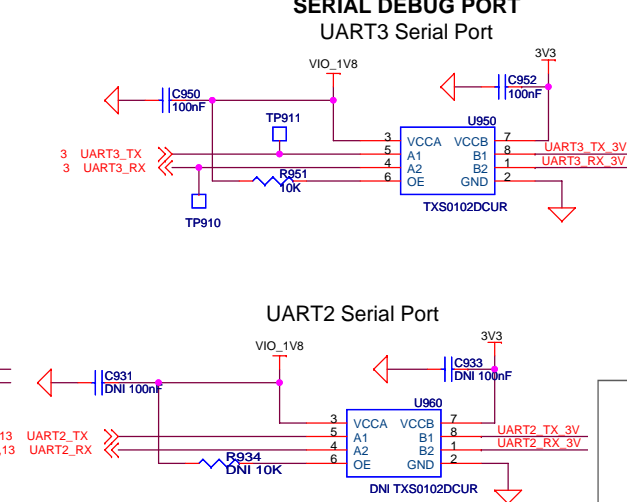
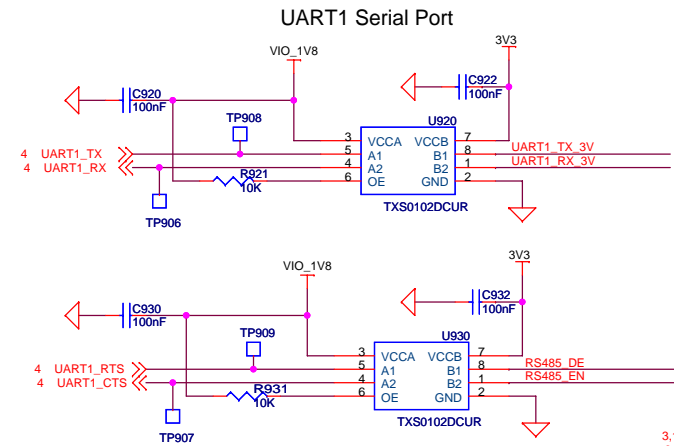
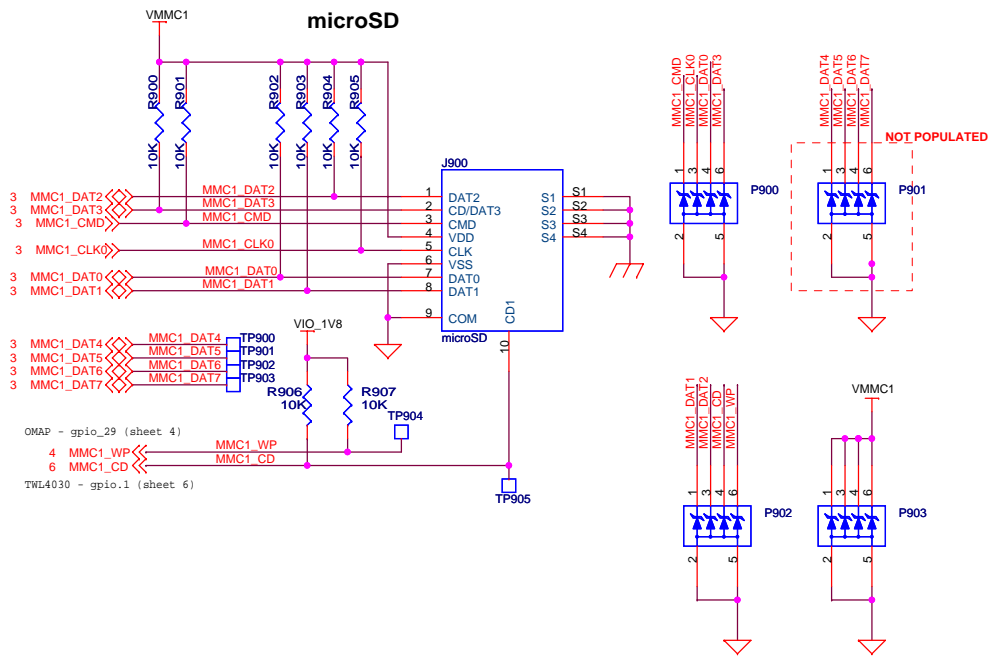


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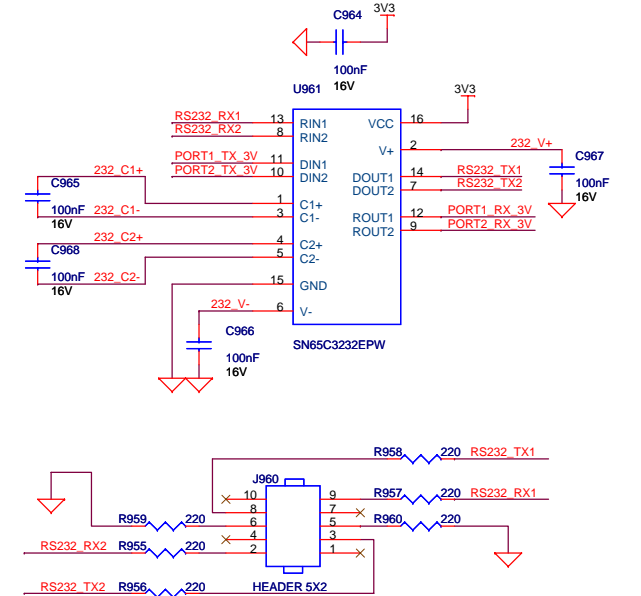
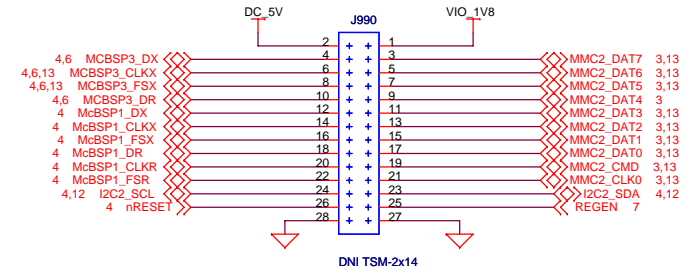
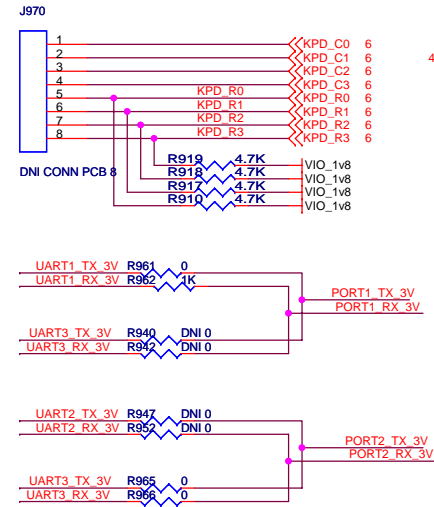
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P08 - USB HOST and OTG						
IGEPv2-RC						
A	INIT	EDIT	AFG	22/05/09	MCT	21/03/11
SCALE	1/1	REV	MODIF.	DES.	DATE	VER.
REV. C						
SHEET 8/13						
						
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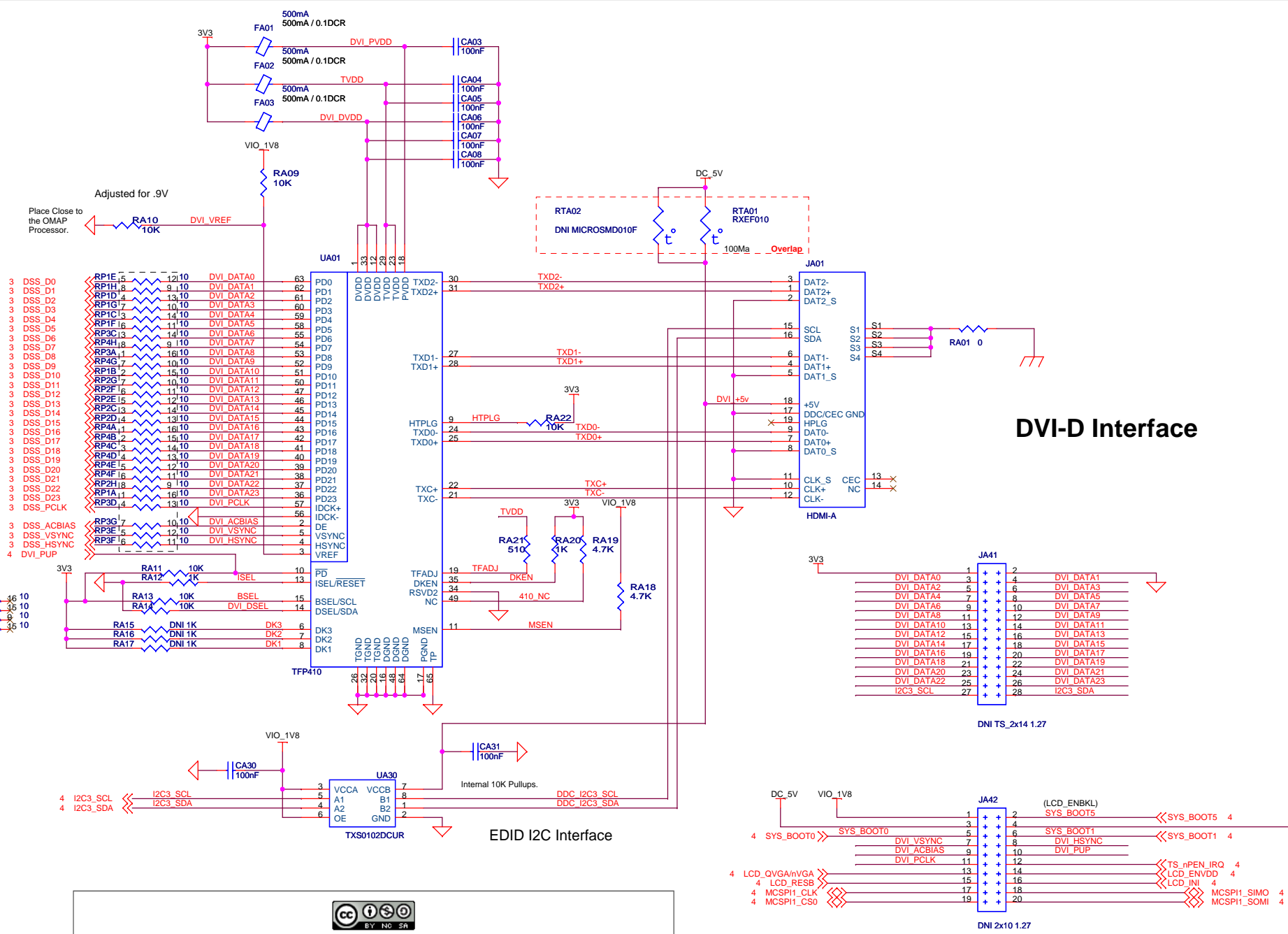


Expansion Connectors



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<Variant Name>		Wednesday, March 23, 2011			
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REV	MODIF.	DES.	DATE	VER.	DATE
SCALE 1/1				REV.	SHEET
IGEPv2-RC				C	9/13



DVI-D Interface

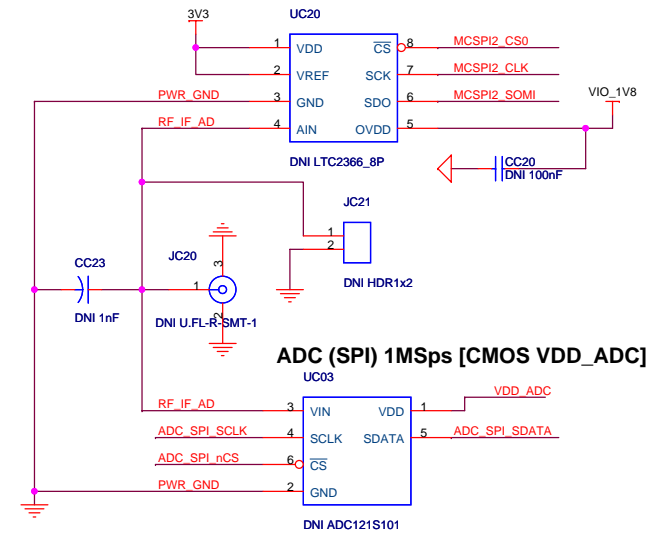
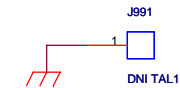
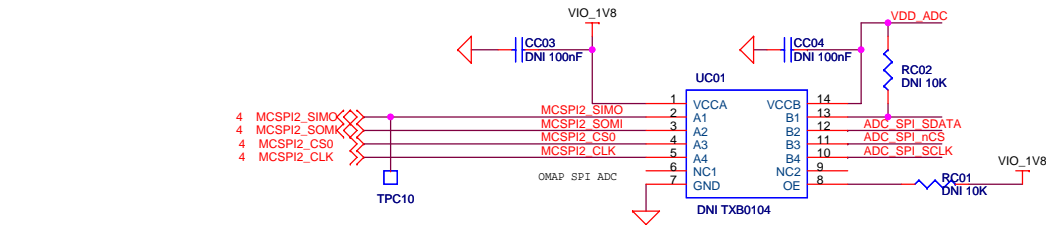
EDID I2C Interface




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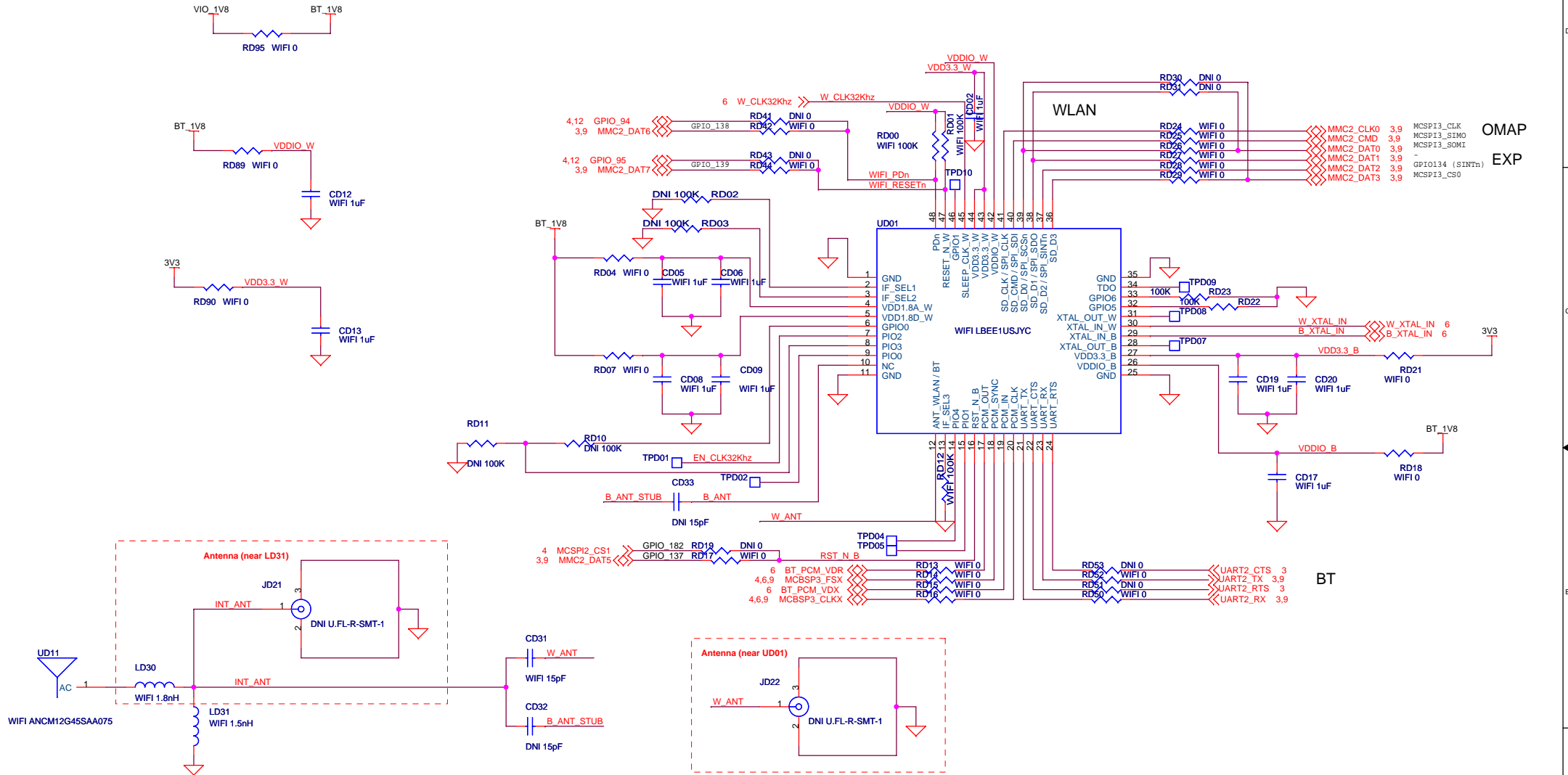
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P10 - DVI-D / HDMI				c	10/13
IGEPv2-RC					

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 ISEE P12 - ADC_CAM IGPv2-RC		Wednesday, March 23, 2011			
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			DATE	YEAR	21/03/11
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		 		C	12/13
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WIFI-BLUETOOTH



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Wednesday, March 23, 2011					
P13 - WIFI BLUETOOTH					
IGEPv2-RC					
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				C	13/13