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CU Boulder - ECEA 5318 - Real-Time Project for Embedded Systems
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Preliminary Timing Diagram for 1Hz Synchronome with Estimated WCET

Service	Service Name	Frequency (Hz)	Period (ms)	Period (Time Slices)	Estimated WCET (Time Slices)	Utilization	Core	Core Utilization	Core RM LUB	Core LCM	1	2	3 4	4 5	6 7	8	9	10 1	1 12	13	14	15 1	6 17	18	19	20	21 :	22 23	24	25	26 2	7 28	3 29 30
Seq	Sequencer	30	33.333				1																										
S1	Frame Acquisition	6	166.667	5	2	0.400	2	0.400	1.000	5		SL	ACK SL/	ACK SLACI	C	SLACK	SLACK S	SLACK		SLACE	K SLACK	SLACK		SLACE	K SLACK	SLACK		SLAC	K SLACK	SLACK		SLAC	CK SLACK SLAC
S2	Frame Differencing	2	500.000	15	6	0.400		0.567					-																				
S3	Frame Selection	2	500.000	15	1	0.067	3		0.780	30																							
S4	Frame Storage	1	1000.000	30	3	0.100	,			30																							
																		SLA	CK SLAC	K SLACE	K SLACK	SLACK						SLAC	K SLACK	SLACK 5	SLACK SL	ACK SLAG	CK SLACK SLAC