Ltc2872 🡪 it is RS485. What we need is RS422/ RS232.

TM4C12 part number is not matching with schematics and block diagram

Update FPGA part number same in both Block diagram and schematics (XC6VLX130T-1FF484C)

Update the block diagram with Memory size (in facts all memory size)

Block diagram no information for 1Wire for control processor

One-wire interface working type… Ask Ramana Murthy Sir for clarification on what exact requirements on 1-wire interface.

OTP is provided on NOR Flash due to technical limitation on control processor. Jayesh to provide some information on how are we planning for booting from very small OTP that is in Serial Flash.

Block Diagram update control processor onchip RAM/ Flash in block diagram

All media side connector to be circular – Part number to take from Digikey, select miniaturization one

Properly Label for Sync and Async connector.

Block Diagram, Sync interface is missing

Total 6 media side – Async 2, Sync1, Ethernet 1, Line 1, Audio interface 1.

Provide 12V via jumper on pin 4 of Control processor RS232 debug connector

Analog custom circuit is not done yet

DSHSL is not yet done