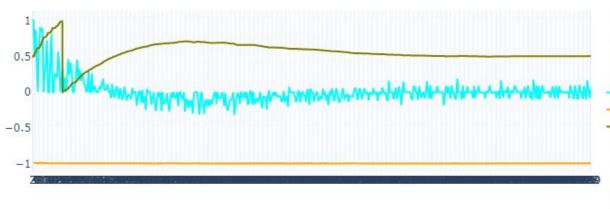
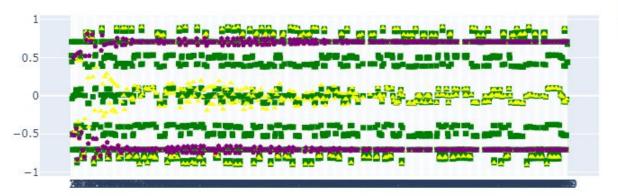
Signals





- ADC input. Real part.
- ADC output, Real part,
- Interpolator output. Real part.
- · Symbols recovered. Real part.
 - TED error.
- W error.
- mu values.
- Transmitted symbols. Real part.
- Iter vector.
- ADC input. Imaginary part.
- ADC output. Imaginary part.
- Interpolator output. Imaginary part.
- Symbols recovered. Imaginary part.
- Transmitted symbols. Imaginary part.