PIN Mapping (PIC32 MM 28 Lead)					
PIN No	DETAIL	Description	FELIX USE	VIKI USE	Comment
1	PGED1	ICSP Port 1 programming data	PGED	PGED	
2	PGEC1	ICSPTM Port 1 programming clock input	PGEC	PGEC	
3	SDA2	I2C2 data input/output	SDA1	SDA (I2C)	2 Wire I2C
4	SCL2	I2C2 synchronous serial clock input/output	SCL1	SCL (I2C)	2 Wire I2C
5	Vss	Digital modules ground	GND	GND	
6	OSC1 / RA2	Primary Oscillator crystal / PORTA digital I/Os	SCK2	SW-SPI-SCK1 (SCK1)	
7	OSC2 / RA3	Primary Oscillator crystal /PORTA digital I/Os	MOSI2	SW-SPI-SCK0 (SCK0)	
8	RB4	PORTB digital I/Os	MISO2	CS-ROW0	
9	RA4	PORTB digital I/Os	CS2.1	CS-ROW1	
10	VDD	Digital modules power supply	VCC	VCC	
11	RB5	PORTB digital I/Os	CS2.2	CS-ROW2	
12	RB6	PORTB digital I/Os	CS2.3	CS-ROW3	
13	RB7	PORTB digital I/Os	CS2.4	CS-ROW4	
14	SCK1 / RB8	SPI1 clock (input or output) / PORTB digital I/Os	SCK1	HW-SPI-SCK	
15	SDO1	SPI1 Data Output	MISO1	HW-SPI-MISO	
16	RC9	PORTC digital I/Os	CS2.5	CS-ROW5	
17	VCAP	Core voltage regulator filter capacitor connection	FREE	FREE (VCAP)	
18	RB10	PORTB digital I/Os	GPIO0	SW-SPI-MISO-C0	
19	RB11	PORTB digital I/Os	GPIO1	SW-SPI-MISO-C1	
20	VUSB3V3	USB transceiver power input (3.3V nominal)	FREE	FREE (VUSB3V3)	
21	RB13(1)	PORTB digital I/Os	GPIO2	SW-SPI-MISO-C2	
22	SDI1	SPI1 Data Input	MOSI1	HW-SPI-MOSI	
23	!SS1	SPI1 slave select input	CS1	HW-SPI-CS-IN	
24	AVSS / VSS	Analog/Digital modules ground	GND	GND	
25	AVDD / VDD	Analog/Digital modules power supply	VCC	VCC	
26	MCLR	Master Clear (device Reset)	MCLR - DEVICE RESET	MCLR - DEVICE RESET	
27	RA0	PORTA digital I/Os	FREE	SW-SPI-MISO-C3	
28	RA1	PORTA digital I/Os	FREE	SW-SPI-MISO-C4	