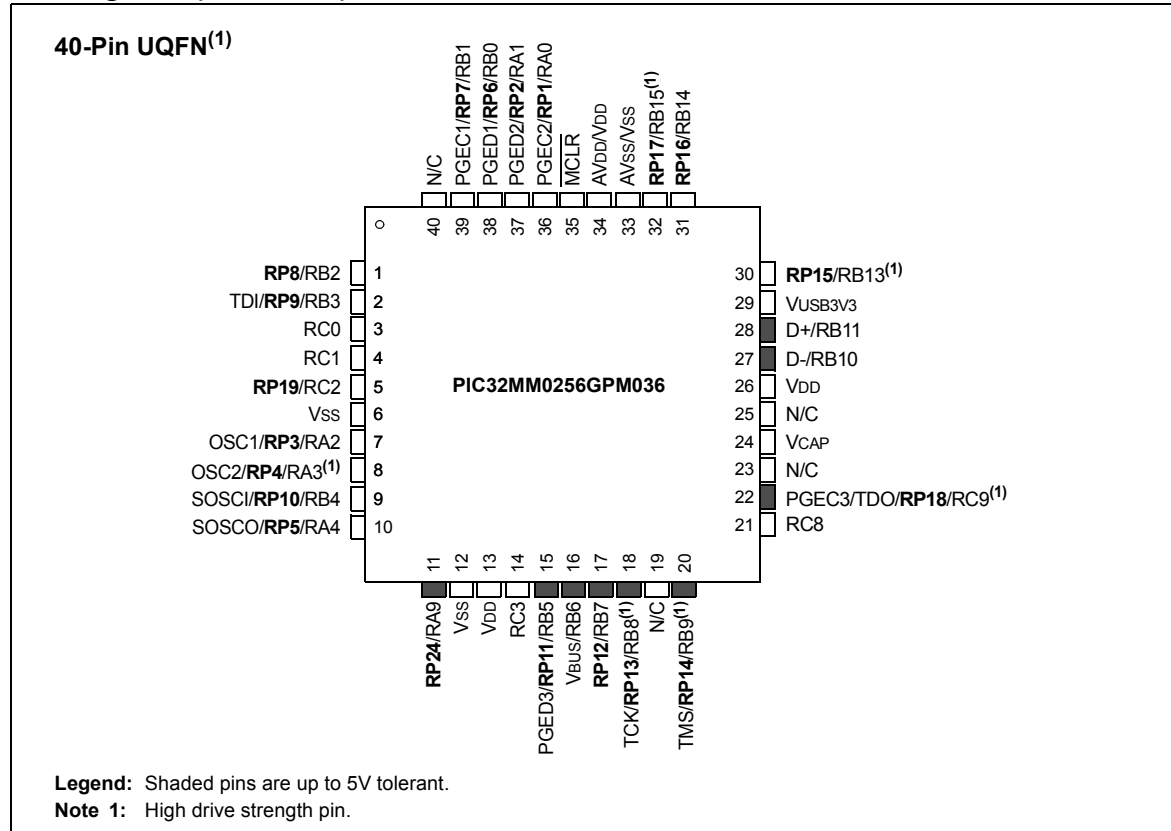


# PIC32MM0256GPM064 FAMILY

## Pin Diagrams (Continued)



**TABLE 5: COMPLETE PIN FUNCTION DESCRIPTIONS FOR 40-PIN UQFN DEVICES**

Pin	Function	Pin	Function
1	AN4/C1INB/RP8/SDA2/OCM2E/RB2	21	AN14/LVDIN/C2INC/RC8
2	TDI/AN11/C1INA/RP9/SCL2/OCM2F/RB3	22	PGEC3/TDO/RP18/ASCL1 <sup>(2)</sup> /SDO3/USBOEN/RC9 <sup>(1)</sup>
3	AN12/C2IND/T2CK/T2G/RC0	23	N/C
4	AN13/T3CK/T3G/RC1	24	VCAP
5	RP19/OCM2A/RC2	25	N/C
6	Vss	26	VDD
7	OSC1/CLKI/AN5/RP3/OCM1C/RA2	27	D-/RB10
8	OSC2/CLKO/AN6/C3IND/RP4/OCM1D/RA3 <sup>(1)</sup>	28	D+/RB11
9	SOSC1/AN7/RP10/OCM3C/RB4	29	VUSB3V3
10	SOSCO/SCLKI/RP5/PWRLCLK/OCM3D/RA4	30	AN8/RP15/SCL3/SCK3/RB13 <sup>(1)</sup>
11	RP24/OCM3A/RA9	31	CVREF/AN9/C3INB/RP16/RTCC/U1TX/VBUSON/SDI1/OCM3B/INT1/RB14
12	Vss	32	AN10/C3INA/REFCLKO/RP17/U1RX/SS1/FSYNC1/OCM2B/INT0/RB15 <sup>(1)</sup>
13	VDD	33	AVss/Vss
14	RC3	34	AVDD/VDD
15	PGED3/RP11/ASDA1 <sup>(2)</sup> /USBID/SS3/FSYNC3/OCM3E/RB5	35	MCLR
16	VBUS/RB6	36	PGEC2/VREF+/CVREF+/AN0/RP1/OCM1E/INT3/RA0
17	RP12/SDA3/SDI3/OCM3F/RB7	37	PGED2/VREF-/AN1/RP2/OCM1F/RA1
18	TCK/RP13/SCL1/U1CTS/SCK1/OCM1A/RB8 <sup>(1)</sup>	38	PGED1/AN2/C1IND/C2INB/C3INC/RP6/OCM2C/RB0
19	N/C	39	PGEC1/AN3/C1INC/C2INA/RP7/OCM2D/RB1
20	TMS/REFCLKI/RP14/SDA1/T1CK/T1G/U1RTS/U1BCLK/SDO1/OCM1B/INT2/RB9 <sup>(1)</sup>	40	N/C

**Note 1:** High drive strength pin.

**2:** Alternate pin assignments for I2C1 as determined by the I2C1SEL Configuration bit.