PIN Mapping (PIC32MM0256GPM036 40 PIN UQFN - 5x5 mm)						
PIN No	DETAIL	5V Tolerance	Description	FELIX USE	CUSTOM NAME	USAGE
1	SDA2	YES	I2C2 data input/output	SDA1	SDA	SDA (HW-I2C)
2	SCL2	YES	I2C2 synchronous serial clock input/output	SCL1	SCL	SCL (HW-I2C)
3	RC0		PORTC digital I/Os	-	MISO0	SW-SPI-MISO-COL0
4	RC1		PORTC digital I/Os	-	MISO1	SW-SPI-MISO-COL1
5	RC2	YES	PORTC digital I/Os	-	MISO2	SW-SPI-MISO-COL2
6	Vss		Digital modules ground	-	Vss	Vss
7	OSC1 / RA2	YES	Primary Oscillator crystal / PORTA digital I/Os	SCK2	SCK0	SW-SPI-SCK0 (SCK0)
8	OSC2 / RA3	YES	Primary Oscillator crystal /PORTA digital I/Os	MOSI2	SCK1	SW-SPI-SCK1 (SCK1)
9	RB4	YES	PORTB digital I/Os	MISO2	MISO3	SW-SPI-MISO-COL3
10	RA4	YES	PORTB digital I/Os	CS2.1	MISO4	SW-SPI-MISO-COL4
11	RA9	YES	PORTA digital I/Os	-	MISO5	SW-SPI-MISO-COL5
12	Vss		Digital modules ground	VSS	VSS	VSS
13	VDD		Digital modules power supply	VDD	VDD	VDD
14	RC3		PORTC digital I/Os		MISO6	SW-SPI-MISO-COL6
15	RB5	YES	PORTB digital I/Os	CS2.2	MISO7	SW-SPI-MISO-COL7
16	RB6	YES	PORTB digital I/Os	CS2.3	MISO8	SW-SPI-MISO-COL8
17	RB7	YES	PORTB digital I/Os	CS2.4	MOSI0	SW-SPI-MOSI
18	SCK1	YES	SPI1 clock (input or output)	SCK1	SCK	HW-SPI-SCK
19	NC		No Connection	-	NC	NC
20	SDO1	YES	SPI1 Data Output	MISO1	MISO	HW-SPI-MISO
21	RC8		PORTC digital I/Os	-	MOSI1	SW-SPI-MOSI
22	RC9	YES	PORTC digital I/Os	CS2.5	CS0	CS-ROW0
23	NC		No Connection	-	NC	NC
24	VCAP		Core voltage regulator filter capacitor connection	-	VCAP	FREE (VCAP)
25	NC		No Connection	-	NC	NC
26	VDD		Digital modules power supply	VDD	VDD	VDD
27	RB10	YES	PORTB digital I/Os	GPIO0	CS1	CS-ROW1
28	RB11	YES	PORTB digital I/Os	GPIO1	CS2	CS-ROW2
29	VUSB3V3		USB transceiver power input (3.3V nominal)	-	VUSB3V3	FREE (VUSB3V3)
30	RB13	YES	PORTB digital I/Os	GPIO2	CS3	CS-ROW3
31	SDI1	YES	SPI1 Data Input	MOSI1	MOSI	HW-SPI-MOSI
32	!SS1	YES	SPI1 slave select input	CS1	CS	HW-SPI-CS-IN
33	AVSS / VSS		Analog/Digital modules ground	VSS	VSS	VSS
34	AVDD / VDD		Analog/Digital modules power supply	VDD	VDD	VDD
35	MCLR		Master Clear (device Reset)	MCLR - DEVICE RESET	MCLR	MCLR - DEVICE RESET
36	RA0	YES	PORTA digital I/Os	-	CS4	CS-ROW4
37	RA1	YES	PORTA digital I/Os	-	CS5	CS-ROW5
38	PGED1	YES	ICSP Port 1 programming data	PGED	PGED	PGED
39	PGEC1	YES	ICSPTM Port 1 programming clock input	PGEC	PGEC	PGEC
40	NC		No Connection	-	NC	NC