

6. Pin-out and connection diagram

6.1. Pin-out

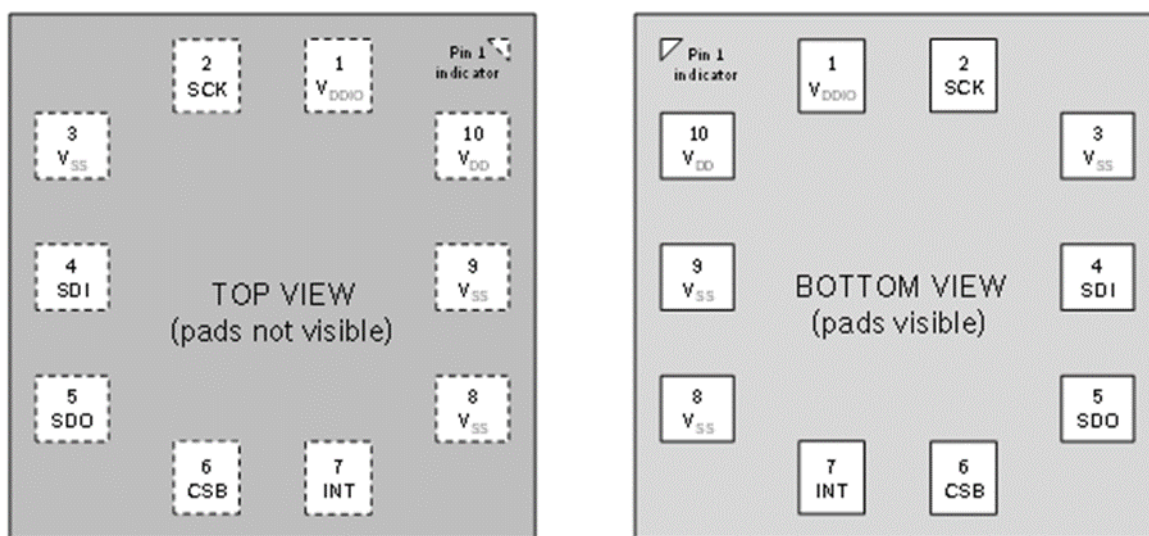


Figure 23: Pin-out top and bottom view

Table 50: Pin description

Pin	Name	I/O Type	Description	Connect to		
				SPI 4W	SPI 3W	I ² C
1	VDDIO	Supply	Digital interface supply	VDDIO		
2	SCK	In	Serial clock input	SCK	SCK	SCL
3	VSS	Supply	Ground	GND		
4	SDI	In/Out	Serial data input	SDI	SDI/SDO	SDA
5	SDO	In/Out	Serial data output	SDO	DNC	SA0
6	CSB	In	Chip select	CSB	CSB	CSB
7	INT	Out	INT output	host INT input or DNC		
8	VSS	Supply	Ground	GND		
9	VSS	Supply	Ground	GND		
10	VDD	Supply	Analog supply	VDD		