WORKSHOP REGISTRATION (FREE)

Online Workshop on “FPGA Prototyping of VLSI Designs using High-Level Synthesis”

**Speaker (Brief Profile)**  
Prateek Sikka, SoC Emulation Manager

NXP Semiconductors

Workshop Registration Link: https://docs.google.com/forms/d/e/1FAIpQLScm\_wgVT8HamKvHYpiXUkAYQQqjqL9ZbB2qJXs35l7JxoB2XA/viewform?usp=pp\_url

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