PinHeader	GPIO	Name	BB_Blocked	PRU	ShepV1-FN	GPS-Capelet	nRF52	ShepV2-FN	copy-pin
P8_07	66	TIMER4			xxx	GPS_PPS		(GPS)	P8_07
P8_08	67	TIMER7			xxx	GPS_nRESET		(GPS)	P8_08
P8_09	69	TIMER5			xxx	GPS_nSAFEBOOT		(GPS)	P8_09
P8_10	68	TIMER6						WD_ACK	P8_10
P8_11	45			pr1_pru0_pru_r30_15	Debug Pin0 PRU0			pru0_dbg_Led1	P8_11
P8_12	44			pr1_pru0_pru_r30_14	Led1 PRU0			pru0_dbg_Led0	P8_12
P8_13	23				EN Harvester			EN_Shepherd	P8_13
P8_14	26							target_GPIO0	P8_14
P8_15	47			pr1_pru0_pru_r31_15				target_GPIO3	P8_15
P8_16	46			pr1_pru0_pru_r31_14				target_GPIO2	P8_16
P8_17	27							target_GPIO1	P8_17
P8_18	65							BTN Sense	P8_18
P8_19	22	EHRPWM2A						BTN Led	P8_19
P8_26	61							target_GPIO4	P8_26
P8_27	86		HDMI	pr1_pru1_pru_r31_08				pru1_target_batOK	P8_27
P8_28	88		HDMI	pr1_pru1_pru_r31_10	ADC RST / PDN			pru1_dbg_Led0	P8_28
P8_29	87		HDMI	pr1_pru1_pru_r31_09				pru1_target_GPIO4	P8_29
P8_30	89		HDMI	pr1_pru1_pru_r31_11	Led2 User Space			pru1_dbg_Led1	P8_30
P8_39	76		HDMI	pr1_pru1_pru_r31_06				pru1_target_swd_clk	P8_39
P8_40	77		HDMI	pr1_pru1_pru_r31_07				pru1_target_swd_io	P8_40
P8_41	74		HDMI	pr1_pru1_pru_r31_04	Debug Pin0 PRU1			pru1_target_uatx	P8_41
P8_42	75		HDMI	pr1_pru1_pru_r31_05	Debug Pin1 PRU1			pru1_target_uarx	P8_42
P8_43	72		HDMI	pr1_pru1_pru_r31_02	Target GPIO2		GPIO2	pru1_target_GPIO2	P8_43
P8_44	73		HDMI	pr1_pru1_pru_r31_03	Target GPIO3		GPIO3	pru1_target_GPIO3	P8_44
P8_45	70		HDMI	pr1_pru1_pru_r31_00	Target GPIO0		GPIO0	pru1_target_GPIO0	P8_45
P8_46	71		HDMI	pr1_pru1_pru_r31_01	Target GPIO1		GPIO1	pru1_target_GPIO1	P8_46
P9_11	30	UART4_RXD			EN MPPT			target_io_SEL	P9_11
P9_12	60				EN V Analog			target_io_EN	P9_12
P9_13	31	UART4_TXD						target_Pwr_SEL	P9_13
P9_17	5	I2C1_SCL			I2C1_SCL			target_swd_clk	P9_17
P9_18	4	I2C1_SDA			I2C1_SDA			target_swd_io	P9_18
P9_19	13	I2C2_SCL	I2C		I2C2_SCL			Mem_SCL	P9_19
P9_20	12	I2C2_SDA	I2C		I2C2_SDA			Mem_SDA	P9_20
P9_21	3	UART2_TXD			xxx	UART2_TX		(GPS_UART)	P9_21
P9_22	2	UART2_RXD			xxx	UART2_RX		(GPS_UART)	P9_22
P9_23	49							Mem_WP	P9_23
P9_24	15	UART1_TXD		pr1_pru0_pru_r31_16	UART_TX		UART_TX	target_uart_tx	P9_24
P9_25	117		mcasp	pr1_pru0_pru_r31_07	SPI CS ADC			pru/SPI_CS_ADC2_REC	P9_25
P9_26	14	UART1_RXD		pr1_pru1_pru_r31_16	UART_RX		UART_RX	target_uart_rx	P9_26
P9_27	115		mcasp	pr1_pru0_pru_r31_05	SPI MISO			pru/SPI_CS_DAC_REC	P9_27
P9_28	113	SPI1_CS0	mcasp	pr1_pru0_pru_r31_03	SPI SCLK			pru/SPI_CS_DAC_EMU	P9_28
P9_29	111	SPI1_DO	mcasp	pr1_pru0_pru_r31_01	Select LOAD PRU			pru/SPI_MOSI	P9_29
P9_30	112	SPI1_D1	mcasp	pr1_pru0_pru_r31_02	SPI MOSI			pru/SPI_MISO	P9_30
P9_31	110	SPI1_SCLK	mcasp	pr1_pru0_pru_r31_00	SPI CS DAC			pru/SPI_SCK	P9_31
P9_41A	20	CLKOUT2			SWDIO		SWDIO	(shared pin)	P9_41A
P9_41B	116			pr1_pru0_pru_r31_06				pru/SPI_CS_ADC1_REC	P9_41B
P9_42A	7	SPI1_CS1	mcasp		SWDCLK		SWDCLK	(shared pin)	P9_42A
P9_42B	114		mcasp	pr1_pru0_pru_r31_04				pru/SPI_CS_ADC_EMU	P9_42B