Vector Addition Lab

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May 15, 2014

In this lab, we will be experimenting with two vector programming interfaces to utilize vector ALU's of an Intel compatible processor. You will be writing a method *addvec* in **addvec.c** which will perform a component-wise addition of vectors A and B of length l and store the result in vector of equal length C. This assignment must be completed in two different ways:

1. Using the SSE instruction intrinsics provided for your SSE capable system via **xmmintrin.h**

2. Using the gcc vectors interface provided by the GNU compiler.