

**CPU\_WR\_ADDR – MEM\_PROT\_BASE**



**Address Offset (11 – 0)**



**Block Num. (11 – 3)**

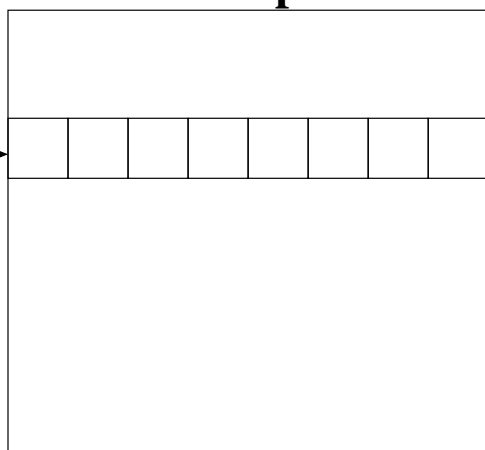
**Offset (2 – 0)**



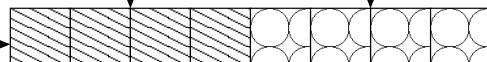
**Memmap Offset (11 – 5)**



**Memmap Table**



**1**



**Memmap Records**

**MEM MAP BASE**