COVER PAGE:
"RUNTIME OPTIMIZATION OF SYSTEM UTILITY WITH VARIABLE HARDWARE"

This work is in submission to the TECS **Special Issue on Parallel Programming and Run-Time Management for Embedded Systems**

Work presented in this paper is a followup to [Wanner et al. 2012], but all results and findings presented herein are new and have not been submitted to other conferences or journals

REFERENCE:

L. Wanner, C. Apte, R. Balani, P. Gupta, and M. Srivastava. 2012. Hardware Variability-Aware Duty Cycling for Embedded Sensors. IEEE T. Very Large Scale Integration (VLSI) Systems 21, 6 (2012), 10001012.