

1-INITIALIZE MASTER

```
void spi-init_master (void) \frac{1}{2}

DDRB = (1 << 5) | (1 << 3); // MOSI & SCK -> out

SPCR = (1 << SPE) | (1 << MSTR) | (1 << SPRO);

SPCR |= (1 << SPIE);

Sei(); // Enable Global Interrupts
```

2- INITIALIZE SLAVE

3- JEND & RECEIVE DATA FOR BOTH MASTER & SLAVE

```
unsigned char spi_transreceiver (unsigned char data) {

SPDR = data;

/* Wait until transmission complete */

while (! (SPSR & (1 << SPIF))); when SPSR, SPIF = 0

Ly true, danga

return (SPDR);

When SPSR, SPIF = 1

Ly false, dangated Gik.
```