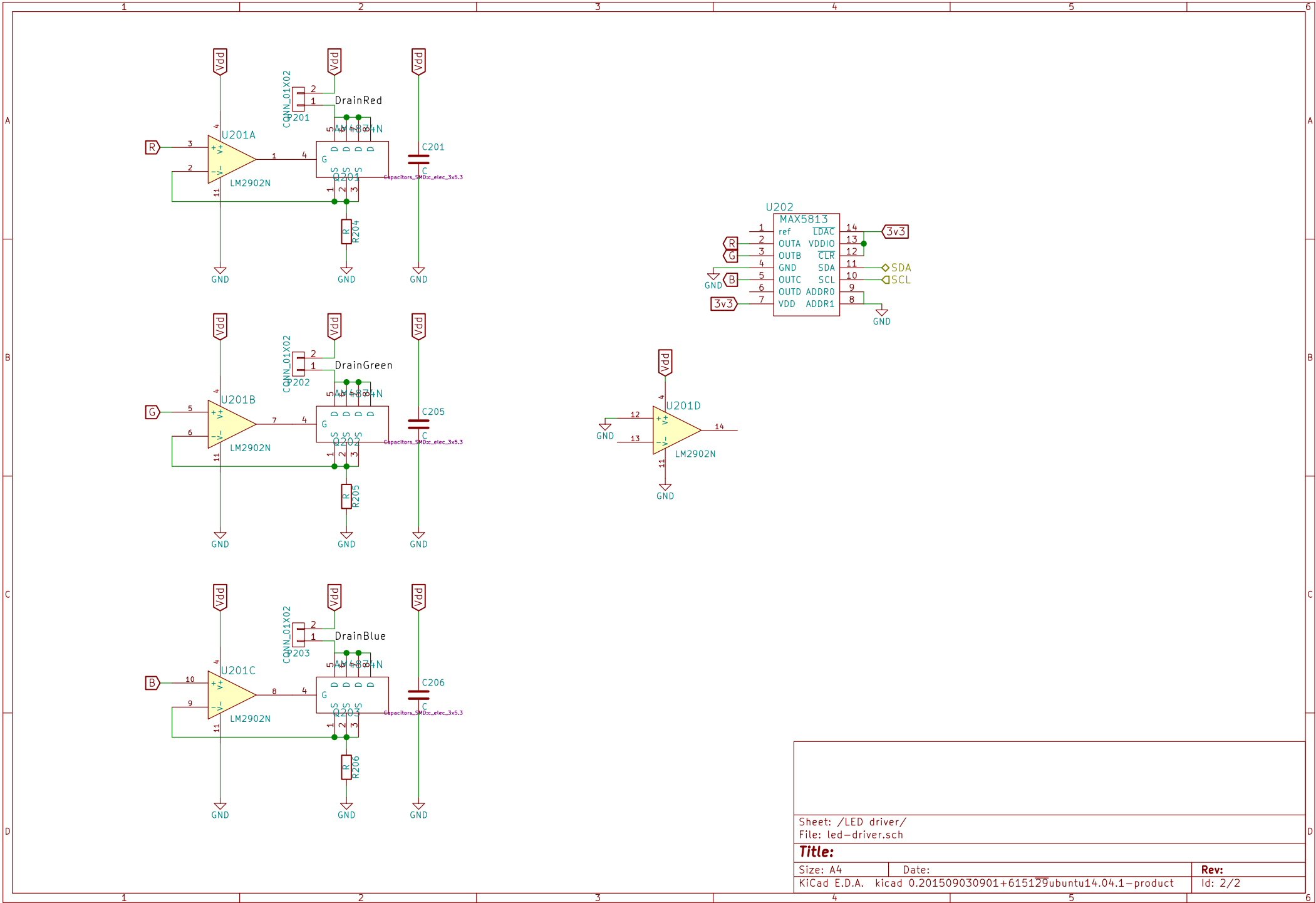


The schematic shows an RPi shield circuit. It features a voltage regulator (U101: LM317AEMP) that takes a 5V input and provides a 3.3V output (Vdd). The output is filtered by capacitor C101. An LED (D101) is connected to the 3.3V line through a resistor (R101). The LED is also connected to the output of an LED driver (U201: LED_DRIVER). The LED driver is powered by a 5V source and has its output connected to the LED. A 40-pin header (P102) is used for interfacing with the RPi, with pins 1-4 connected to 3.3V, 5V, 5V, and GND respectively. Other pins are connected to GND or specific signals like RESET, MOSI, MISO, and SCK. A sub-component 'led_driver' is shown in a separate window, connected to the LED driver's output.

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