Group 4

Realization of artificial vision devices and utilization of silicon technology for recording and stimulation

4-1   Analog vision chip for early vision
   Hiroo Yonezu *(Toyohashi University of Tech.)*

4-2   Analog-digital hybrid vision chips for motion processing systems based on direction-selective neural networks
   Yoshihito Amemiya *(Hokkaido University)*

4-3   Fabrication of micro 3D sensor array with ultra -small Si wire electrodes and applications to measurement of retina cell potentials
   Makoto Ishida *(Toyohashi University of Tech.)*