Papilio_Audio_Modular_top Project Status					
Project File:	Papilio_Audio_Modular.xise	Parser Errors:	No Errors		
Module Name:	Papilio_Audio_Modular_top	Implementation State:	New		
Target Device:	xc6slx9-2tqg144	• Errors:			
<b>Product Version:</b>	ISE 14.7	• Warnings:			
Design Goal:	Balanced	• Routing Results:			
Design Strategy:	Xilinx Default (unlocked)	• Timing Constraints:			
<b>Environment:</b>		• Final Timing Score:			

Detailed Reports					[-]	
Report Name	Status	Generated	Errors	Warnings	Infos	
Synthesis Report						
Translation Report						
Map Report						
Place and Route Report						
Power Report						
Post-PAR Static Timing Report						
Bitgen Report						

Secondary Reports				
Report Name	Status	Generated		
ISIM Simulator Log	Out of Date	Sun Apr 17 16:00:18 2016		

**Date Generated:** 04/22/2016 - 01:10:17