3 pin header:

1 - “dummy load switch”

2 - “primary no sunlight”

3 - “secondary no sunlight”

4 pin header:

1 - “dummy load top”

2 - “dummy load bottom”

3 - “battery left”

4 - “battery right”

7 pin header:

1 - “solar panel 1”

2 - “solar panel 2”

3 - Battery

4 - Load

5 - GND

6 - GND

7 - GND

Q1 - leftmost diode on schematic

Q2 - middle diode on schematic

Q3 - rightmost diode on schematic

JP1: diode after solar panel test point

JP2: between resistors near Q5

JP3: pinout 6 on DC/DC

JP4: pinout 1/ between diode and inductor

JP5: VIN regulator

JP6: VOUT regulator

JP7: Potentiometer

JP8: battery