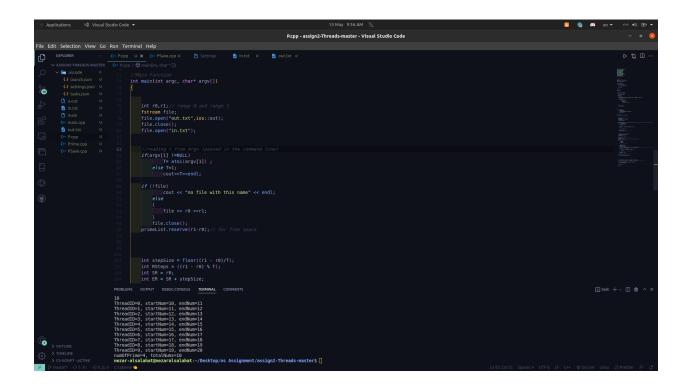
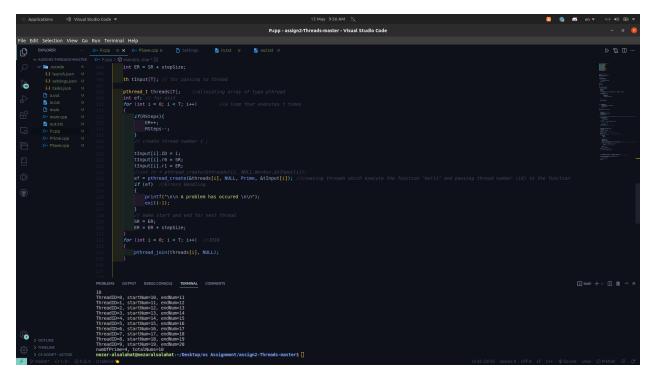
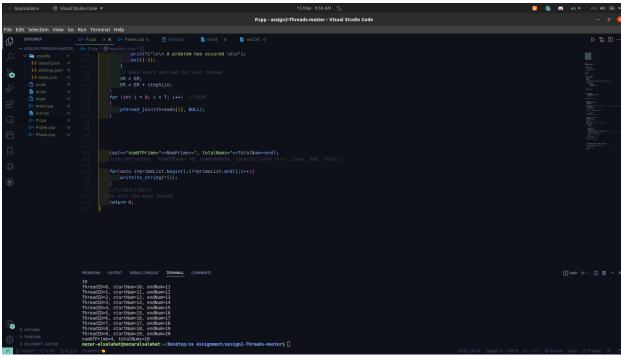
Assignment: Threading

By:
Nezar Al-Salahat
132827
Raghad Ghalib
137144

Task 1:



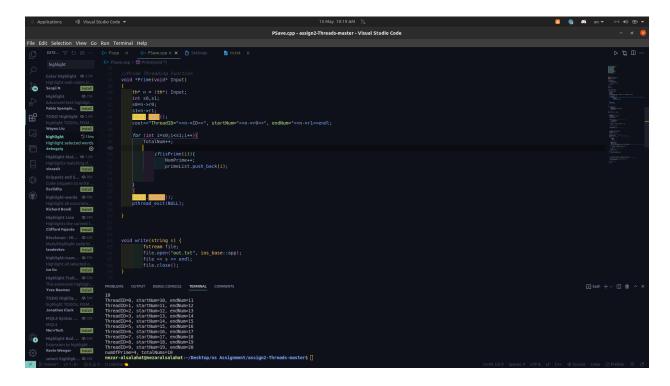




Task 2:

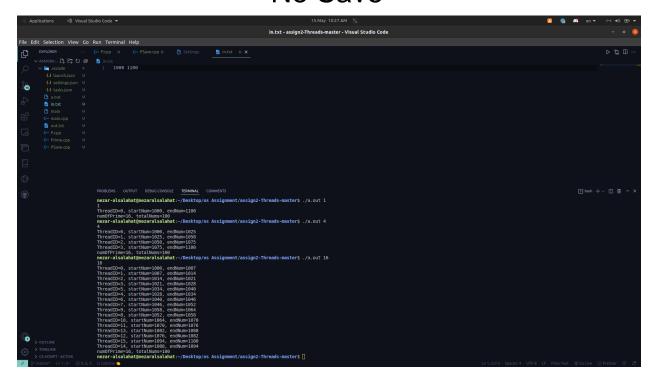
```
| Separation | Company | C
```

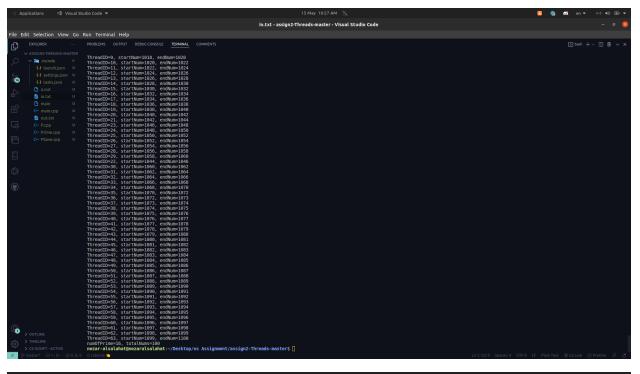
Task3:

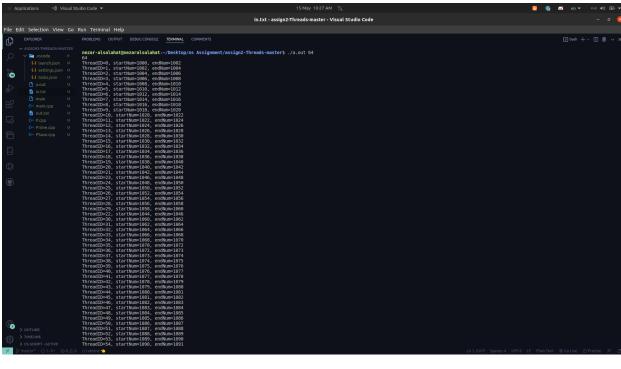


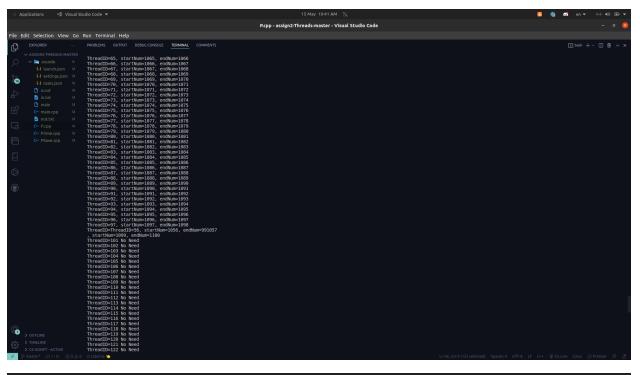
Lock1: we used this lock on critical section, to save the global variables NumPrime, TotalNum And for saving output style from racing between threads, because these variables are shared between all the threads and all the threads (more than 1) write on them.

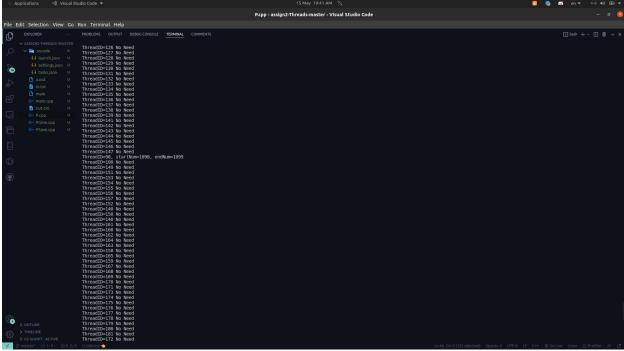
Task 4: No Save

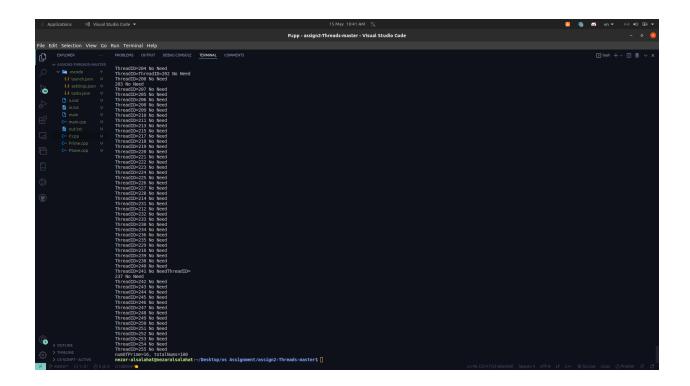




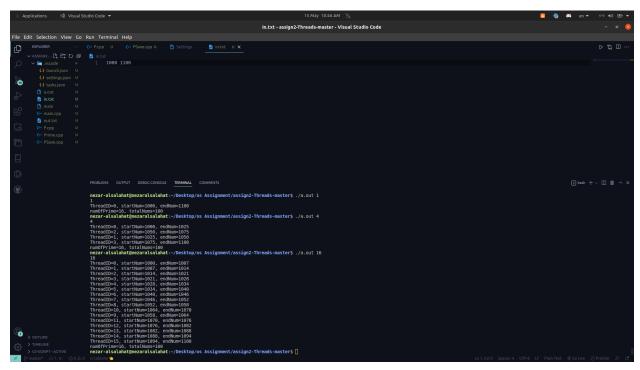


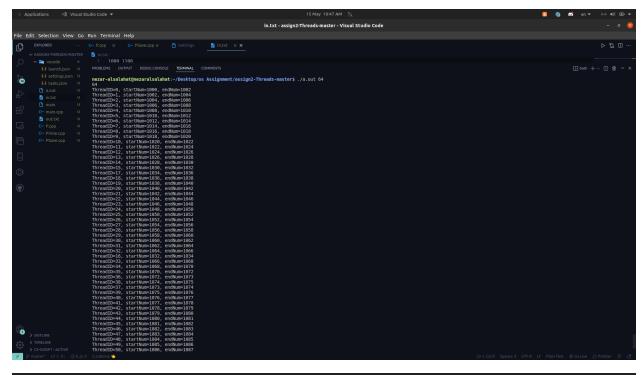


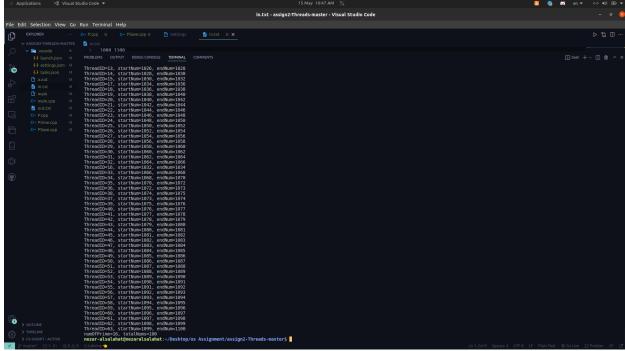


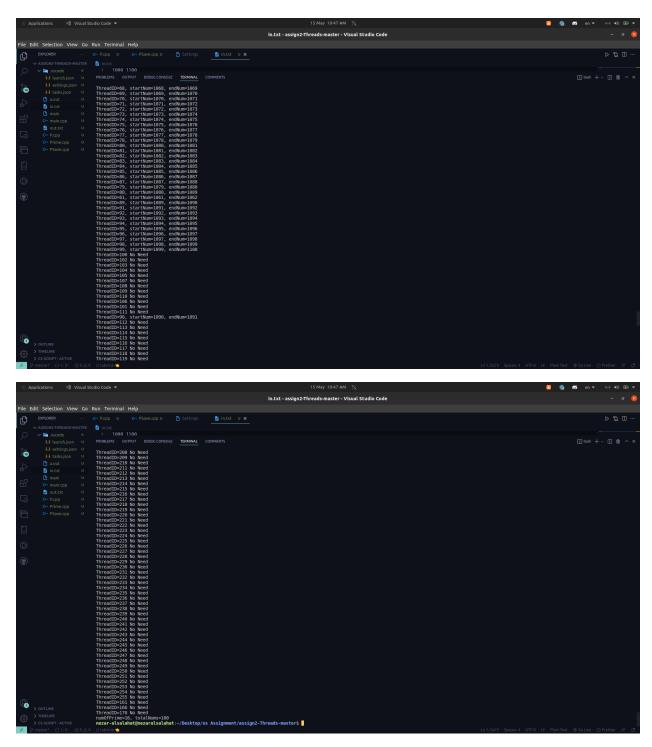


Save









**note: 1024 my terminal can't show all

Task 5:

	Test1		Test2		Test3	
T=1	real	0m0.010s	real	0m0.006s	real	0m0.009s
	user	0m0.006s	user	0m0.001s	user	0m0.004s
	sys	0m0.004s	sys	0m0.006s	sys	0m0.005s
T=4	real	0m0.010s	real	0m0.010s	real	0m0.010s
	user	0m0.003s	user	0m0.002s	user	0m0.007s
	sys	0m0.008s	sys	0m0.010s	sys	0m0.004s
T=16	real	0m0.011s	real	Om0.009s	real	0m0.010s
	user	0m0.009s	user	Om0.006s	user	0m0.007s
	sys	0m0.006s	sys	Om0.006s	sys	0m0.006s
T=64	real	0m0.008s	real	0m0.017s	real	0m0.010s
	user	0m0.004s	user	0m0.007s	user	0m0.003s
	sys	0m0.007s	sys	0m0.019s	sys	0m0.009s
T=256	real	0m0.042s	real	0m0.021s	real	0m0.016s
	user	0m0.018s	user	0m0.001s	user	0m0.008s
	sys	0m0.039s	sys	0m0.028s	sys	0m0.015s
T=1024	real	0m0.051s	real	0m0.071s	real	Om0.084s
	user	0m0.030s	user	0m0.023s	user	Om0.022s
	sys	0m0.043s	sys	0m0.082s	sys	Om0.087s

Conclusion:

our machine should have between 1 and 4 cores with 1 hyper-thread each or something like these numbers,

in our tests we noticed that whenever we use more than 4 threads we don't see any improvement on the execution time, and sometimes the time increases instead(the law of diminishing)

```
nezar-alsalahat@nezaralsalahat:~/Desktop/os Assignment/assign2-Threads-master$ lscpu
Architecture:
                                   x86 64
CPU op-mode(s):
                                   32-bit, 64-bit
                                   Little Endian
Byte Order:
                                   39 bits physical, 48 bits virtual
Address sizes:
CPU(s):
                                   0-7
On-line CPU(s) list:
Thread(s) per core:
Core(s) per socket:
Socket(s):
NUMA node(s):
Vendor ID:
                                   GenuineIntel
CPU family:
Model:
Model name:
                                   Intel(R) Core(TM) i7-8550U CPU @ 1.80GHz
Stepping:
                                   10
CPU MHz:
CPU max MHz:
                                   2000.000
4000.0000
                                   400.0000
CPU min MHz:
BogoMIPS:
                                   3999.93
Virtualization:
Lld cache:
                                   128 KiB
Lli cache:
                                   128 KiB
L2 cache:
                                   1 MiB
_3 cache:
                                   8 MiB
```