

Fernandez, Nathaniel Formento  
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#### Summary of my profile:

- Demonstrated success in all of the projects or assignment being given
- Quickly learn new technologies and adapt to a fast-paced development environment.
- Have a Certificate in Team Enhancement and People Handling Training
- Have a software project management using agile scrum, waterfall, and other software management methodology
- Have a Training Certificate in Problem Solving & Decision Making through Critical Thinking
- Exposed in Firmware Development using C/C++/Assembly using different CPU's from 8bit to 32bit
- Exposed in windows device driver development using wdk
- Exposed in Linux Device Driver using different Linux Kernel, OpenWRT and GIT Repository
- Exposed in High Level Programming Using C++ and Java for front/back end High Level Programming Languages
- Exposed in Android Studio using Kotlin for Payment System

#### Work History

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03.2022 – Present

##### **Senior Software Engineer**

###### **Igen Technologies (Makati)**

- Handle Payment System Using Android Studio and Kotlin

04.2020 – 02.2022

##### **Free Lancer**

- Flexxon Singapore – Front End using QT and Visual studio for Back End SSD Software
- Company in Dubai doing CopyA/CopyB project
- Upwork Projects Using:
  - OpenWRT
  - Json
  - MQTT

03.2013 – 03.2020

##### **ADVANCED SOFTWARE GROUP, SR MANAGER (HANDLED BIOS, IOC AND SOFTWARE TEAM)**

###### **Bitmicro Network International Inc.**

- Managed Firmware and Software For the following projects
- ULPA2 - A low cost and industrial grade to ethernet cover Nvme Device
- Acumen v1 - A scalable Secure Network Storage node. reduced feature
- Acumen v2 - A Scalable Secure Network Storage Node, Full blown features
- Achievement: all of the projects have been achieved and have a customers

04.2008 – 02.2013      ADVANCED SOFTWARE GROUP - MANAGER (HANDLED BIOS, IOC, AND SOFTWARE TEAM)

ADVANCED SOFTWARE GROUP - OFFICER I (HANDLED BIOS TEAM)

**Bitmicro Network International Inc.**

- Managed PCIe 2.0 Project from firmware, Software and Linux Device Driver
- Managed SAS/SATA Project Across Engineering Execution
- Architect Caching Software for agnostic and non-agnostic SSD Drive
- Developed Linux Device Driver for PCIe SSD
- Developed Windows Driver For PCIe SSD
- Developed EDMx using Java
- Developed VmWare Device Driver For Virtualized PCIe Products
- Developed Windows Device Driver for XP, 2003, Win7 and 2008 using WDK ver 7
- Developed SAS/SATA II using PPC405Ex, 32bit Processor
- PATA/SATA EDisk Products Development and Management
- Achievement: All the projects have been achieved as agreed.

09. 2004 – 04. 2007      Sr Project Lead for BIOS Team  
Head Project Lead for BIOS Team  
Sr Technical Lead

**Bitmicro Network International Inc.**

- Team Leader for BIOS Team in Advanced Software Group
- EDisk Tester Manager - Software Development using Java
- Created RTOS mqx-based code for multi-processor system using ARC700
- Created several bios-based code for PATA, /SATA/FC/U320 products
- Lead the Controller-Board-Tester project for Edisk Products
- Support Sanitize Feature for Edisk Products like PATA/FC and U320
- Achievement: All the projects have been achieved as agreed

11. 2002 – 08. 2004      Project Leader/Design Engineer

**HUGLE TECHNOLOGIES INC**

- Designed & Developed Bluetooth Hug and Dongle using SECS II protocol with ARM9 and nucleus RTOS
- Designed & Developed Bluetooth Hub for Ionizer System
- Designed & Developed Abstraction Layer for bluetooth protocol
- Designed & Developed bluetooth hub with can controller interface
- Designed & Developed contact and contactless smartcard system using atmega8
- Achievement: All the projects have been achieved as agreed

04. 2000 – 10. 2002

Project Leader/Design Engineer

**PRODUCTION MANAGER**

- Handled Production Management/Activities
- Achievement: Daily target have been achieved as agreed

**DESIGN ENGINEER (LOCATED IN SOUTH KOREA)**

- Designed & Developed Control Board for RF Team
- Responsible for IMT2000 PCS Control Board using 87c550-cpu and Visual C++ 6.0
- Responsible for Monitor Board for Satellite Control Board using 80c320-cpu and actel fpga (hdl coding, atmel PLD22v10m uart 232/485 program)
- IR System using visual c++ 6.0 for rs232 and 80c320-cpu
- Achievement: All the projects have been achieved as agreed

08. 1998 – 03. 2000

Design Engineer

**MITECH BTHI**

- Designed & Developed industrial and commercial system for automation
- Automation design using state of the art micro-controller system, hardware, (orcad), firmware (assembly, c language) and software (borland c, visual c++)
- Function as back-up network administrator of the office
- Responsible for Manila Peninsula and SM keppel un-manned carpark system
- Responsible for mandarin oriental hotel carparking system and customization of ayala, megamall, robinson and other carparking system her in metro manila
- Exposed in time keeping system using z80 and 68hc05
- Provide solution to replace obsolete chips with the existing products
- Provide technical support to the engineer in the field
- Achievement: All the projects have been achieved as agreed

**SERVICE ENGINEER (08.96 - 08.98)**

- Provide solution on Mitech Products (computerized parking)
- On-site hardware/firmware/software trouble shooting
- Provide programs to make the product more convenient
- Assigned to make quotation/estimates to existing customers
- Achievement: Daily target have been achieved as agreed

**EDUCATION**

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03.1991 – 03.1996

B.S. In Computer Engineering AMA Computer College, Philippines