SiNG Toolkit

universal FPGA based signal generator / analyzer

block diagram:

DDR3

12 bit DAC (min 60MSPS)

12 bit ADC (min 60MSPS)

Xilinx
Spartan-6

FT601Q
USB 3.0 Bridge

Fast multi-pin
parallel interface
+
Common interfaces
like SPI, I2C

HMI

Display

Buttons

Audio output jack (ΣΔ)

Audio input + PTT

*maybe

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