8253 TEST PROGRAM

This program sets counter Ø to square wave mode, divide by 1000 decimal. Counter Ø gate must be tied high and the input should be tied to the 2Mhz clock. This will give an output of 2000 hz.

```
0000    3E      MVIA        ;Set counter Ø to square wave, binary

0001    36                  ;and prepare to load.

0002    D3      OUT         ;Send it to control port

0003    6F

0004    3E      MVIA        ;Send divide by 1000 to counter

0005    E8                  ;Low order byte

0006    D3      OUT

0007    6C

0008    3E      MVIA        ;Send the rest

0009    03                  ;high order byte

000A    D3      OUT

000B    6C

000C    C3      JMP         ;Wait here until reset

000D    0C

000E    00
```