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SUBJECT MCB Memory + I/O Map

SHEET NO. 1 OF 2
JOB NO.

MEMORY MAP

ØØØØ to ØFFF	EPROM 1 IC-21
1ØØØ to 1FFF	EPROM 2 IC-22
2ØØØ to 2FFF	EPROM 3 IC-23
3ØØØ to 33FF	RAM 1 IC-24 & IC-26
34ØØ to 37FF	RAM 2 IC-25 & IC-27
38ØØ to 3BFF	Mirror of RAM 1
3CØØ to 3FFF	Mirror of RAM 2
4ØØØ to DE5F	FREE SPACE

DE6Ø	8255-1, IC-14	Port A
DE61	↓	Port B
DE62	↓	Port C
DE63	↓	Control Port
DE64	8255-2, IC-15	Port A
DE65	↓	Port B
DE66	↓	Port C
DE67	↓	Control Port
DE68	8255-3, IC-16	Port A
DE69	↓	Port B
DE6A	↓	Port C
DE6B	↓	Control Port
DE6C	8253 IC-18	Counter Ø
DE6D	↓	Counter 1
DE6E	↓	Counter 2
DE6F	↓	Control Word
DE7Ø	9513 IC-19	Data Port
DE71		Control Port
DE72		Mirror Data Port
DE73		Mirror Control Port

MEMORY MAP

DE74	SIO IC-2	Port A Data
DE75	↓	Port B Data
DE76	↓	Port A Control
DE77	↓	Port B Control
DE78	8279 IC-20	Data Port
DE79	↓	Control Port
DE7A	↓	Mirror Data Port
DE7B	↓	Mirror Control Port
DE7C	9511 IC-17	Data Port
DE7D	↓	Control Port
DE7E	↓	Mirror Data Port
DE7F	↓	Mirror Control Port

DE80 to DF5F	FREE SPACE
DF60 to DF7F	Mirror of DE60 to DE7F (I/O Ports)
DF80 to FFFF	FREE SPACE

I/O Map

The I/O Ports Map from 60 to 7F. Their identity is identical to DE60 to DE7F.

Examples:

8255-1 Port A can be accessed in memory at DE60. in I/O it is Port 60

Serial I/O (SIO) Port A Data is in memory at DE74 in I/O it is Port 74

Note: the timing of some chips may prevent them from being used in the memory mapped mode. This may be overcome by using lower clock speeds.