

1. Description

1.1. Project

Project Name	TFT
Board Name	custom
Generated with:	STM32CubeMX 6.12.0
Date	11/17/2024

1.2. MCU

MCU Series	STM32F7
MCU Line	STM32F7x6
MCU name	STM32F746BETx
MCU Package	LQFP208
MCU Pin number	208

1.3. Core(s) information

Core(s)	Arm Cortex-M7

2. Pinout Configuration



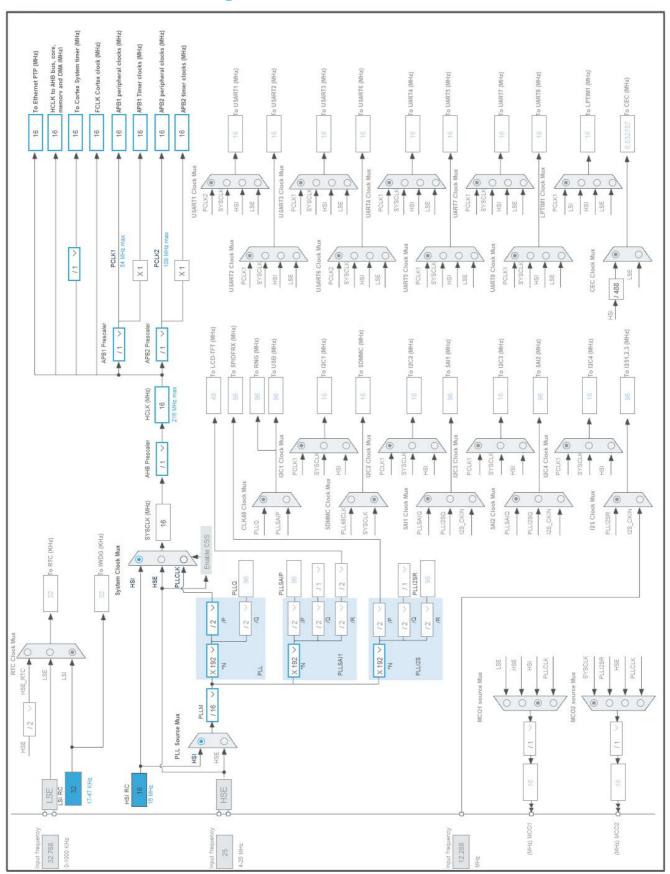
3. Pins Configuration

Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP208	(function after		Function(s)	
	reset)			
6	VBAT	Power		
14	VSS	Power		
15	VDD	Power		
25	VSS	Power		
26	VDD	Power		
34	NRST	Reset		
35	PC0 *	I/O	GPIO_Output	RESX
36	PC1 *	I/O	GPIO_Output	CSX
37	PC2 *	I/O	GPIO_Output	WRX_DCX
39	VDD	Power		
40	VSSA	Power		
41	VREF+	Power		
42	VDDA	Power		
43	PA0/WKUP *	I/O	GPIO_Input	BTN TURN PAGES
44	PA1 *	I/O	GPIO_Input	BTN START/RESET
51	VSS	Power		
52	VDD	Power		
54	PA5	I/O	SPI1_SCK	
55	PA6	I/O	SPI1_MISO	
56	PA7	I/O	SPI1_MOSI	
59	VDD	Power		
60	VSS	Power		
72	VSS	Power		
73	VDD	Power		
82	VSS	Power		
83	VDD	Power		
92	VCAP_1	Power		
93	VSS	Power		
94	VDD	Power		
103	VDD	Power		
114	VSS	Power		
115	VDD	Power		
124	VDD	Power		
125	VSS	Power		
136	VSS	Power		
137	VDDUSB	Power		

Pin Number LQFP208	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
148	VCAP_2	Power		
149	VSS	Power		
150	VDD	Power		
158	VDD	Power		
170	VSS	Power		
171	VDD	Power		
184	VSS	Power		
185	VDD	Power		
197	воото	Boot		
202	VSS	Power		
203	PDR_ON	Reset		
204	VDD	Power		

^{*} The pin is affected with an I/O function

4. Clock Tree Configuration



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1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32F7
Line	STM32F7x6
MCU	STM32F746BETx
Datasheet	DS10916_Rev4

1.2. Parameter Selection

Temperature	25
Vdd	3.3

1.3. Battery Selection

Battery	Alkaline(9V)
Capacity	625.0 mAh
Self Discharge	0.3 %/month
Nominal Voltage	9.0 V
Max Cont Current	200.0 mA
Max Pulse Current	0.0 mA
Cells in series	1
Cells in parallel	1

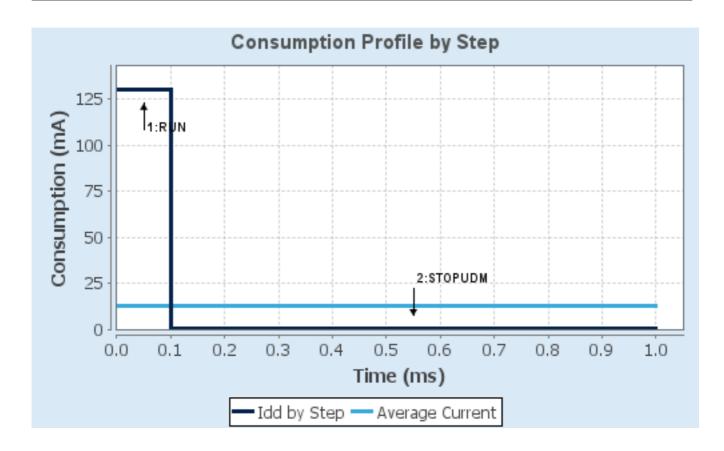
1.4. Sequence

Ston	Cton4	Ston 2
Step	Step1	Step2
Mode	RUN	STOP UDM (Under Drive)
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	Scale1-High	No Scale
Fetch Type	ITCM/FLASH/REGON	n/a
CPU Frequency	216 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator LP Flash-PwrDwn
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	130 mA	100 μΑ
Duration	0.1 ms	0.9 ms
DMIPS	462.0	0.0
Ta Max	96.85	104.99
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	13.09 mA
Battery Life	1 day, 23 hours	Average DMIPS	462.24005
			DMIPS

1.6. Chart



2. Software Project

2.1. Project Settings

Name	Value
Project Name	TFT
Project Folder	C:\Users\khong\hcmut\EMBEDDED C\MY_WORKSPACE\HOST\TFT
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F7 V1.17.2
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_SPI1_Init	SPI1

3. Peripherals and Middlewares Configuration

3.1. CORTEX_M7

3.1.1. Parameter Settings:

Speculation default mode Settings:

Speculation default mode Enabled

Cortex Interface Settings:

Flash Interface AXI Interface
ART ACCELERATOR Disabled
Instruction Prefetch Disabled
CPU ICache Disabled
CPU DCache Disabled

Cortex Memory Protection Unit Control Settings:

MPU Control Mode Background Region Privileged accesses only + MPU Disabled during hard fault,

NMI and FAULTMASK handlers

Cortex Memory Protection Unit Region 0 Settings:

MPU Region Enabled

MPU Region Base Address 0x0 *

MPU Region Size 4GB

MPU SubRegion Disable 0x87 *

MPU TEX field level level 0

MPU Access Permission ALL ACCESS NOT PERMITTED

MPU Instruction AccessDISABLEMPU Shareability PermissionENABLEMPU Cacheable PermissionDISABLEMPU Bufferable PermissionDISABLE

Cortex Memory Protection Unit Region 1 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 2 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 3 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 4 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 5 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 6 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 7 Settings:

MPU Region Disabled

3.2. RCC

3.2.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3

Flash Latency(WS) 0 WS (1 CPU cycle)

RCC Parameters:

HSI Calibration Value 16

TIM Prescaler Selection Disabled
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

Power Parameters:

Power Over Drive Disabled

Power Regulator Voltage Scale Power Regulator Voltage Scale 3

3.3. SPI1

Mode: Full-Duplex Master

3.3.1. Parameter Settings:

Basic Parameters:

Frame Format Motorola

Data Size 4 Bits

First Bit MSB First

Clock Parameters:

Prescaler (for Baud Rate) 2

Baud Rate 8000000
Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

Advanced Parameters:

CRC Calculation Disabled

NSSP Mode Enabled

NSS Signal Type Software

3.4. SYS

Timebase Source: SysTick

* User modified value

4. System Configuration

4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA6	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA7	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
GPIO	PC0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	RESX
	PC1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	CSX
	PC2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	WRX_DCX
	PA0/WKUP	GPIO_Input	Input mode	Pull-up *	n/a	BTN TURN PAGES
	PA1	GPIO_Input	Input mode	Pull-up *	n/a	BTN START/RESET

4.2. DMA configuration

nothing configured in DMA service

4.3. NVIC configuration

4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Pre-fetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	15	0	
PVD interrupt through EXTI line 16	unused			
Flash global interrupt	unused			
RCC global interrupt	unused			
SPI1 global interrupt		unused		
FPU global interrupt	unused			

4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init	Generate IRQ	Call HAL handler
	sequence ordering	handler	
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

* User modified value

5. System Views

5.1. Category view

5.1.1. Current

			Middleware			
System Core	Analog	Timers	Connectivity	Multimedia	Security	Computing
CORTEX_M7 ♥			SPI1 ♥			
DMA						
GPIO ♥						
NVIC 🕏						
RCC ♥						
sys ♥						

6. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl_model/stm32f7_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis_model/stm32f7_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f7-svd.zip

Description

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stm8_embedded_software_solutions.pdf

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