

```
class VALUVVMIQS<bits<5> funct5, bits<3> funct3, string opcodestr>  
  : RVInstVVMIS<funct5, opv, (outs VR:$vd), (ins VR:$vs2, VR:$vs1),  
    opcodestr, "$vd, $vs2, $vs1"> {}
```

```
def VADD_VV : VALUVVMIQS<0b00001, OPVV, "vadd.w">  
  Sched<[WriteVIALUV, ReadVIALUV, ReadVIALUV]>;
```