

	31	27	26	25	24	20	19	15	14	12	11	7	6	0
RVInstVVMIQS	funct5	size	vs2	vs1	funct3	vd	opcode							
RVInstIVIMIQS	funct5	size	imm5	vs1	funct3	vd	opcode							

	31	27	26	25	24	23	22	15	14	13	12	11	7	6	0
RVInstIVI8MIQS	funct5	size	funct2	imm8	sh	funct2	vd	opcode							

	31	28	27					15	14	12	11	7	6	0
RVInstIVI13MIQS	0011	imm13						010	vd	0001011				