```
# %bb.0:
# <MCInst #10797 ADDW
  # <MCOperand Reg:50>
  # <MCOperand Reg:50>
  # <MCOperand Reg:51>>
# <MCInst #11108 JALR
  # <MCOperand Reg:40>
  # <MCOperand Reg:41>
  # <MCOperand Imm:0>>
```