

```
1 class VALUVV_VEx <bits<3> funct3, string opcodestr>
2   : RVInstVV_VEx <funct3, (outs VR:$vd), (ins VR:$vs2, VR:$vs1),
3     opcodestr, "$vd, $vs2, $vs1"> {
4 }
5
6 def VADD_VV : VALUVV_VEx <0b000, "vadd.w">,
7   Sched<[WriteVIALUV, ReadVIALUV, ReadVIALUV]>;
8
9 def VSUB_VV : VALUVV_VEx <0b001, "vsub.w">,
10   Sched<[WriteVIALUV, ReadVIALUV, ReadVIALUV]>;
```