```
class VALUVVMIQS<bits<3> funct3, string opcodestr>
  : RVInstVVMIQS<funct3, (outs VR:$vd), (ins VR:$vs2, VR:$vs1),
        opcodestr, "$vd, $vs2, $vs1"> {
}

def VADD_VV : VALUVVMIQS<0b000, "vadd.w">,
        Sched<[WriteVIALUV, ReadVIALUV, ReadVIALUV]>;

def VSUB_VV : VALUVVMIQS<0b001, "vsub.w">,
        Sched<[WriteVIALUV, ReadVIALUV, ReadVIALUV]>;
```