```
class RVInstVVMIQS<bits<3> funct3, dag outs, dag ins, string opcodestr, string argstr>
 : RVInst<outs, ins, opcodestr, argstr, [], InstFormatR> {
bits<5> vs2:
bits<5> vs1:
bits<5> vd;
let Inst{31-27} = 0b00001;
let Inst{26-25} = 0b10;
let Inst{24-20} = vs2;
let Inst{19-15} = vs1;
let Inst{14-12} = funct3;
let Inst{11-7} = vd;
let Inst{6-0} = 0b0001011;
```