1.

"Big-endian" refers to an ordering of bits in which the most significant bit comes first.

Bit order will be: Bit2 – Bit1 – Bit0.

2-3.

A diagram of a circuit

Description automatically generated

4-6.

A diagram of a computer

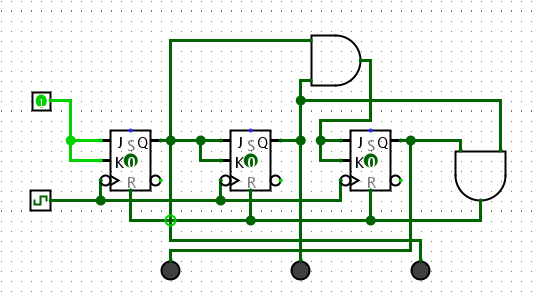
Description automatically generated

7-8.

A diagram of a circuit

Description automatically generated

9.



10.

A diagram of a machine

Description automatically generated

10.2.

Managing issues like momentary illegal states is essential because it ensures circuit reliability, data integrity, predictability, compatibility with other systems, consistency, and prevents glitches.

11.

A diagram of a circuit

Description automatically generated