



王炫然

Xuan-Ran, Wang

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Skills

- Wafer chip probing
- Image processing
- Algorithm development
- Optical System Design
and Alignment

Software

MATLAB 

ImageJ(FIJI) 

JAVA 

python 



Adviser Professor

詹明哲

mcchan@nycu.edu.tw

Course Record

CNN, Transformer & Generative AI

Applied deep learning models to underwater image enhancement tasks, focusing on deblurring techniques--gaining hands-on experience in pairing data and using adversarial loss for quality refinement.

Computer Graphics 3D(JAVA)

Implemented core 3D rendering concepts including object transformation (rotation, translation, scaling), and matrix-based space transitions (model, world, camera, and projection spaces).

Digital chip design and implementation

Designed a 3x2 multiplier using Verilog to understand the full digital IC design flow—from RTL coding and functional simulation, to circuit-level verification using HSPICE (Prsim & Posim). Final layout was completed using DRC and LVS checks to ensure manufacturability.

Internship

Assistant Test Engineer

KYEC, 2020/07 – 2021/06

- Participated in machine training (Advantest 93K, Accretech TSK) and system software operation. Later took on client-facing roles involving communication, scheduling, and reporting.
- Initiated discussions based on Test Spec reviews and collaborated with clients to finalize product Data Sheets — ensuring compliance with test criteria and product stability.
- Took ownership of analyzing wafer test results (DC/Function Test), identifying performance patterns, and aligning them with client expectations.

Education

National Yang Ming Chiao Tung University

Master of Science in Photonics

2022/09 - 2024/08

National Kaohsiung University of Science and Technology

Bachelor of Science in Electrical Engineering

2017/07 - 2021/06

Detail Information



Please scan the QR code for **Course Record, Internship, Master thesis** and more.