



Smart Network Switch

Group 9



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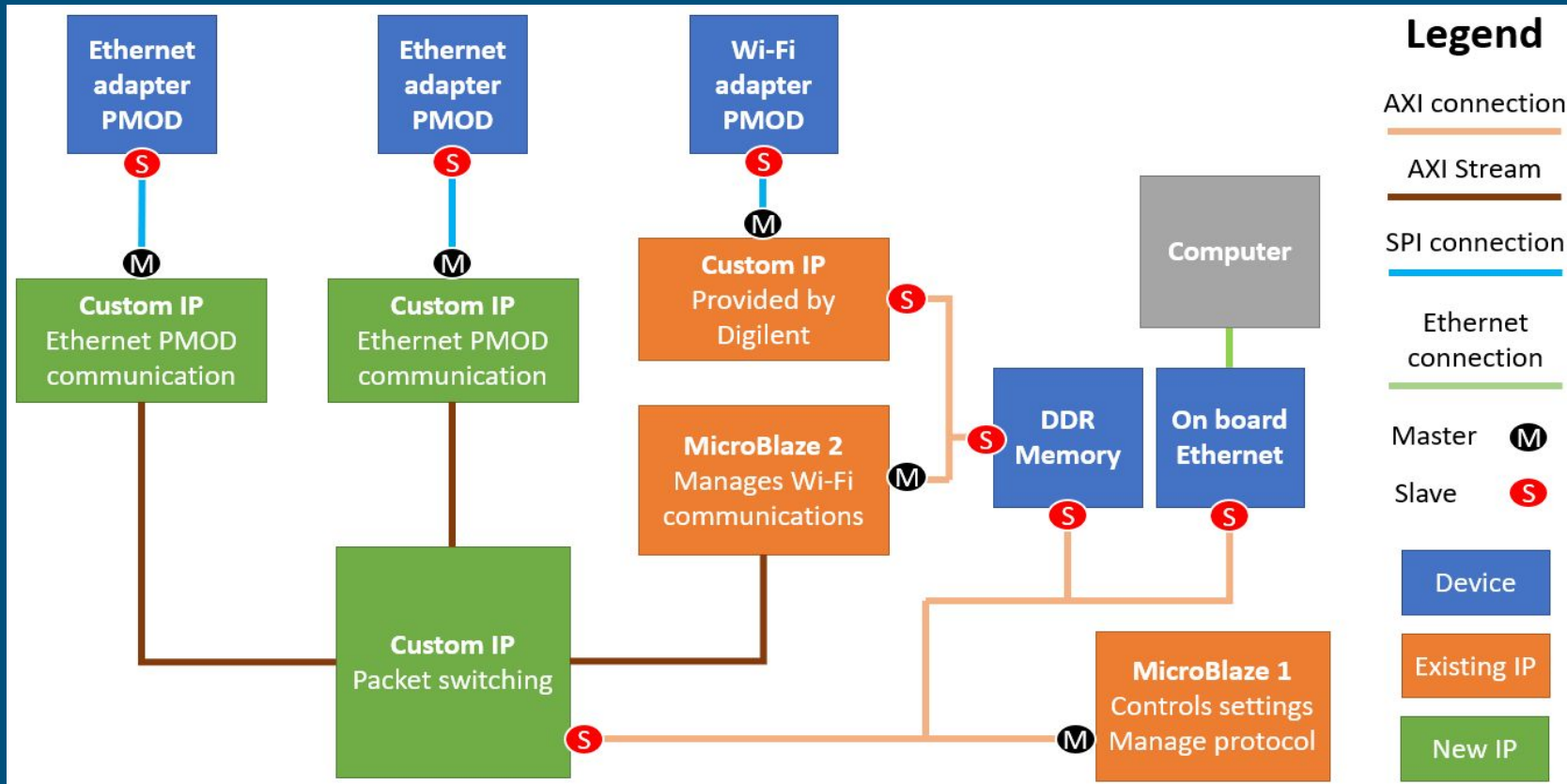


Project Overview

- Forward network traffic to the right target
- Done by building an address-port table



System Block Diagram



Requirements

• FPGA routes packets to the correct destination device (in hardware)	~
• Support two devices connected across Ethernet	✓
• Support one device connected across Wi-Fi	✗
• Support IPv4	✓
• Remotely configurable	✗
• Has monitoring capabilities from a PC connected across Ethernet	✗
• Minimum Throughput: 1Mb/s	✓
• Maximum Latency: 100 ms	✓

Initial Project Timeline (Mid-Project Demo)

Ethernet PMOD

- Look at Ethernet PMOD specs
- Implement the SPI interface
- Initialize the PMOD correctly

Showcase:
N/A (this will be done later on)

Status:
Completed ahead of Schedule

Wi-Fi PMOD

- Look at Wifi PMOD specs
- Initialize and set as access point correctly
- Establish communication through sending commands

Showcase:
Communication with a remote device

Status:
Not Completed / behind schedule

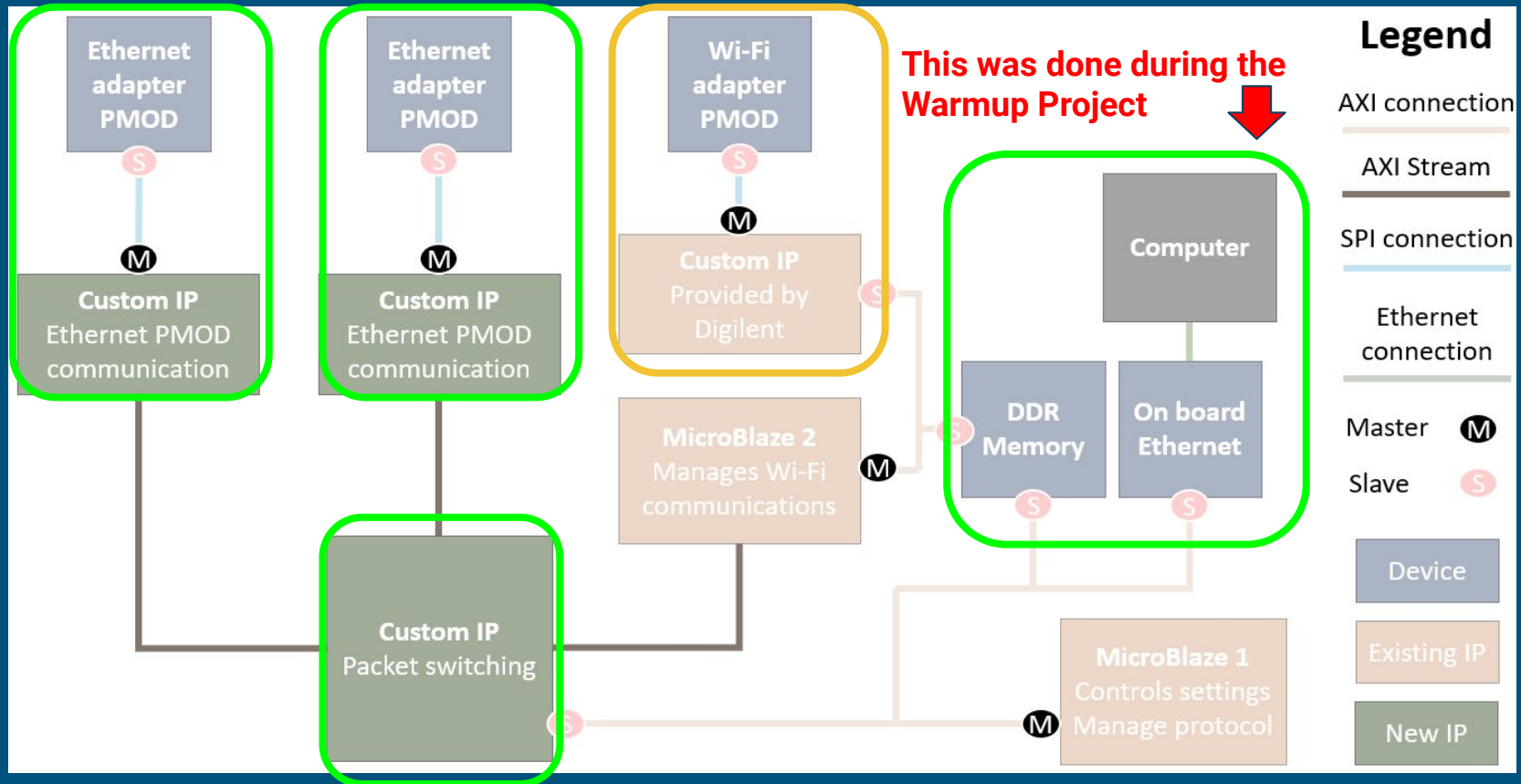
Packet Switching IP

- Create design plan
- Implement in Verilog
- Develop testbench
- Verify and debug functionality using simulation

Showcase:
Switching of packets

Status:
Completed on schedule

Implementation Progress



Demo: Ethernet PMOD Communication (Transmit)

352	103.418077	Digilent_03:23:df	Broadcast	ARP	60 Who has 192.168.1.11? Tell 192.168.1.10
353	103.418089	WistronI_5b:ec:4c	Digilent_03:23:df	ARP	42 192.168.1.11 is at 48:2a:e3:5b:ec:4c
> Ethernet II, Src: Digilent_03:23:df (00:18:3e:03:23:df), Dst: Broadcast (ff:ff:ff:ff:ff:ff)					
v Address Resolution Protocol (request)					
Hardware type: Ethernet (1)					
Protocol type: IPv4 (0x0800)					
Hardware size: 6					
Protocol size: 4					
Opcode: request (1)					
Sender MAC address: Digilent_03:23:df (00:18:3e:03:23:df)					
Sender IP address: 192.168.1.10					
Target MAC address: 00:00:00_00:00:00 (00:00:00:00:00:00)					
Target IP address: 192.168.1.11					
0000	ff ff ff ff ff ff	00 18 3e 03 23 df	08 06 00 01 >.#
0010	08 00 06 04 00 01	00 18 3e 03 23 df	c0 a8 01 0a	>.#
0020	00 00 00 00 00 00	c0 a8 01 0b 00 00	00 00 00 00
0030	00 00 00 00 00 00	00 00 00 00 00 00	

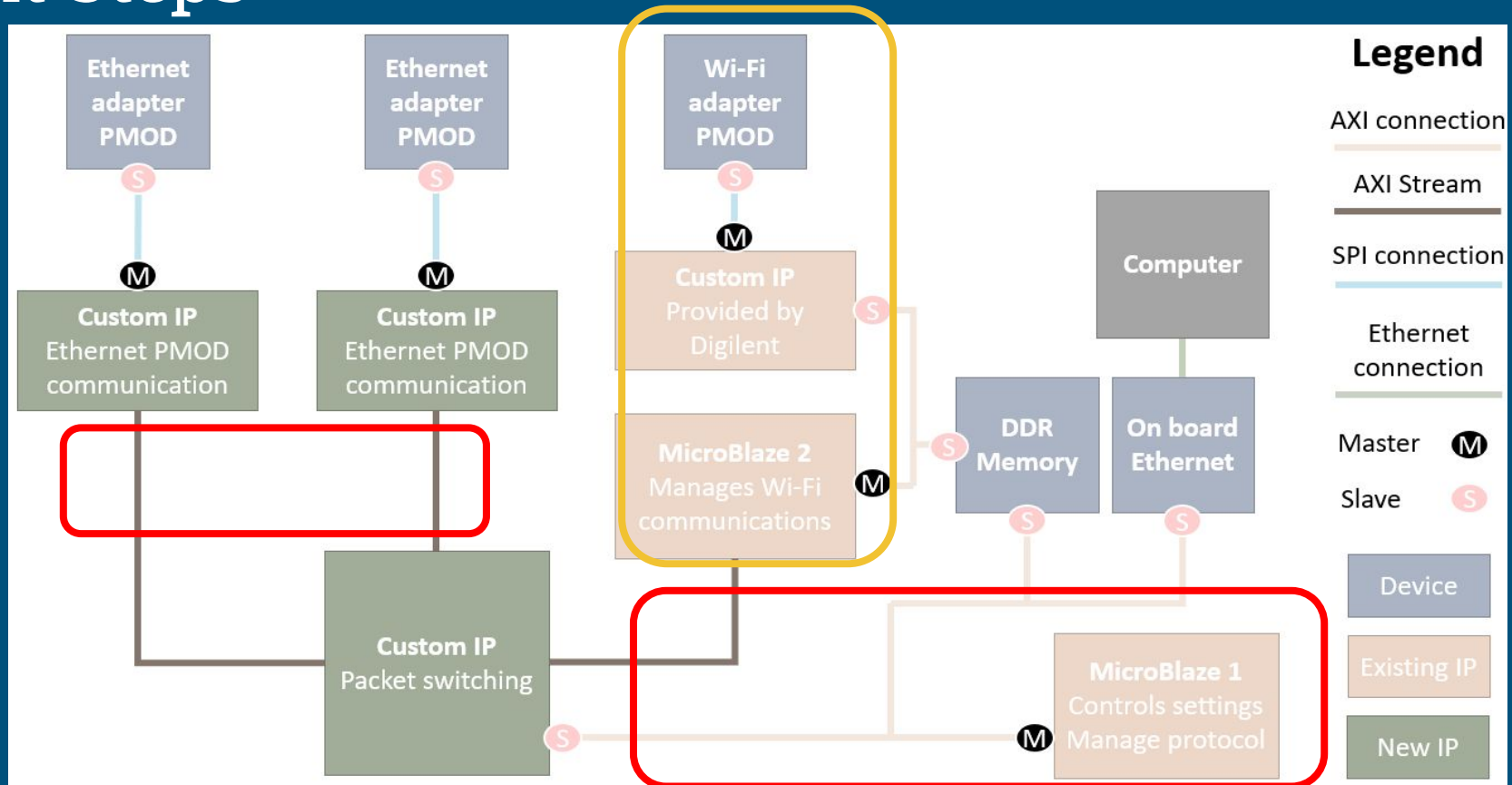
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Challenges Encountered

- Wi-Fi PMOD communication
 - ESP32 PMOD was not readily available, had to wait a full week to order it
 - Debugging issues where the PMOD is not properly responding to sent commands
- AXI-Stream FIFO (for demo only)
 - If Transmit size is not the same as data pushed in, no response

Next Steps



Updated Timeline

Milestone 5

- Integration of the Packet Switching IP and Ethernet PMOD modules
- Debug Wi-Fi PMOD communication

Milestone 6

- Add Microblaze configuration of the Packet Switching IP (turn off ports, track dropped packets, etc.)
- Integration of the Packet Switching IP and Wi-Fi PMOD module

Final Demo

- One week of 'leeway' time
- Resolve/Debug any issues or setbacks that occur in previous milestones

Potential Risks

- Might not get the ESP32 Wi-Fi PMOD working
 - Can't receive much help, not many other groups use this PMOD
 - Likely not enough time to experiment with backup PMODs
 - **Last Resort:** Substitute with 3rd Ethernet PMOD
- Might not successfully integrate the Packet Switch IP + PMODs
 - Main portion of project, hence it's much more severe
 - **Mitigation:** One week of 'leeway' time in the timeline
 - **Last Resort:** Do Packet Switching in software (if no convergence by Milestone 6)

ANY QUESTIONS?