

Board Name

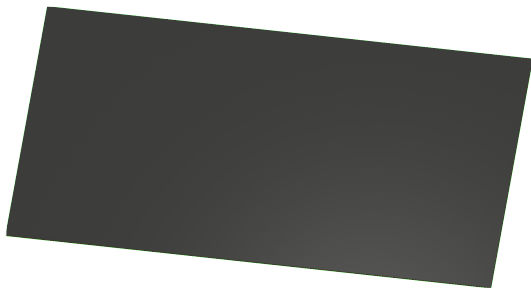
Variant: DRAFT

2025-01-24

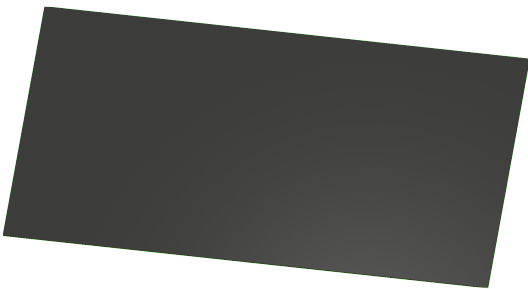
Rev + (Unreleased)

Page	Index	Page	Index	Page	Index	Page	Index
1	Cover Page	11		21		31	
2	Block Diagram	12		22		32	
3	Project Architecture	13		23		33	
4	MCU	14		24		34	
5	IMU	15		25		35	
6	Connectors	16		26		36	
7	Holes, Fiducials	17		27		37	
8	Revision History	18		28		38	
9		19		29		39	
10		20		30		40	

TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as X

- DRAFT - Very early stage of schematic, ignore details.
- PRELIMINARY - Close to final schematic.
- CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.
- RELEASED - A board with this schematic has been sent to production.

Date: 24-Jan-2025

DESIGN CONSIDERATIONS

DESIGN NOTE:

Example text for informational design notes.

DESIGN NOTE:

Example text for debug notes.

DESIGN NOTE:

Example text for cautionary design notes.

DESIGN NOTE:

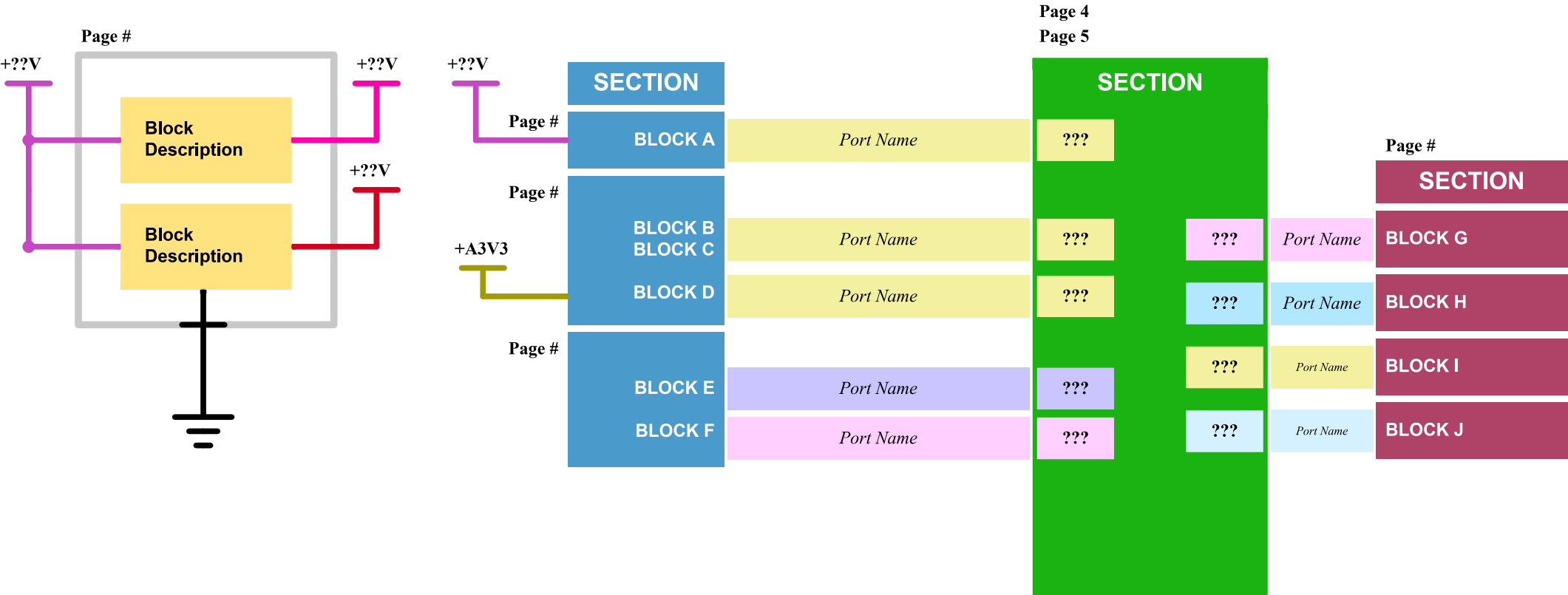
Example text for critical design notes.

LAYOUT NOTE:

Example text for critical layout guidelines.

	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: 77e9e33
		Board Name: Board Name		Project Name: Project Name	
	Sheet Title:	File Name: KiBot_Project_Test.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /		Reviewer:	Size: A3	Sheet: 1 of 8

# [2] Block Diagram



Target specifications:	
Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: 77e9e33
		Board Name: Board Name		Project Name: Project Name	
	Sheet Title: Block Diagram	File Name: Block Diagram.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
Sheet Path: /Block Diagram/			Reviewer:	Size: A3	Sheet: 2 of 8

[3] Project Architecture

The diagram illustrates the project architecture with four main components:

- Connectors (Page 6)**: File: Connectors.kicad\_sch
- MCU (Page 4)**: File: MCU.kicad\_sch
- IMU (Page 5)**: File: IMU.kicad\_sch
- Holes, Fiducials (Page 7)**: File: Holes, Fiducials.kicad\_sch

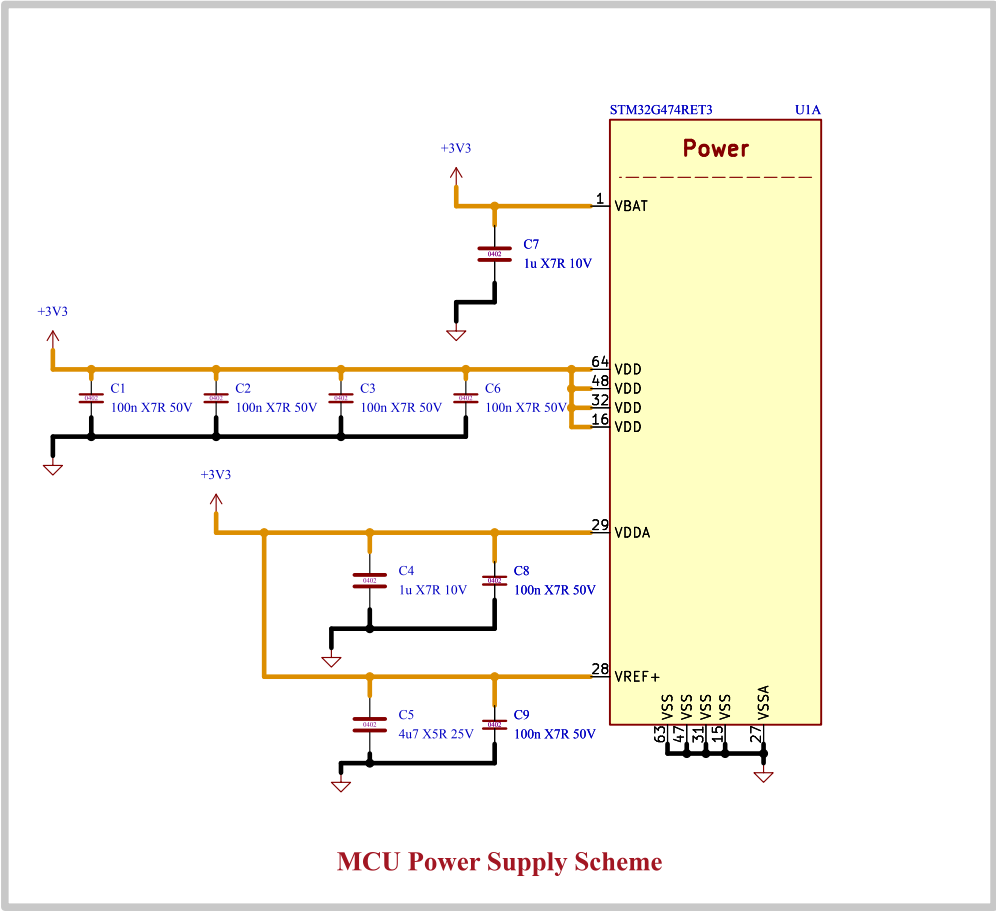
Connections between components:

- Connectors to MCU via {SWD}
- MCU to IMU via {I2C2}

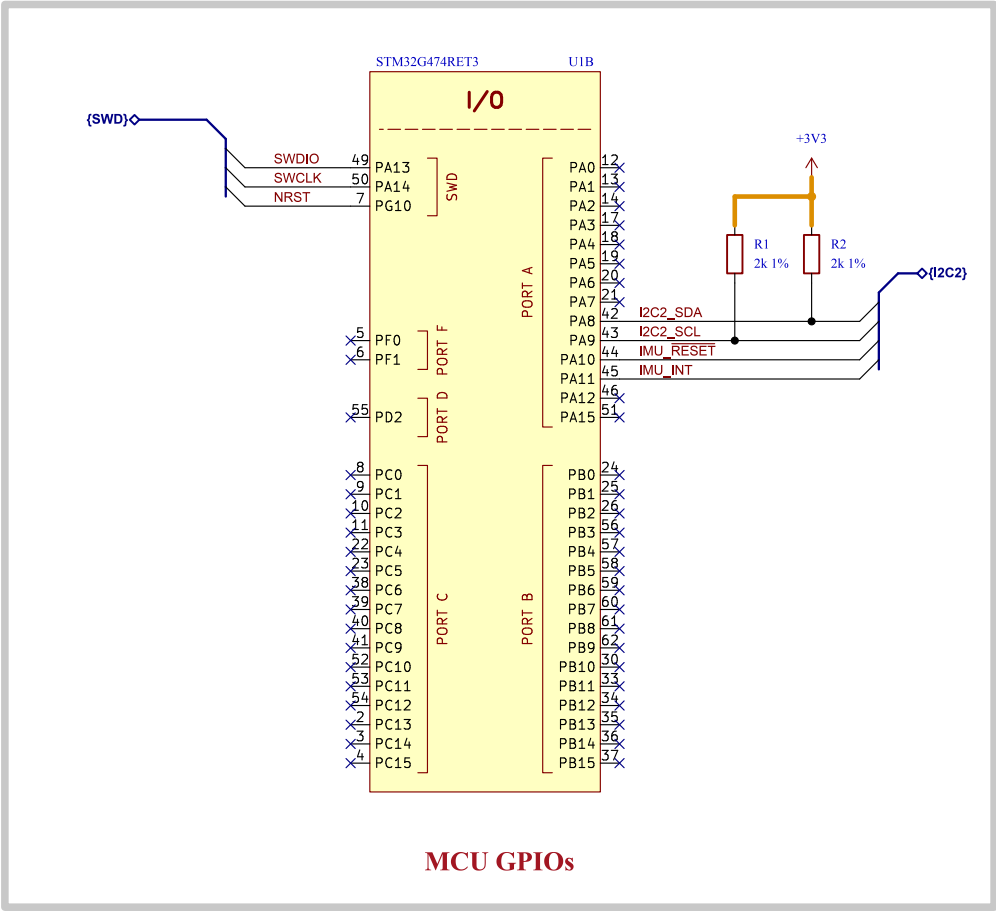
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			Company Name		DRAFT	77e9e33
			Board Name: Board Name		Project Name: Project Name	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)	
Sheet Path: /Project Architecture/		Reviewer:		Size: A3	Sheet: 3 of 8	

[illegible]

[4] MCU



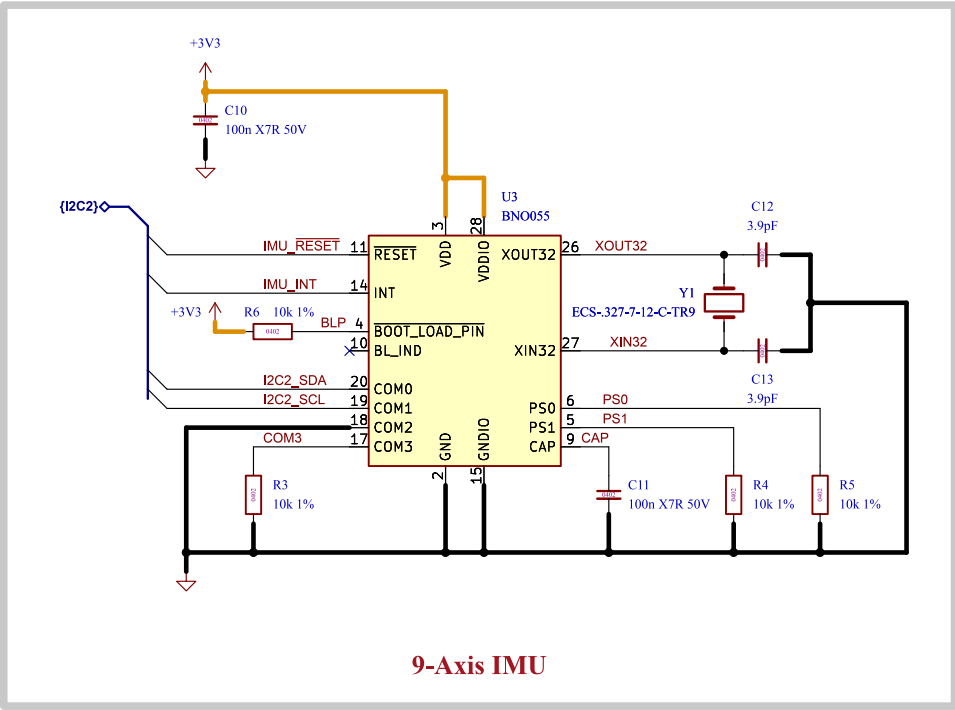
MCU Power Supply Scheme



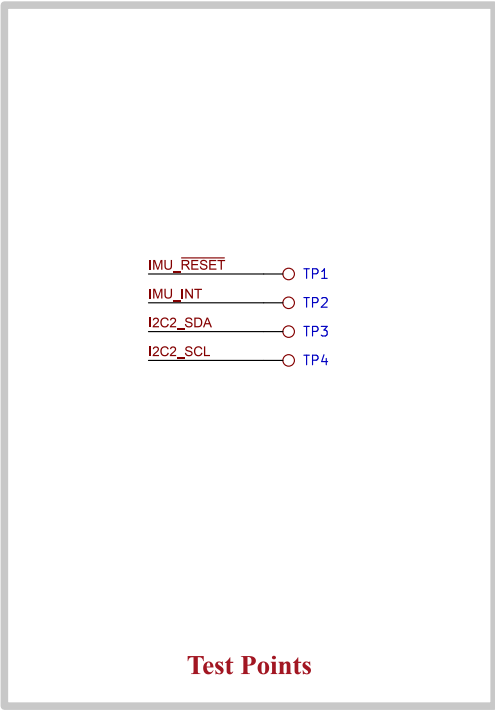
MCU GPIOs

	<b>Comments:</b> AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation		<b>Company:</b> Company Name		<b>Variant:</b> DRAFT	<b>Git Hash:</b> 77e9e33
			<b>Board Name:</b> Board Name		<b>Project Name:</b> Project Name	
	<b>Sheet Title:</b> MCU	<b>File Name:</b> MCU.kicad_sch	<b>Designer:</b> Author	<b>Date:</b> 2025-01-12	<b>Revision:</b> + (Unreleased)	
	<b>Sheet Path:</b> /Project Architecture/MCU/			<b>Reviewer:</b>	<b>Size:</b> A4	<b>Sheet:</b> 4 of 8

[5] IMU



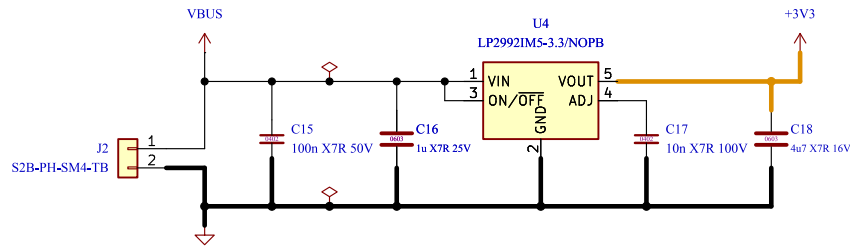
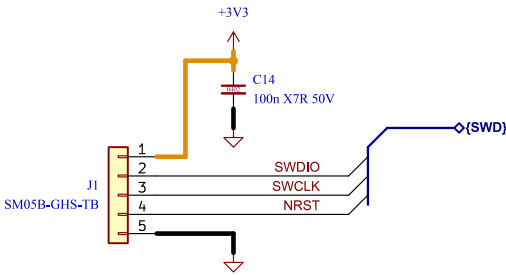
9-Axis IMU



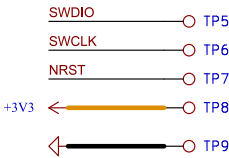
Test Points

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			Board Name		Project Name	
	Sheet Title:	IMU	File Name:	IMU.kicad_sch	Designer:	Author
	Sheet Path:	/Project Architecture/IMU/	Reviewer:		Date:	2025-01-12
					Revision:	+ (Unreleased)
					Size:	A4
					Sheet:	5 of 8

# [6] Connectors



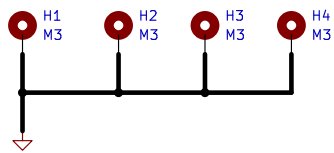
Connectors



Test Points

	Comments:	Company:		Variant:		Git Hash:	
		Company Name		DRAFT		77e9e33	
		Board Name:		Project Name:			
	Board Name		Project Name				
	Sheet Title:	File Name:	Designer:	Date:	Revision:		
Connectors	Connectors.kicad_sch	Author		+ (Unreleased)			
Sheet Path:		Reviewer:	Size:	Sheet:			
/Project Architecture/Connectors/			A4	6 of 8			

# [7] Holes, Fiducials



Mounting Holes

Front

- FD1
- FD2
- FD3

Bottom

- FD4
- FD5
- FD6

Fiducial Markers

	Comments:	Company:		Variant:	Git Hash:
		Company Name		DRAFT	77e9e33
		Board Name:		Project Name:	
		Board Name		Project Name	
	Sheet Title:	File Name:	Designer:	Date:	Revision:
	Holes, Fiducials	Holes, Fiducials.kicad_sch	Author	2025-01-24	+ (Unreleased)
	Sheet Path:		Reviewer:	Size:	Sheet:
	/Project Architecture/Holes, Fiducials/			A4	7 of 8

# [8] Revision History

	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: 77e9e33
		Board Name: Board Name		Project Name: Project Name	
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Revision History/		Reviewer:	Size: A4	Sheet: 8 of 8