

Board Name

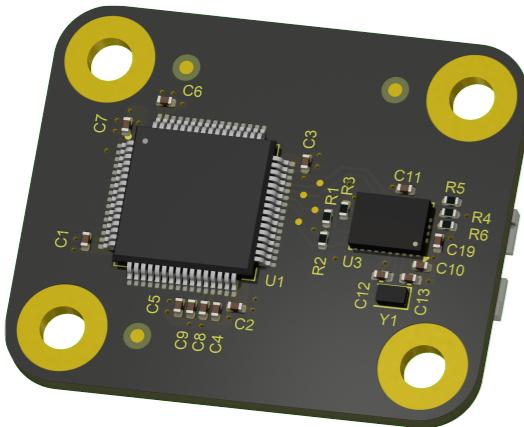
Variant: RELEASED

2025-03-02

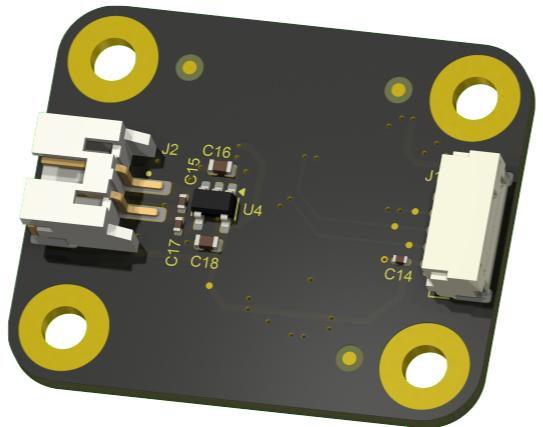
Rev 1.0.0

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 02-Mar-2025

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational design notes.

DESIGN NOTE:
Example text for debug notes.

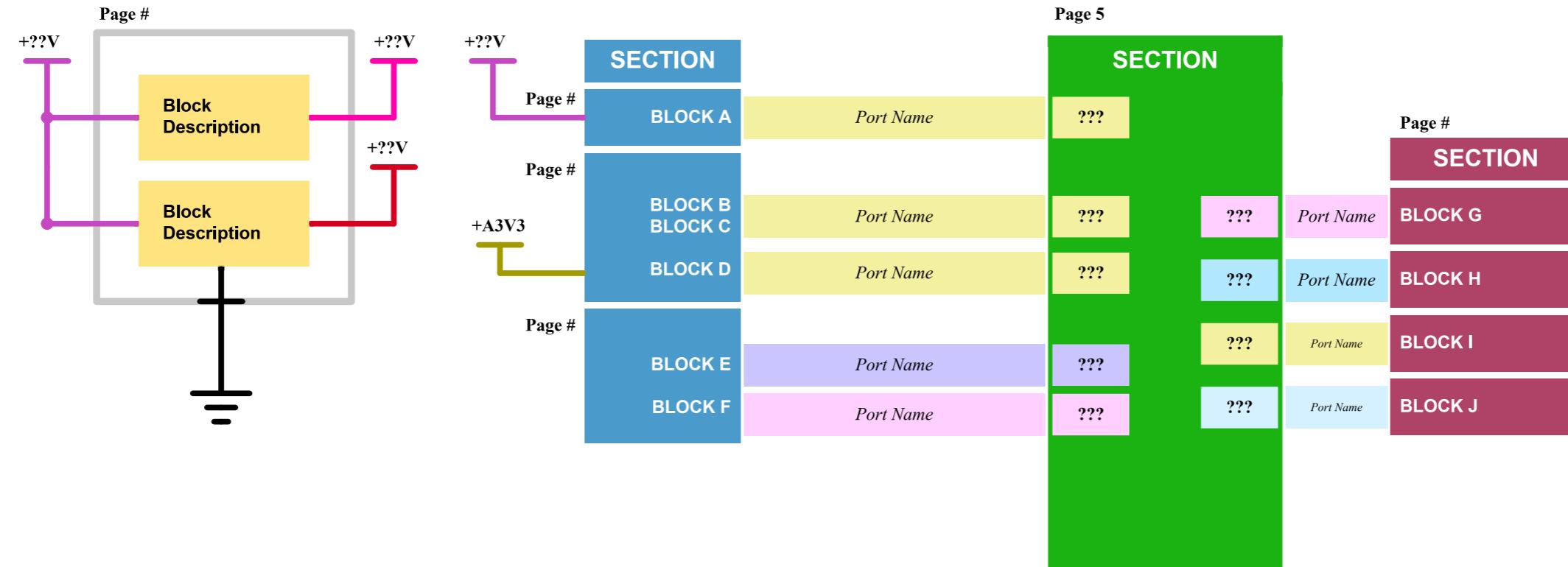
DESIGN NOTE:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

	Comments:	Company: Company Name	Variant: RELEASED	Git Hash: 7ba06dd
	Board Name: Board Name	Project Name: Project Name		
	Sheet Title: File Name: KiBot_Project_Test.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: 1.0.0
	Sheet Path: /	Reviewer:	Size: A3	Sheet: 1 of 8

[2] Block Diagram

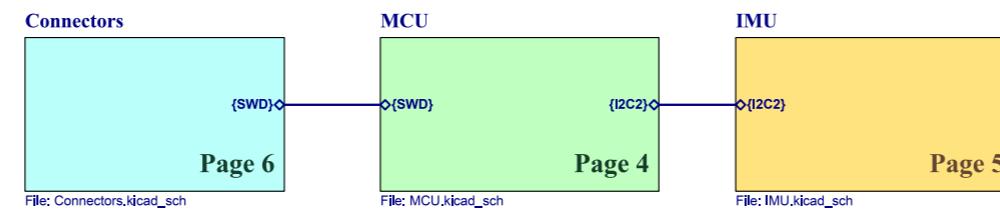


Target specifications:

Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

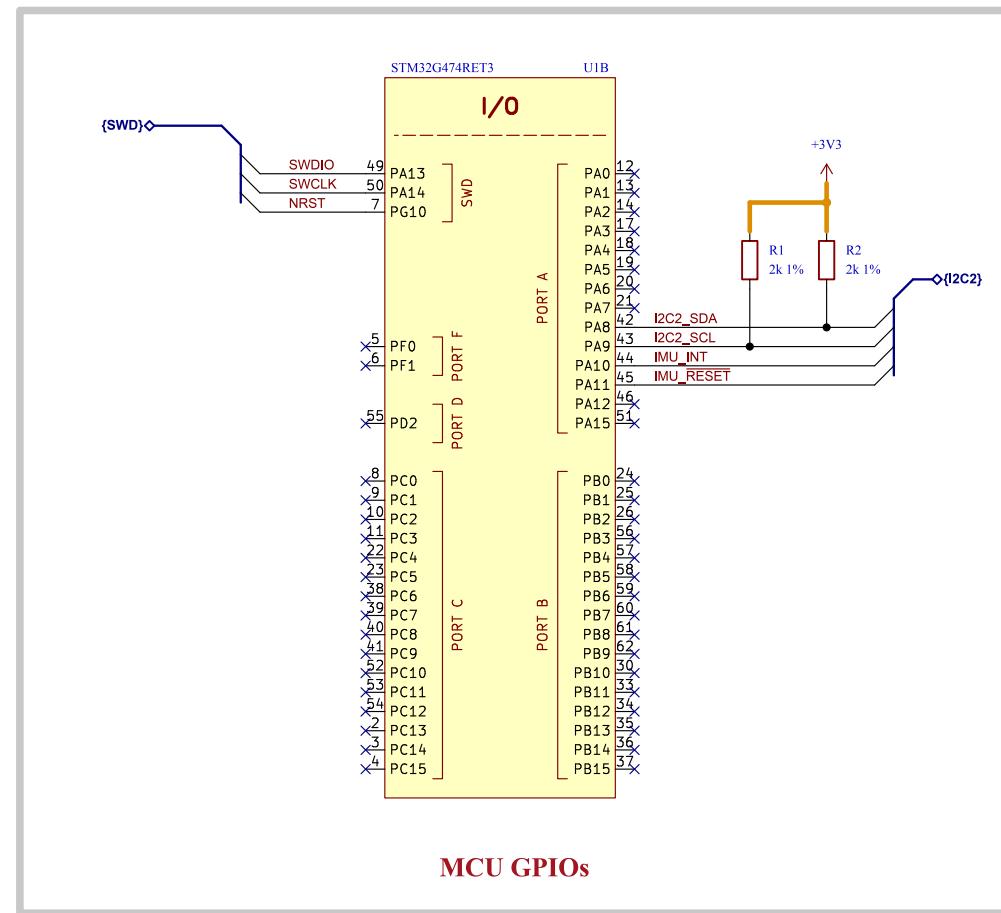
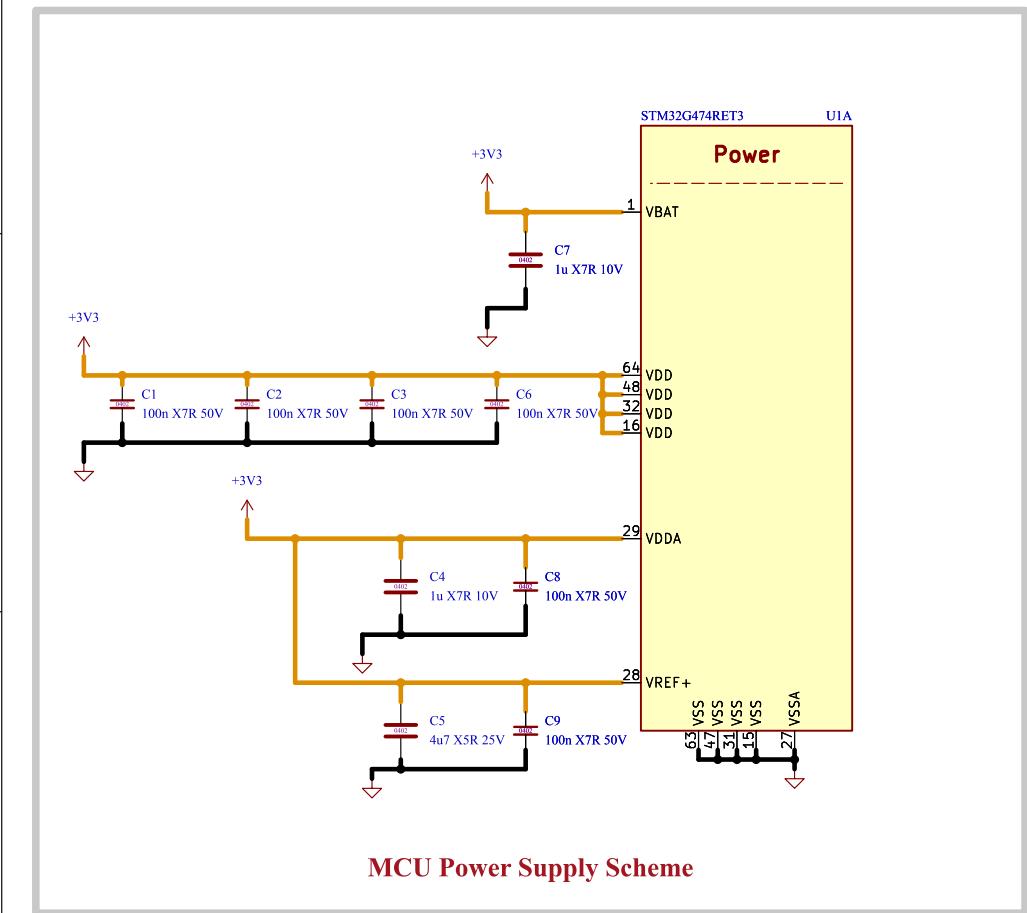
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Sheet Title: Block Diagram	File Name: Block Diagram.kicad_sch	Designer: Author	Date: 2025-01-12
Sheet Path: /Block Diagram/		Reviewer:	Size: A3 Sheet: 2 of 8

[3] Project Architecture



	Comments:	Company: Company Name	Variant: RELEASED	Git Hash: 7ba06dd
	Board Name: Board Name		Project Name: Project Name	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Author	Date: 2025-01-12 Revision: 1.0.0
	Sheet Path: /Project Architecture/		Reviewer:	Size: A3 Sheet: 3 of 8

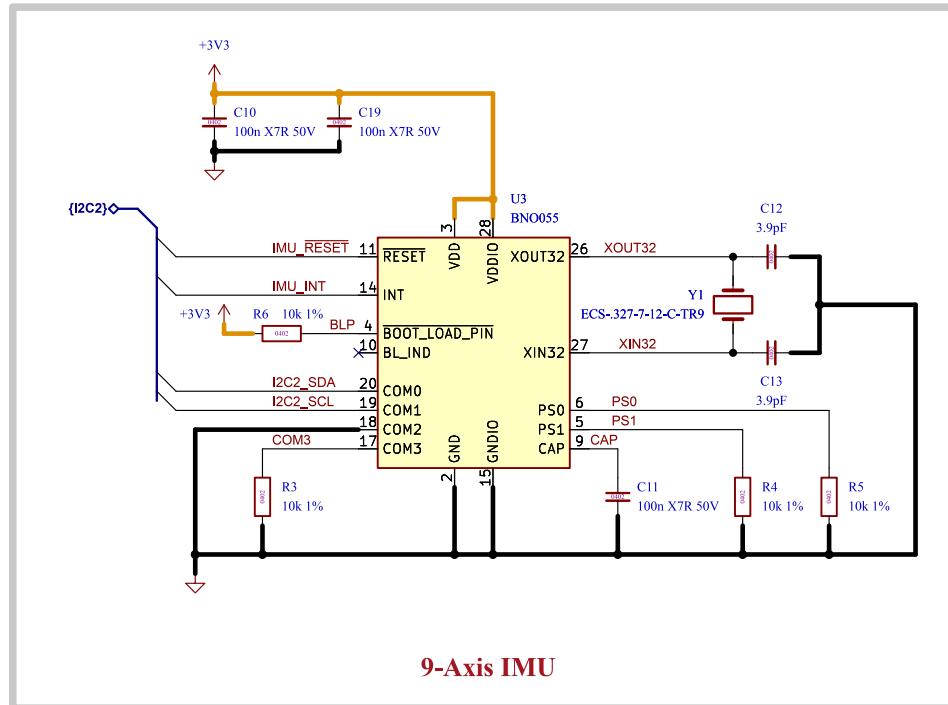
[4] MCU



		<p>Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation</p>	<p>Company: Company Name</p> <p>Board Name: Board Name</p>	<p>Variant: RELEASED</p>	Git Hash: 7ba06dd
		<p>Sheet Title: MCU</p>	<p>File Name: MCU.kicad_sch</p>	<p>Designer: Author</p>	<p>Date: 2025-01-12</p> <p>Revision: 1.0.0</p>
		<p>Sheet Path: /Project Architecture/MCU/</p>			<p>Reviewer: </p> <p>Size: A4</p> <p>Sheet: 4 of 8</p>

[5] IMU

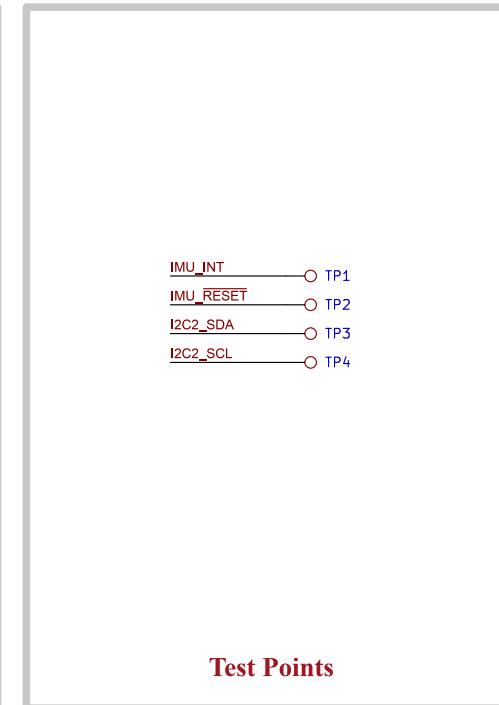
A



IMU_INT	TP1
IMU_RESET	TP2
I2C2_SDA	TP3
I2C2_SCL	TP4

Test Points

B



C

1

	Comments: Company: Company Name	Variant: RELEASED	Git Hash: 7ba06dd
	Board Name: Board Name	Project Name: Project Name	
	Sheet Title: IMU	File Name: IMU.kicad_sch	Designer: Author
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			Sheet: 5 of 8

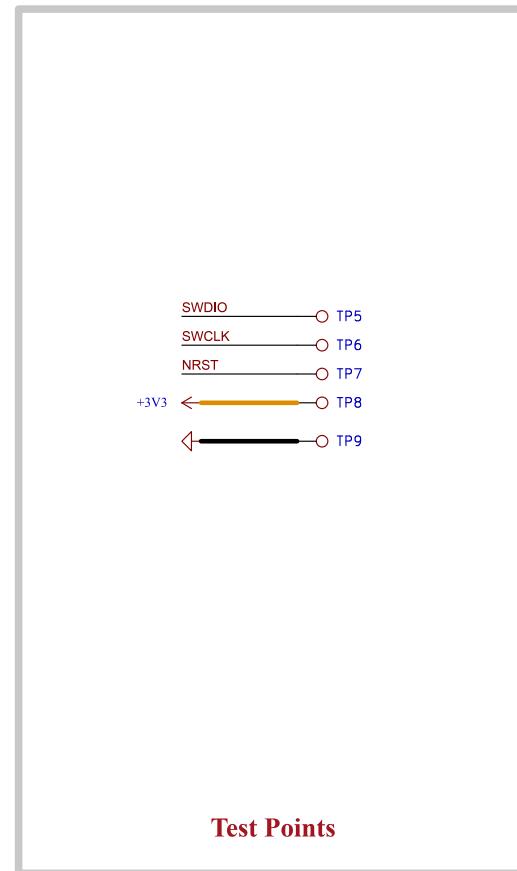
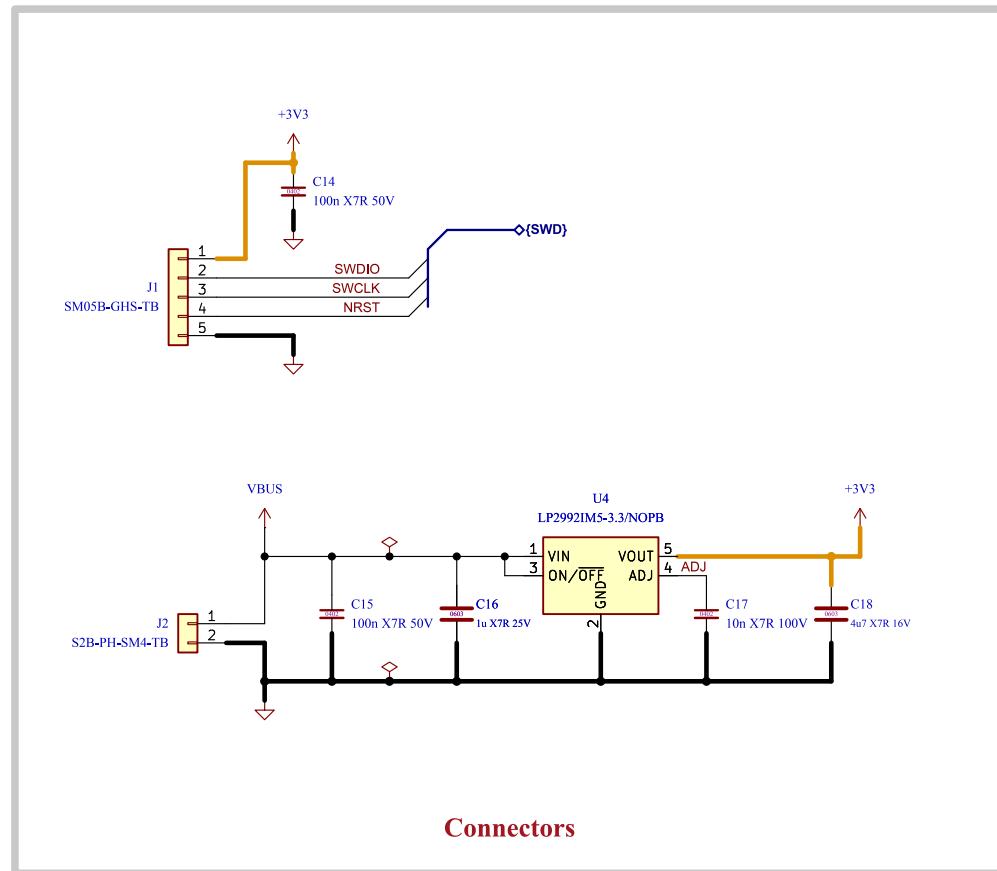
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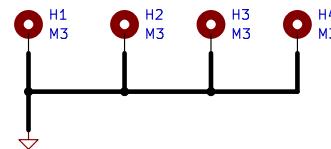
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[6] Connectors



	Comments: Company: Company Name	Variant: RELEASED	Git Hash: 7ba06dd
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	Sheet Title: Connectors	File Name: Connectors.kicad_sch	Designer: Author
	Sheet Path: /Project Architecture/Connectors/	Reviewer:	Size: A4
			Sheet: 6 of 8

[7] Holes, Fiducials



Mounting Holes

Front

- FD1
- FD2
- FD3

Bottom

- FD4
- FD5
- FD6

Fiducial Markers

	Comments:	Company: Company Name	Variant: RELEASED	Git Hash: 7ba06dd
	Board Name: Board Name			Project Name: Project Name
	Sheet Title: Holes, Fiducials	File Name: Holes, Fiducials.kicad_sch	Designer: Author	Date: 2025-01-24 Revision: 1.0.0
	Sheet Path: /Project Architecture/Holes, Fiducials/			Reviewer: Size: A4 Sheet: 7 of 8

[8] Revision History

A	Version 1.0.0 - 2025-03-02 <hr/> <p>Added</p> <ul style="list-style-type: none">- First version of the PCB and schematic.	B	C	D	A	B	C	D																			
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