

Board Name

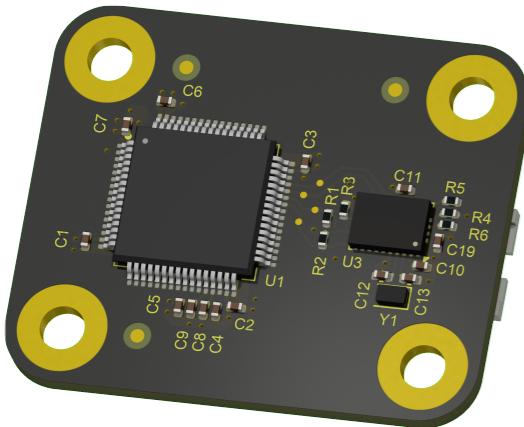
Variant: CHECKED

2025-03-02

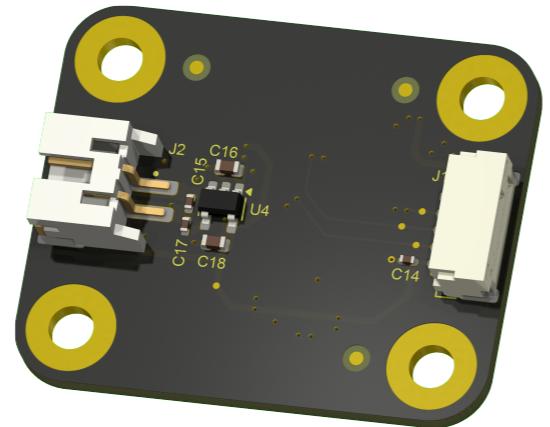
Rev + (Unreleased)

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 02-Mar-2025

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational design notes.

DESIGN NOTE:
Example text for debug notes.

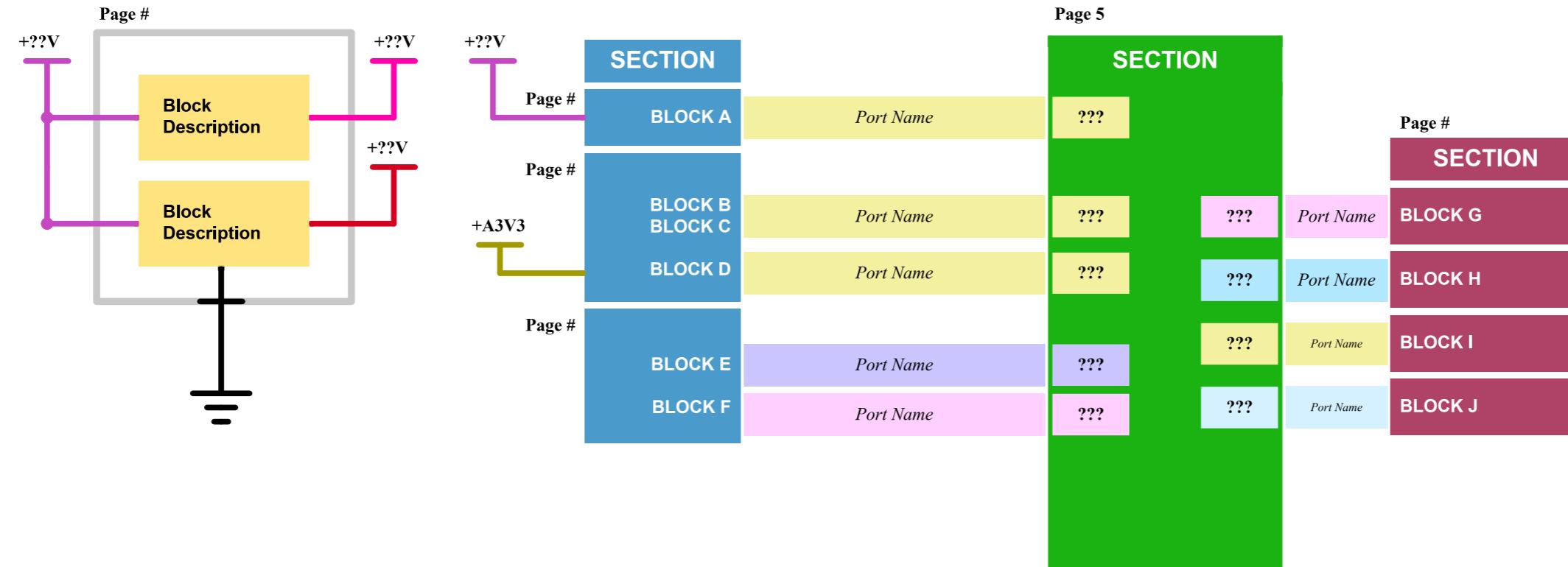
DESIGN NOTE:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

	Comments:	Company: Company Name	Variant: CHECKED	Git Hash: 77e9e33
	Board Name: Board Name	Project Name: Project Name		
	Sheet Title: File Name: KiBot_Project_Test.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /	Reviewer:	Size: A3	Sheet: 1 of 8

[2] Block Diagram

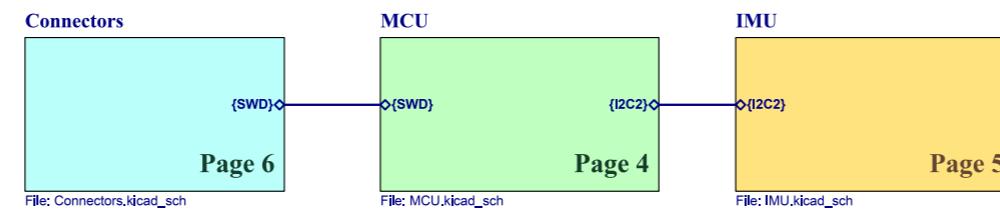


Target specifications:

Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

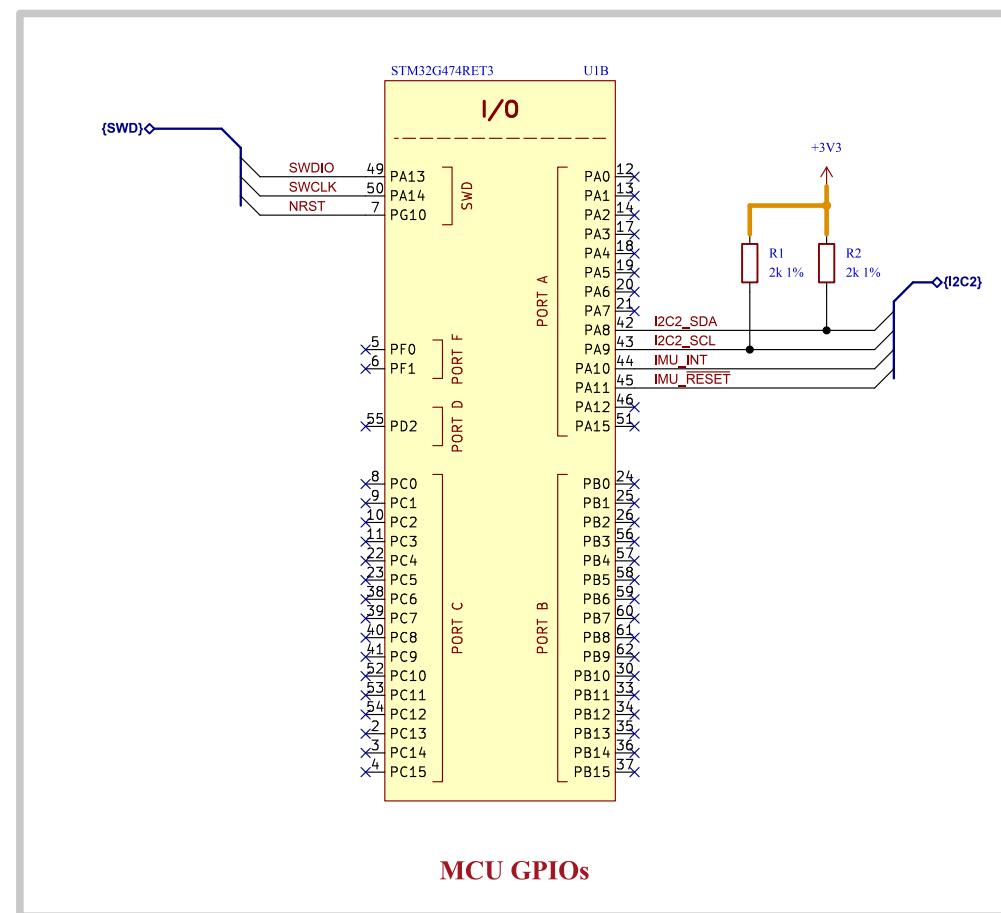
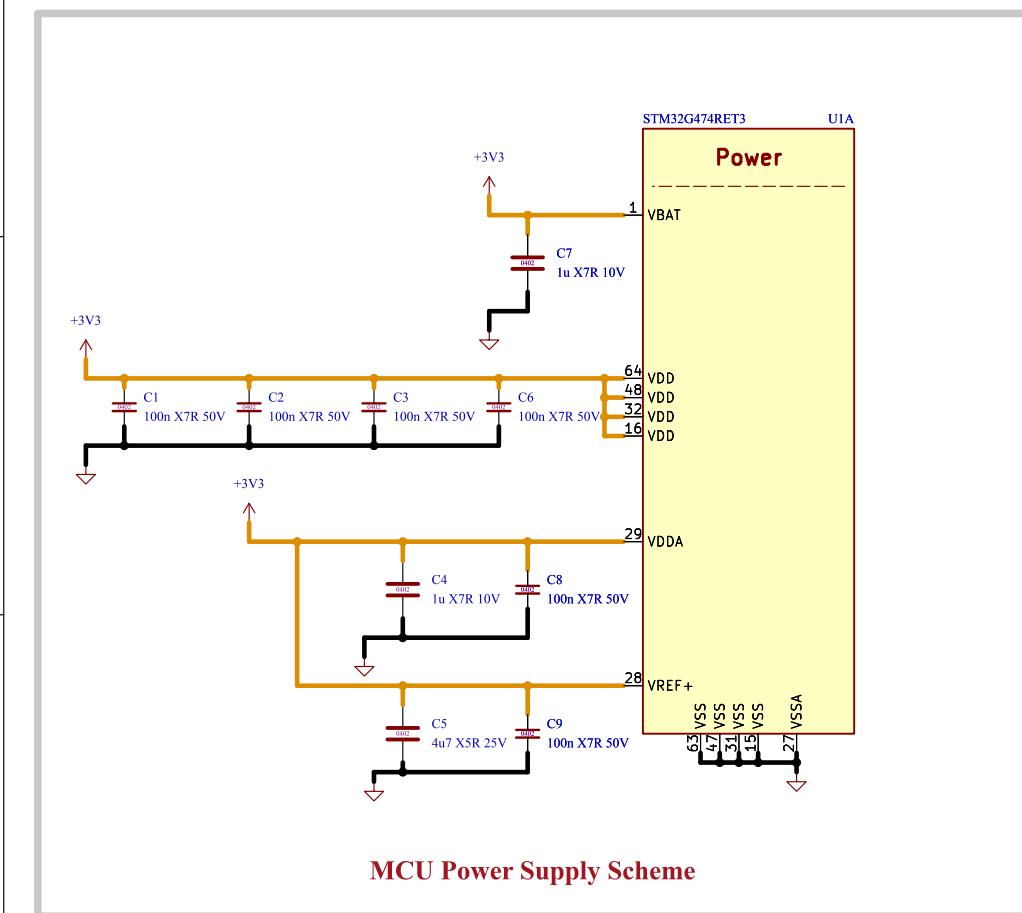
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		Board Name: Board Name	Project Name: Project Name	
	Sheet Title: Block Diagram	File Name: Block Diagram.kicad_sch	Designer: Author	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Block Diagram/	Reviewer:	Size: A3	Revision: Sheet: 2 of 8

[3] Project Architecture



	Comments:	Company: Company Name	Variant: CHECKED	Git Hash: 77e9e33
	Board Name: Board Name		Project Name: Project Name	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Author	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: A3 Sheet: 3 of 8

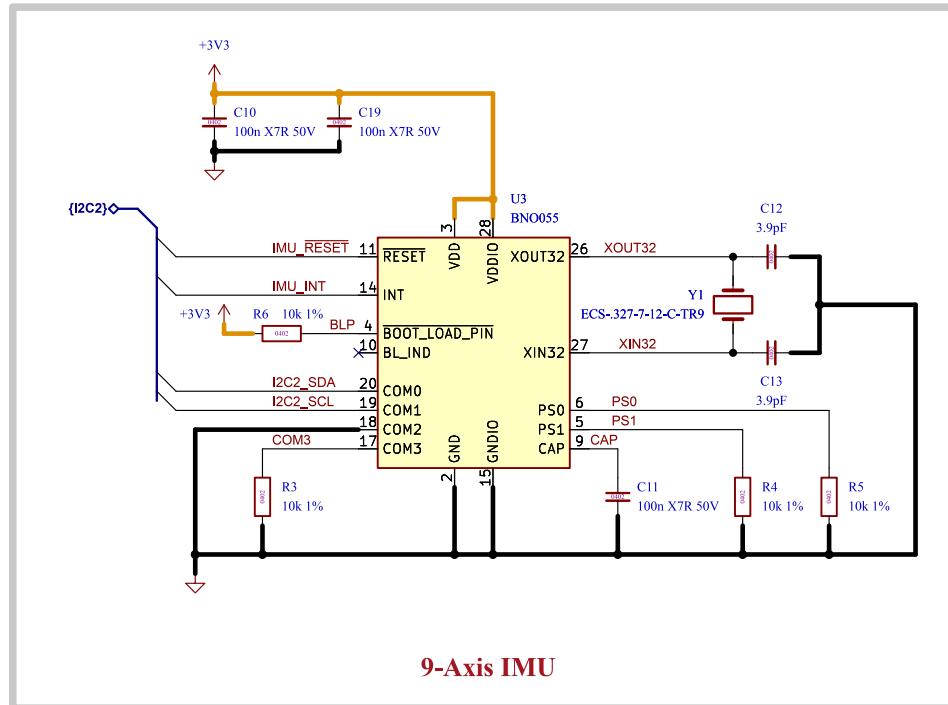
[4] MCU



Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation	Company: Company Name	Variant: CHECKED	Git Hash: 77e9e33
Board Name: Board Name	Project Name: Project Name		
Sheet Title: MCU	File Name: MCU.kicad_sch	Designer: Author	Date: 2025-01-12
Sheet Path: /Project Architecture/MCU/	Reviewer:	Size: A4	Sheet: 4 of 8

[5] IMU

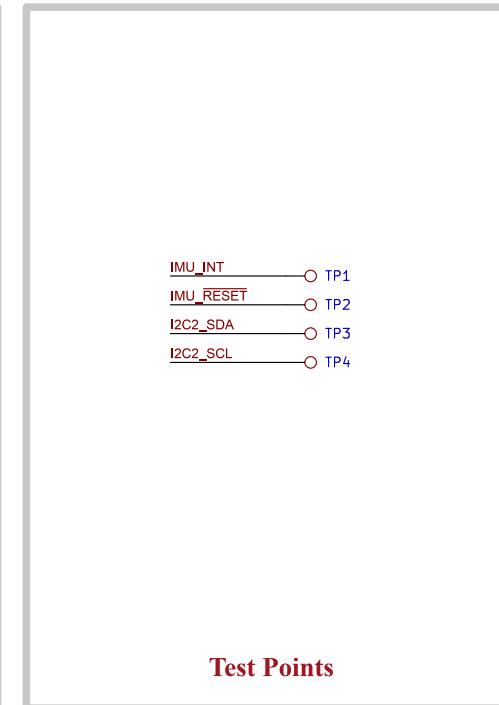
A



IMU_INT	TP1
IMU_RESET	TP2
I2C2_SDA	TP3
I2C2_SCL	TP4

Test Points

B



C

1

	Comments: Company: Company Name	Variant: CHECKED	Git Hash: 77e9e33
	Board Name: Board Name	Project Name: Project Name	
	Sheet Title: IMU	File Name: IMU.kicad_sch	Designer: Author
	Sheet Path: /Project Architecture/IMU/		Reviewer: Size: A4
			Sheet: 5 of 8

3

4

5

6

[6] Connectors

A

A

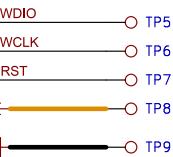
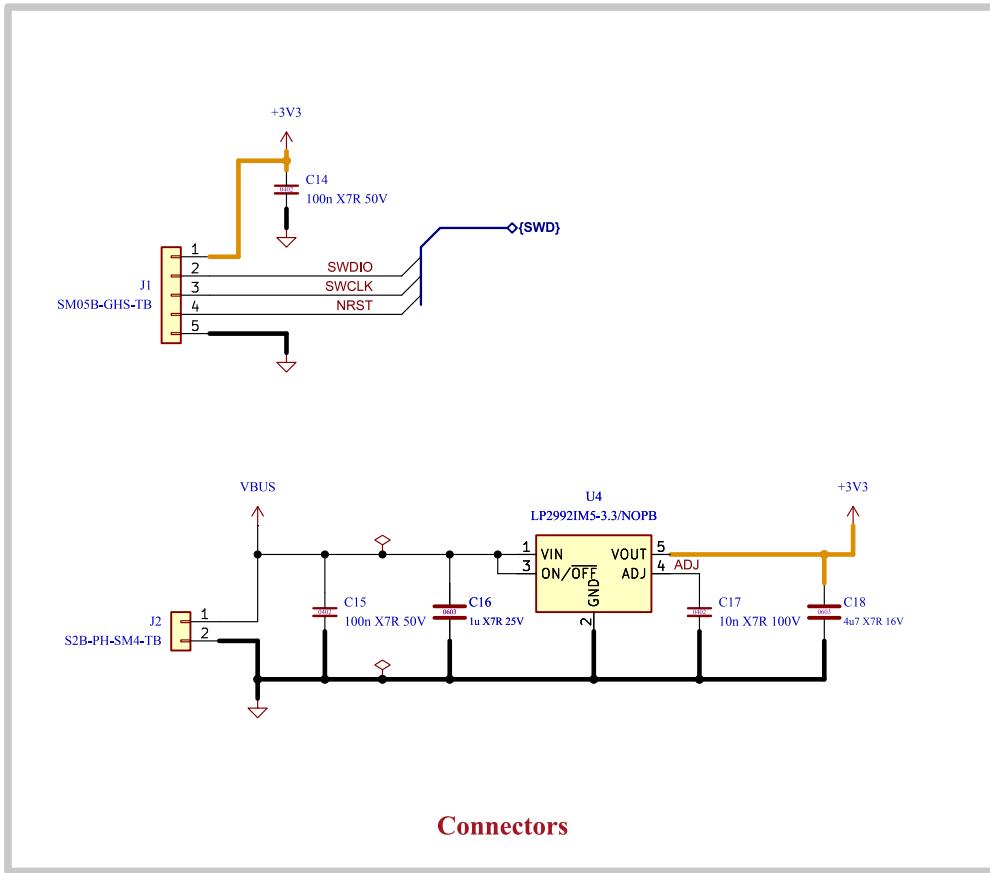
B

B

C

C

Connectors



Test Points

D

D

1

6

	Comments: Company: Company Name	Variant: CHECKED	Git Hash: 77e9e33
	Board Name: Board Name	Project Name: Project Name	
	Sheet Title: Connectors	File Name: Connectors.kicad_sch	Designer: Author
	Sheet Path: /Project Architecture/Connectors/	Reviewer:	Size: A4
			Sheet: 6 of 8

2

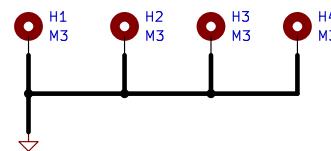
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4

5

6

[7] Holes, Fiducials



Mounting Holes

Front

- FD1
- FD2
- FD3

Bottom

- FD4
- FD5
- FD6

Fiducial Markers

	Comments:	Company: Company Name	Variant: CHECKED	Git Hash: 77e9e33
	Board Name: Board Name			Project Name: Project Name
	Sheet Title: Holes, Fiducials	File Name: Holes, Fiducials.kicad_sch	Designer: Author	Date: 2025-01-24 Revision: + (Unreleased)
	Sheet Path: /Project Architecture/Holes, Fiducials/		Reviewer:	Size: A4 Sheet: 7 of 8

[8] Revision History

A **Version 1.0.0 not found.**

Version 1.0.0 not found.

B

C

D

A

B

C

D

	Comments:	Company: Company Name	Variant: CHECKED	Git Hash: 77e9e33
	Board Name: Board Name	Project Name: Project Name		
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: Author	Date: 2025-01-12 Revision: + (Unreleased)
	Sheet Path: /Revision History/		Reviewer:	Size: A4 Sheet: 8 of 8