

Board Name

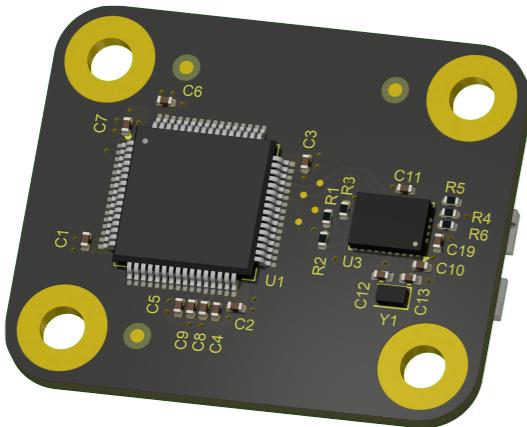
2025-03-02

Variant: PRELIMINARY

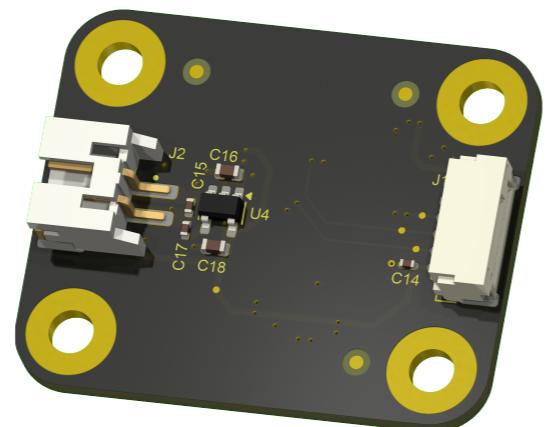
Rev + (Unreleased)

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### TOP VIEW



### BOTTOM VIEW



### NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 02-Mar-2025

### DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational design notes.

DESIGN NOTE:  
Example text for debug notes.

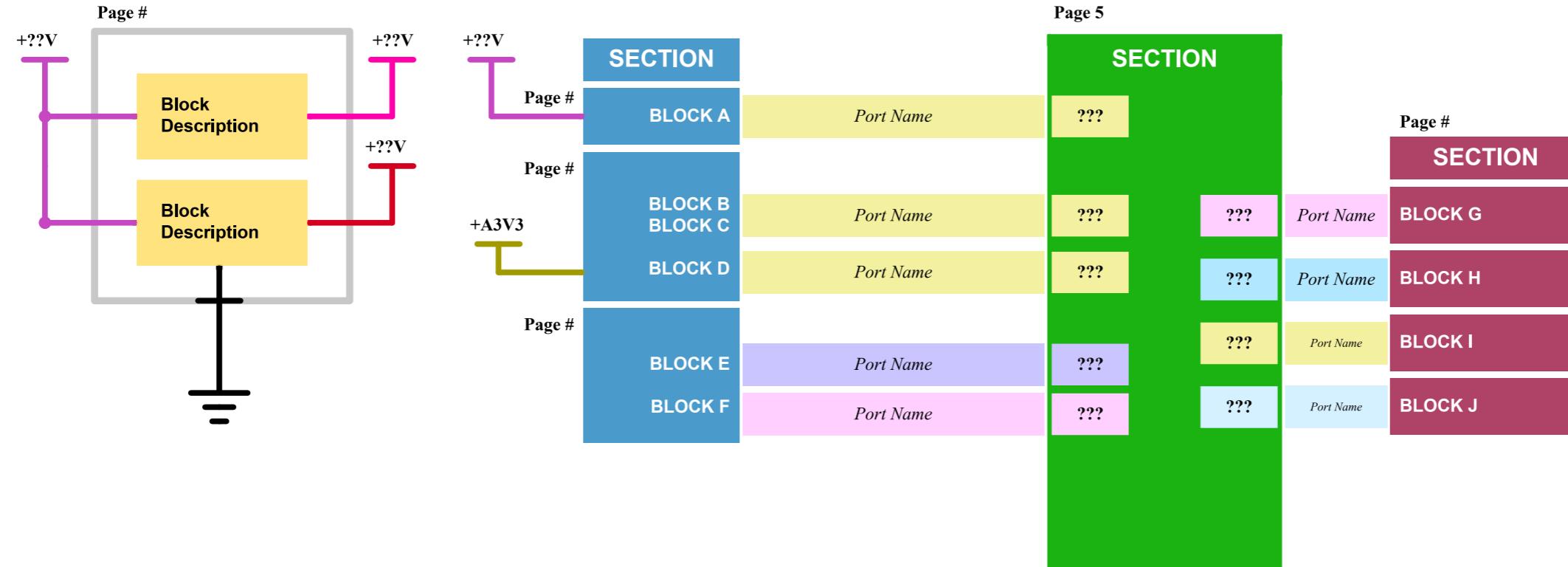
DESIGN NOTE:  
Example text for cautionary design notes.

DESIGN NOTE:  
Example text for critical design notes.

LAYOUT NOTE:  
Example text for critical layout guidelines.

	Comments:	Company: Company Name	Variant: PRELIMINARY	Git Hash: 77e9e33
	Board Name: <b>Board Name</b>	Project Name: <b>Project Name</b>		
	Sheet Title: File Name: KiBot_Project_Test.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /	Reviewer:	Size: <b>A3</b>	Sheet: <b>1</b> of <b>8</b>

# [2] Block Diagram

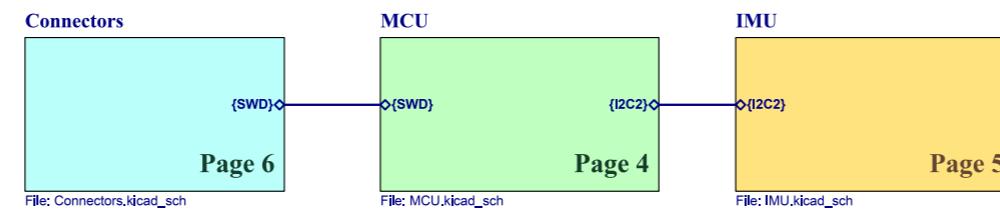


## Target specifications:

Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

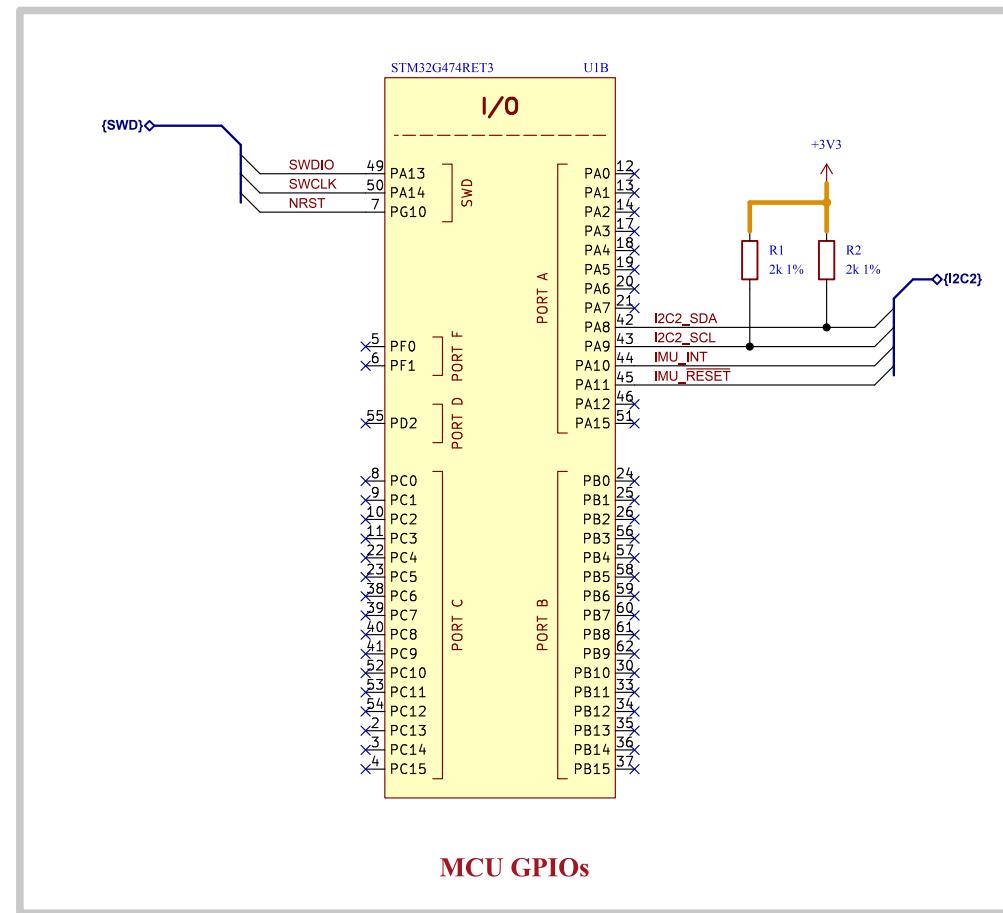
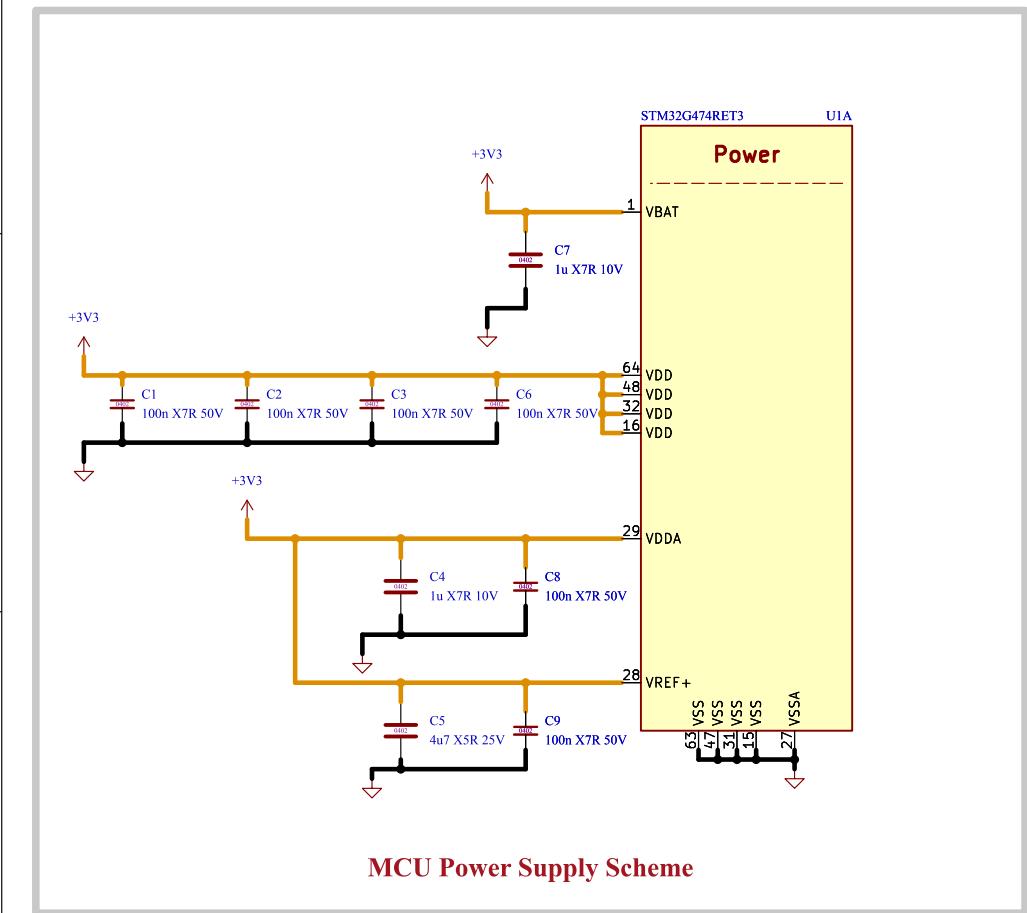
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		Company Name		
	Board Name:	<b>Board Name</b>		
	Sheet Title:	File Name:	Designer:	Date:
	Block Diagram	Block Diagram.kicad_sch	Author	2025-01-12 + (Unreleased)
	Sheet Path:	Reviewer:		Size:
	/Block Diagram/			A3
		Sheet:		2 of 8

# [3] Project Architecture



	Comments:	Company: Company Name	Variant: PRELIMINARY	Git Hash: 77e9e33
	Board Name: <b>Board Name</b>		Project Name: <b>Project Name</b>	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Author	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: <b>A3</b> Sheet: <b>3 of 8</b>

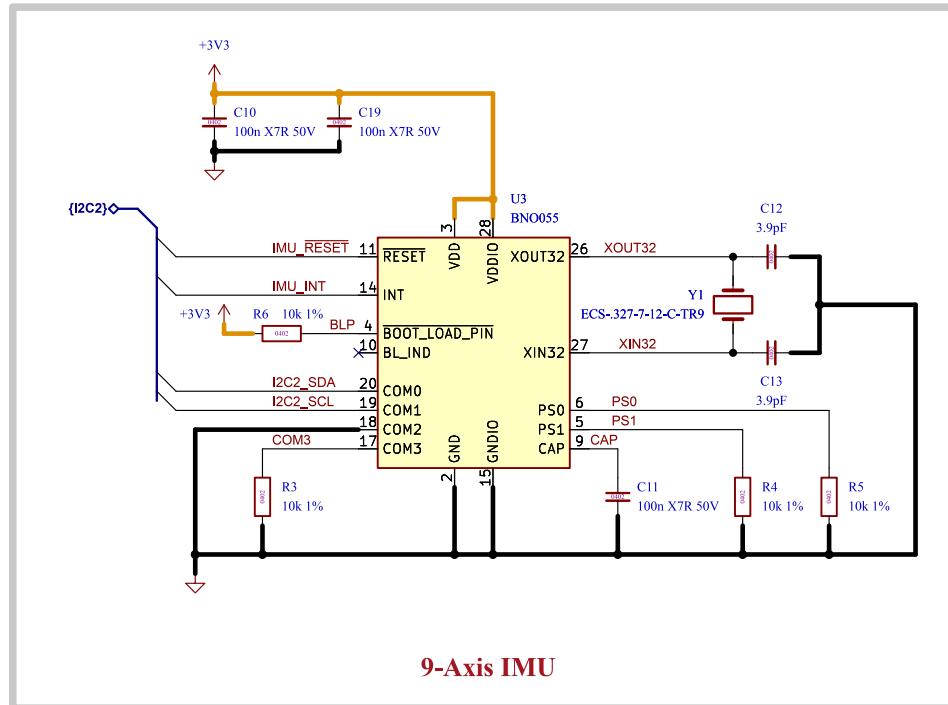
# [4] MCU



Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation	Company: Company Name	Variant: PRELIMINARY	Git Hash: 77e9e33
Board Name: <b>Board Name</b>	Project Name: <b>Project Name</b>		
Sheet Title: MCU	File Name: MCU.kicad_sch	Designer: Author	Date: 2025-01-12
Sheet Path: /Project Architecture/MCU/	Reviewer:	Size: <b>A4</b>	Revision: + (Unreleased)

# [5] IMU

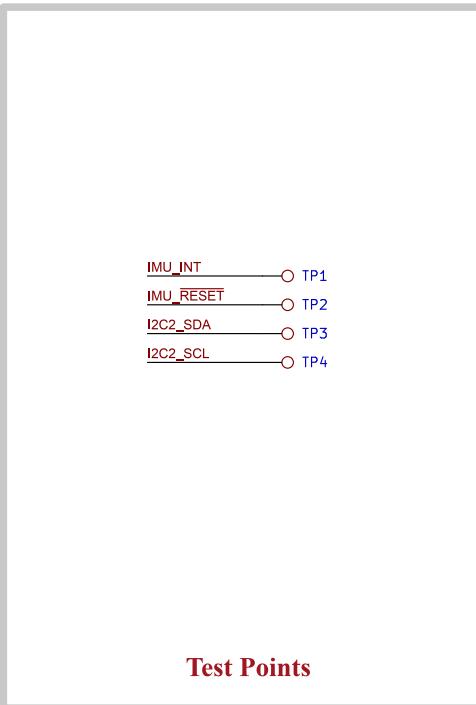
A



IMU_INT	TP1
IMU_RESET	TP2
I2C2_SDA	TP3
I2C2_SCL	TP4

**Test Points**

B

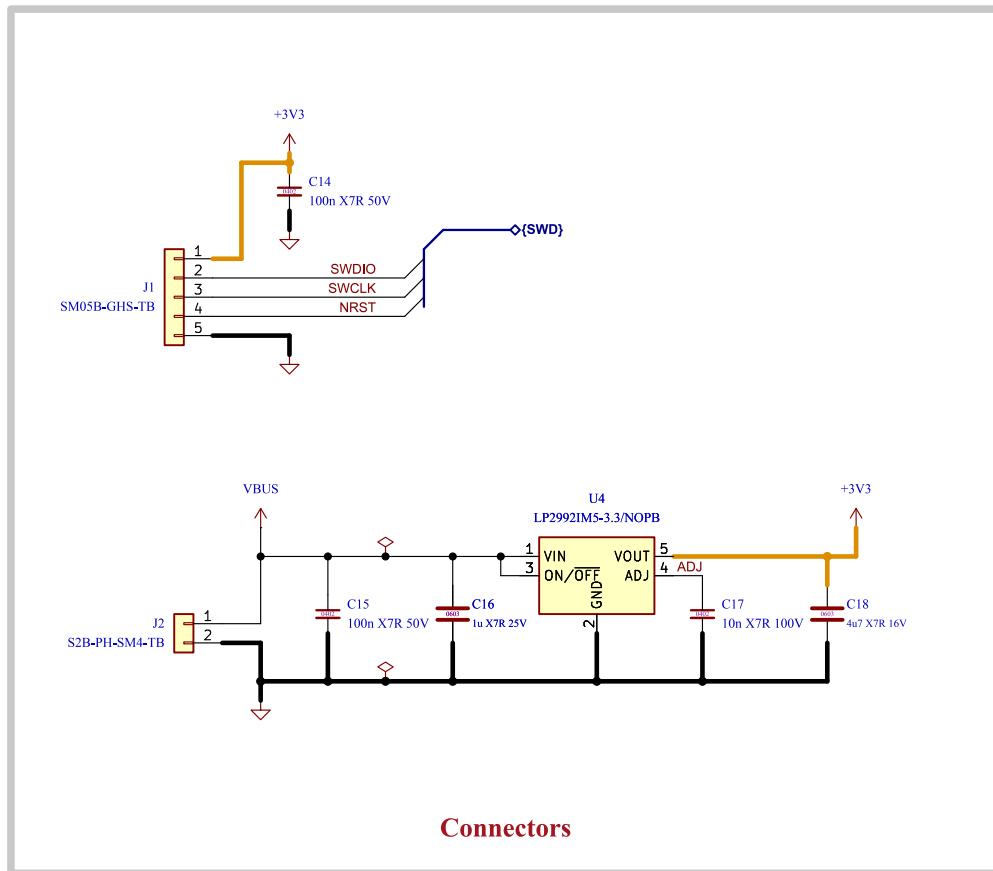


C

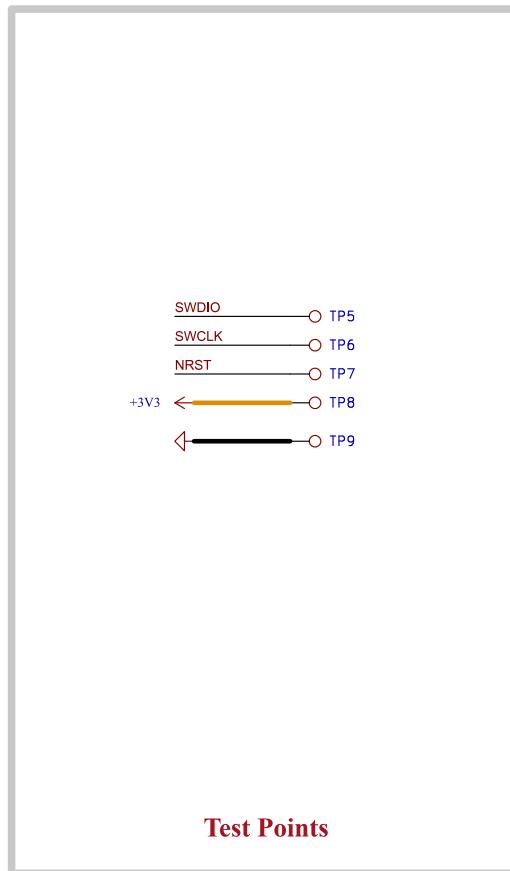
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		Sheet Title: IMU	File Name: IMU.kicad_sch	Designer: Author
		Date: 2025-01-12 Revision: + (Unreleased)		
		Sheet Path: /Project Architecture/IMU/		Reviewer:  Size: <b>A4</b> Sheet: <b>5</b> of <b>8</b>

# [6] Connectors

A



A



B

B

C

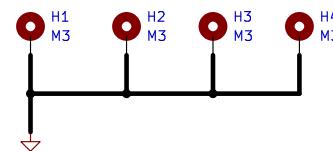
C

D

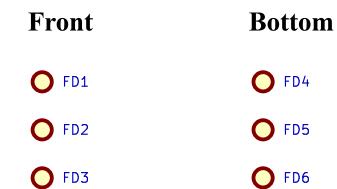
D

	Comments:  Company: Company Name	Variant:  PRELIMINARY	Git Hash:  77e9e33
	Board Name:  <b>Board Name</b>	Project Name:  <b>Project Name</b>	
	Sheet Title:  Connectors	File Name:  Connectors.kicad_sch	Designer:  Author
	Date:  + (Unreleased)		Revision:  Sheet:  <b>A4</b> <b>6</b> of <b>8</b>
1	2	3	4
5			6

# [7] Holes, Fiducials



Mounting Holes



Fiducial Markers

A

B

C

D

A

B

C

D

	Comments:	Company: Company Name	Variant: PRELIMINARY	Git Hash: 77e9e33
	Board Name: <b>Board Name</b>	Project Name: <b>Project Name</b>		
	Sheet Title: Holes, Fiducials	File Name: Holes, Fiducials.kicad_sch	Designer: Author	Date: 2025-01-24 Revision: + (Unreleased)
	Sheet Path: /Project Architecture/Holes, Fiducials/		Reviewer:	Size: <b>A4</b> Sheet: <b>7</b> of <b>8</b>

## [8] Revision History

	Comments:	Company:	Variant: PRELIMINARY	Git Hash:
		Company Name <b>Company Name</b>		77e9e33
	Board Name: <b>Board Name</b>		Project Name: <b>Project Name</b>	
Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: +(Unreleased)
Sheet Path: /Revision History/		Reviewer:	Size: <b>A4</b>	Sheet: <b>8</b> of <b>8</b>