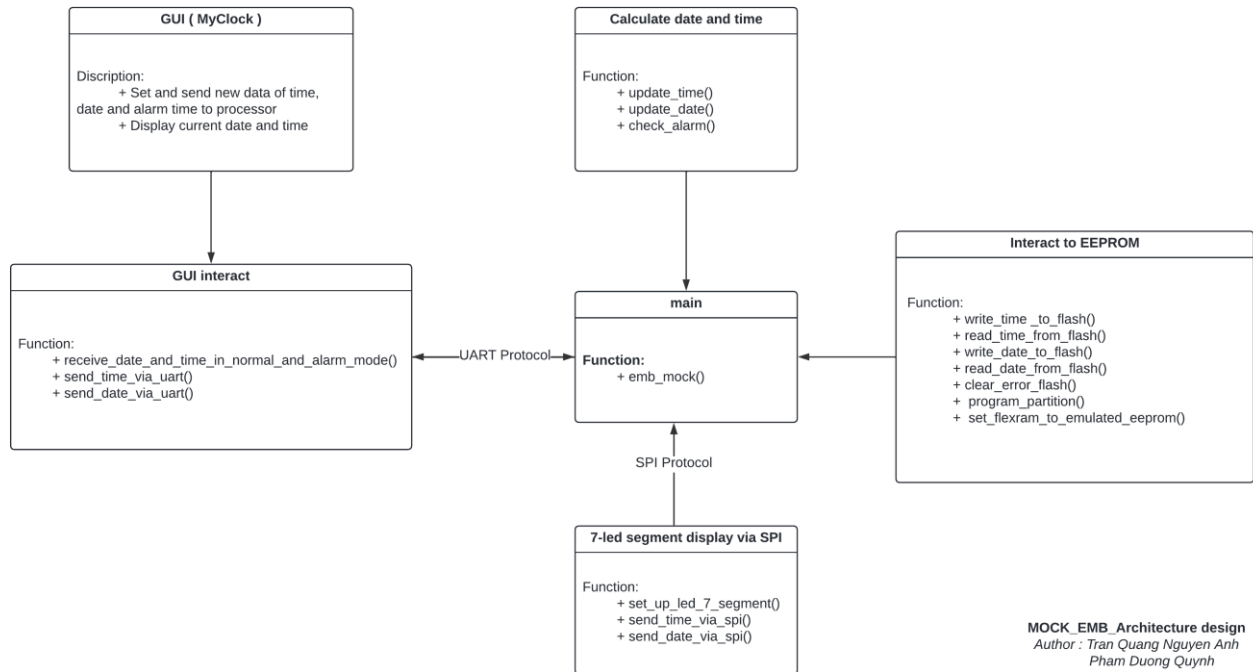


THE DIGITAL CLOCK DESIGN

*Author: Tran Quang Nguyen Anh, Pham Duong Quynh*

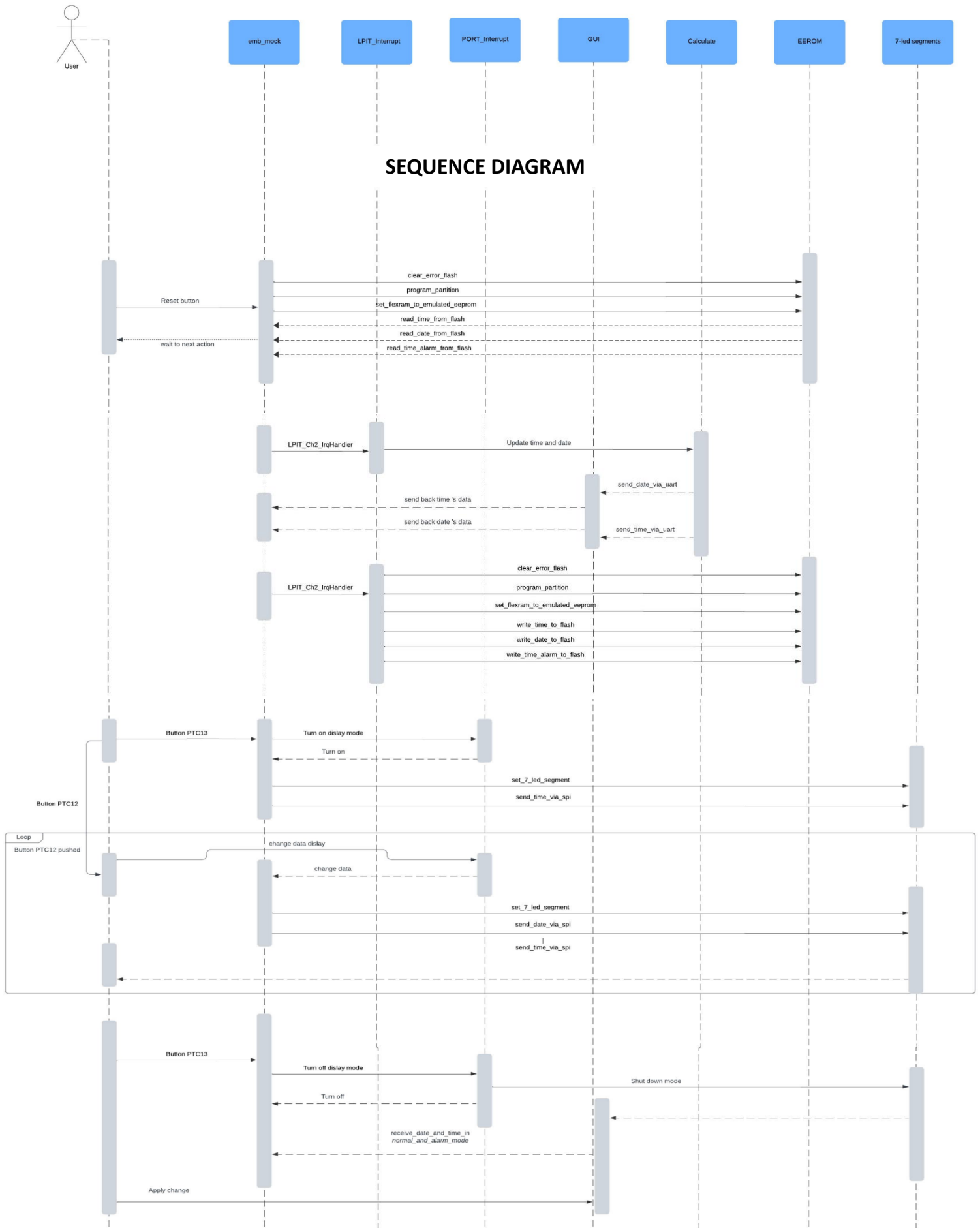
# **THE DIGITAL CLOCK - ARCHITECTURE AND DETAIL DESIGN S32K144EVB**

## ARCHITECTURE DESIGN



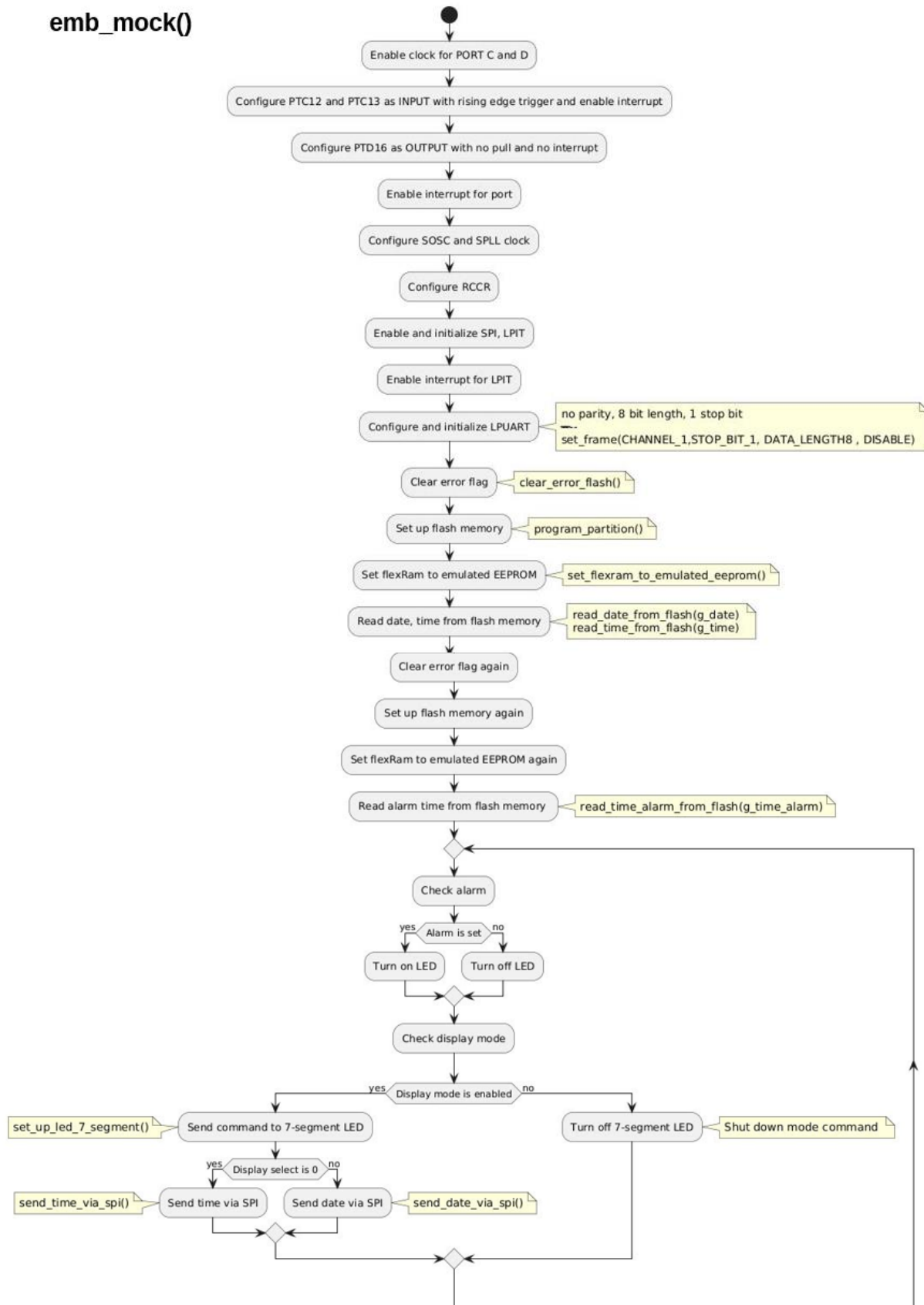
# THE DIGITAL CLOCK DESIGN

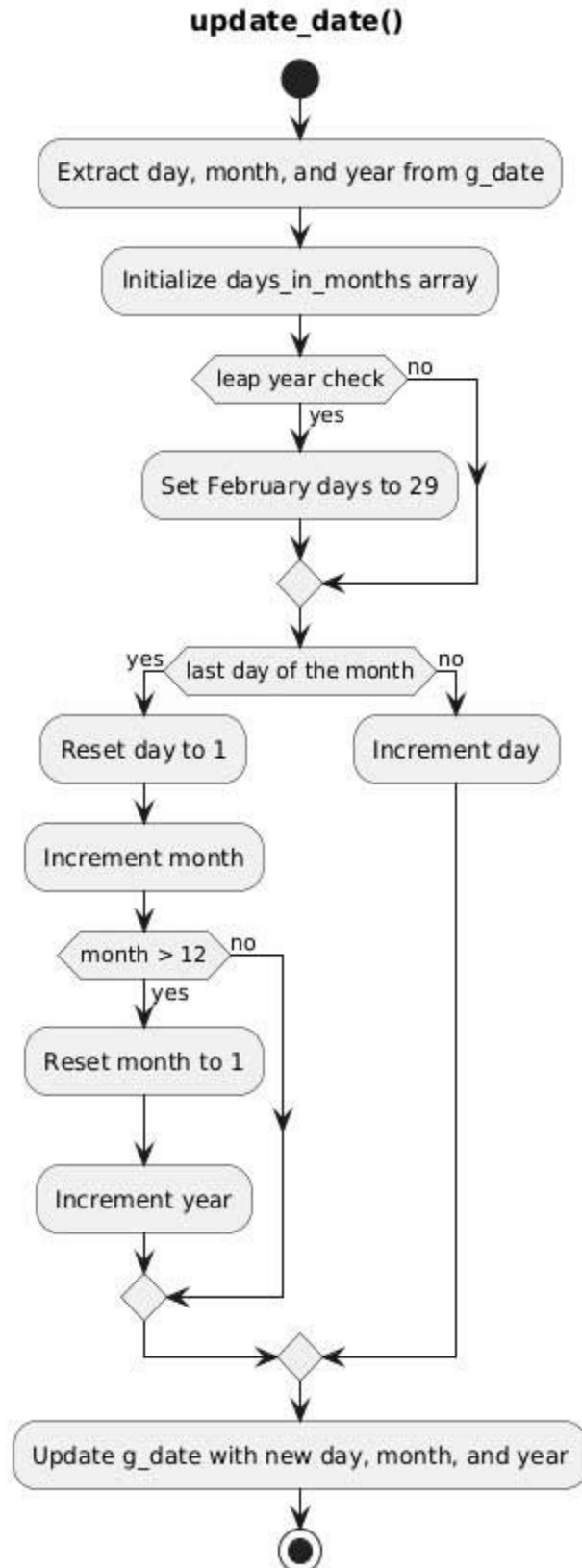
Author: Tran Quang Nguyen Anh, Pham Duong Quynh

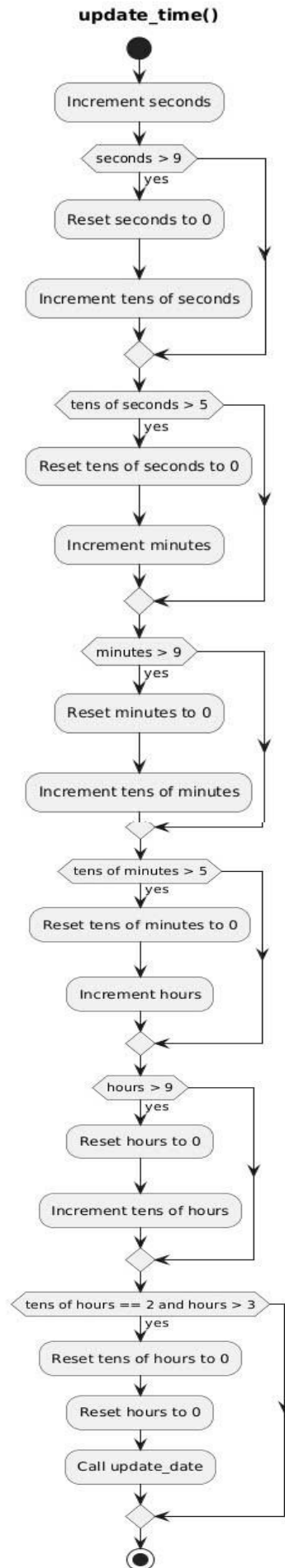


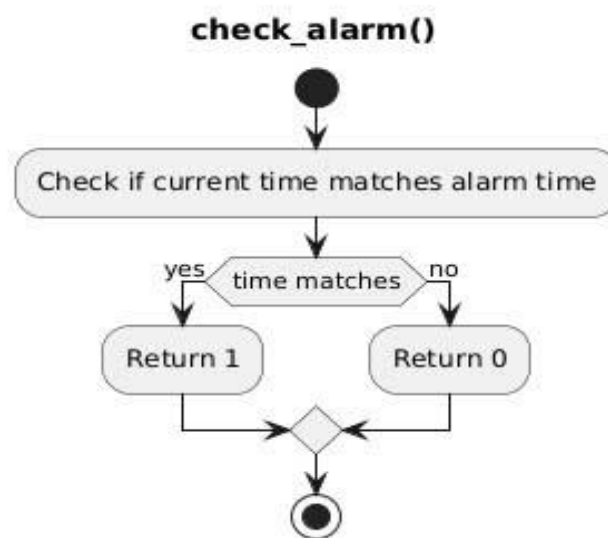
## DETAIL DESIGN

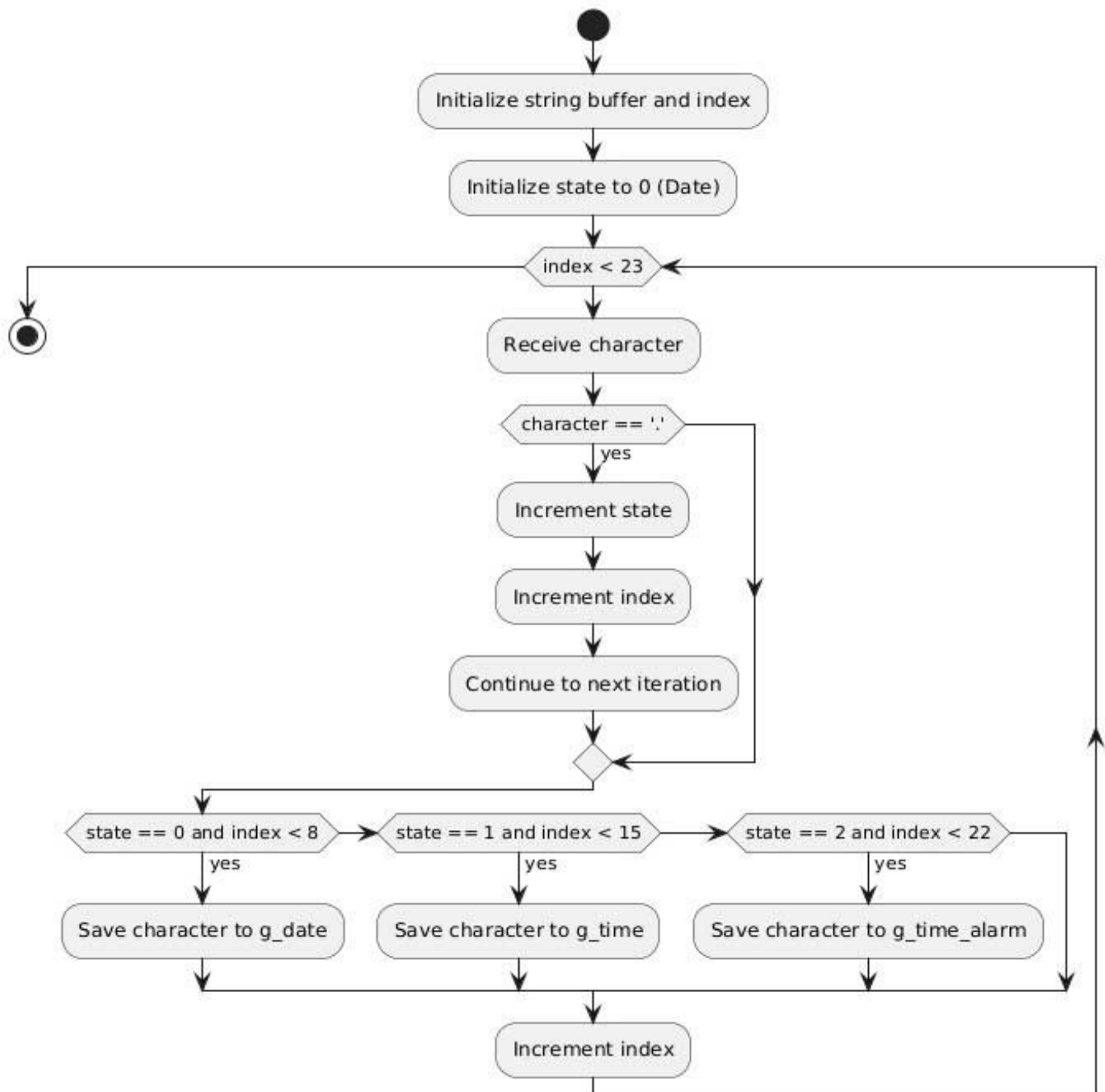
emb\_mock()



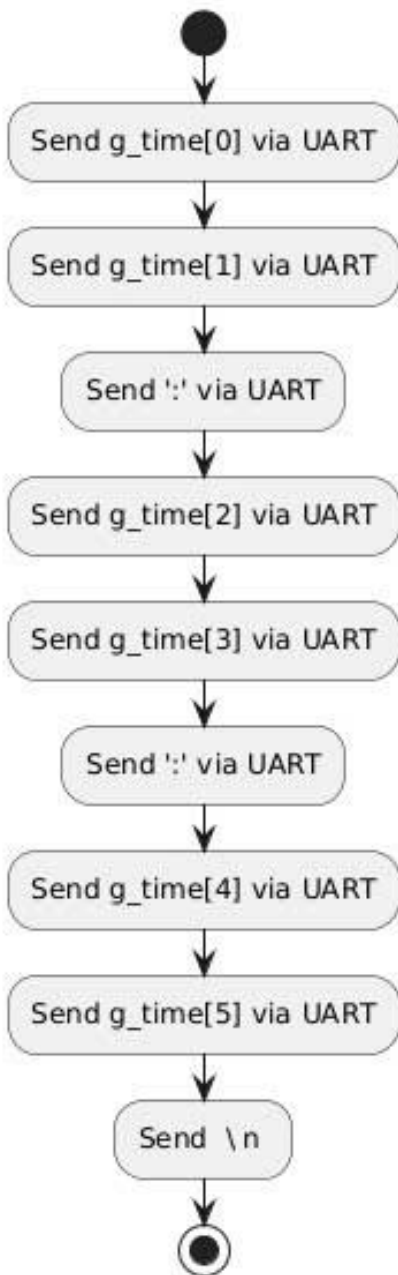
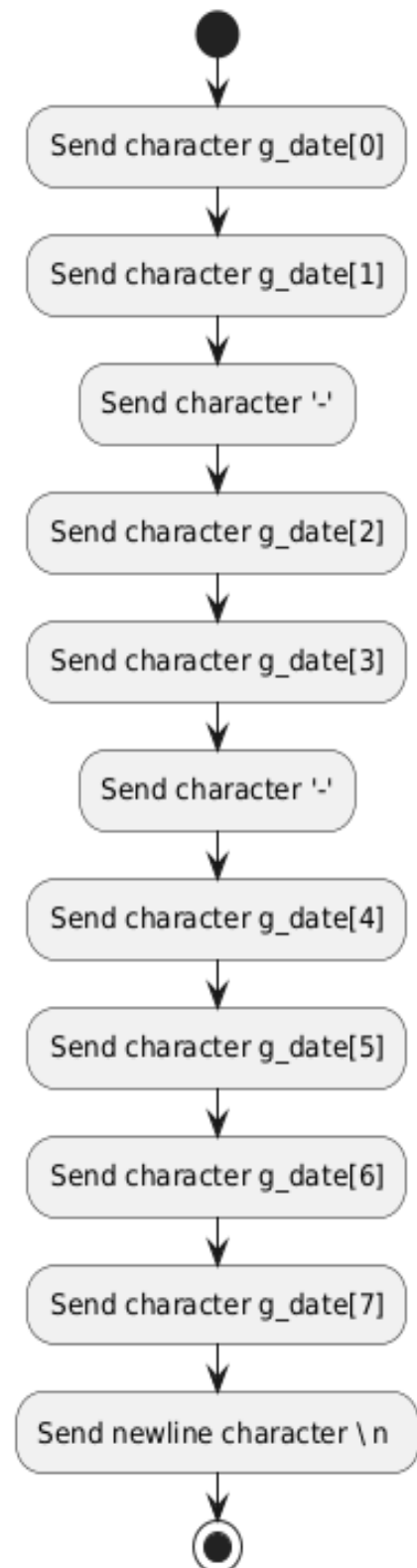




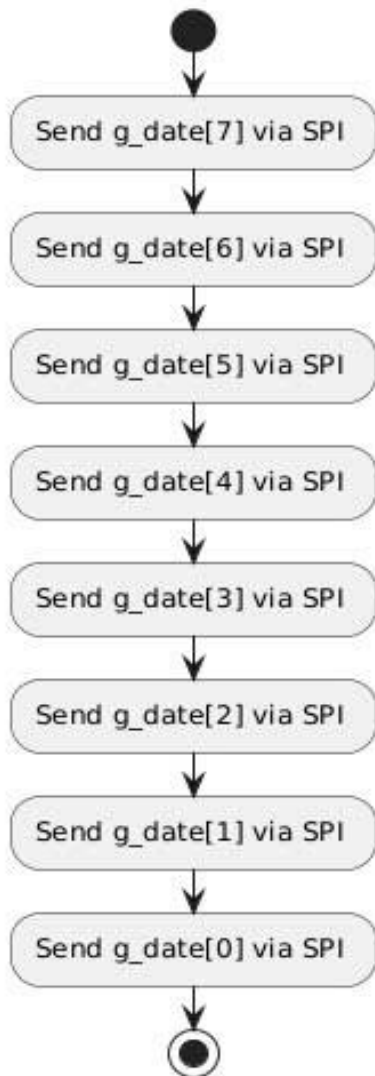


**receive\_date\_and\_time\_in\_normal\_and\_alarm\_mode()**

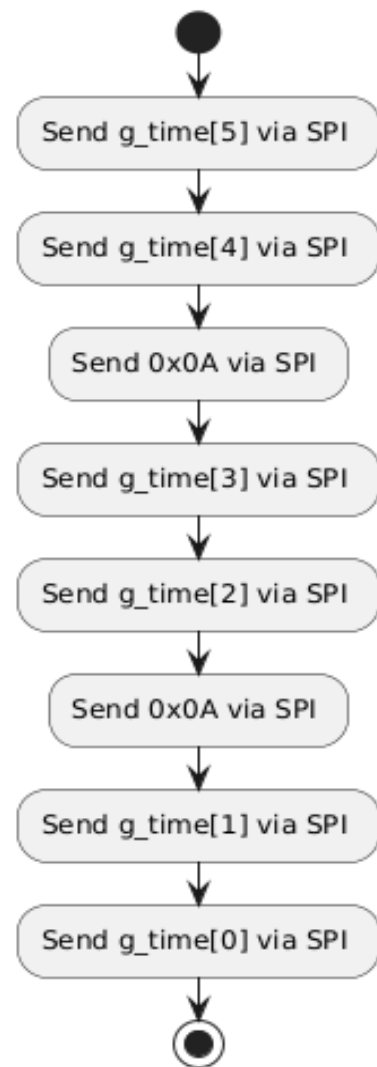


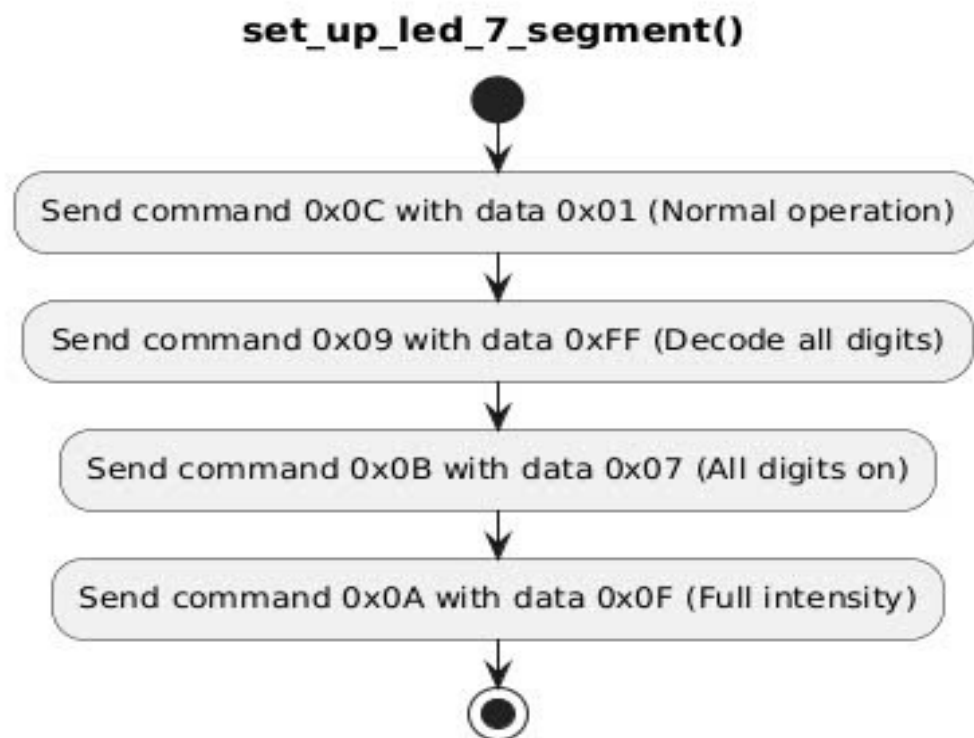
**send\_time\_via\_uart()****send\_date\_via\_uart()**

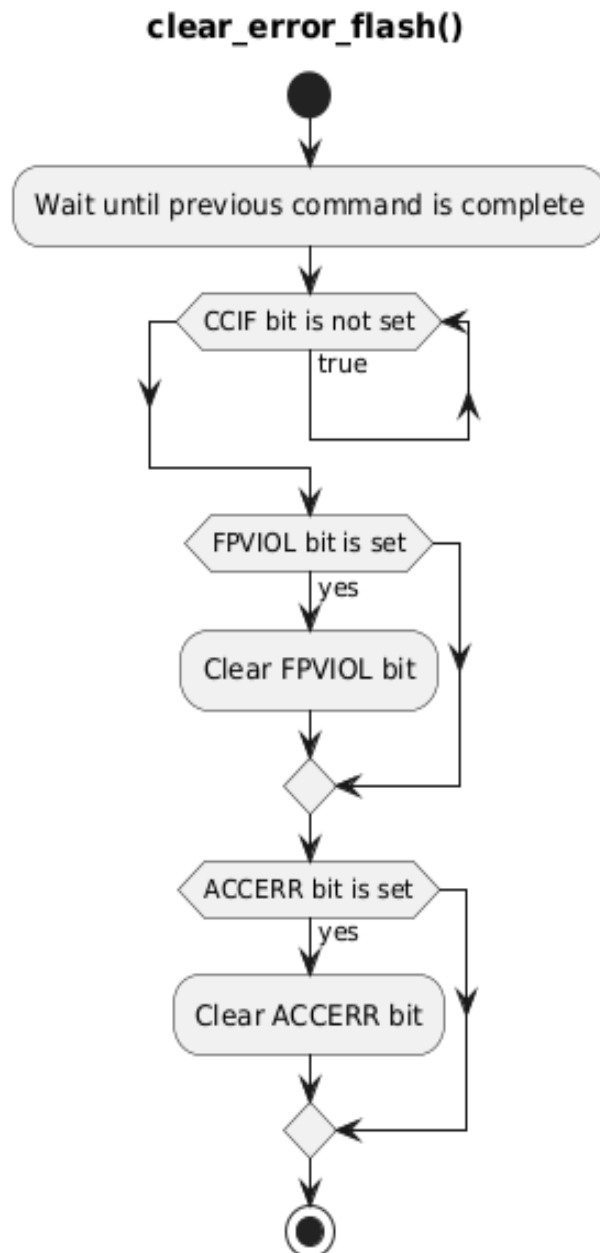
### send\_date\_via\_spi()

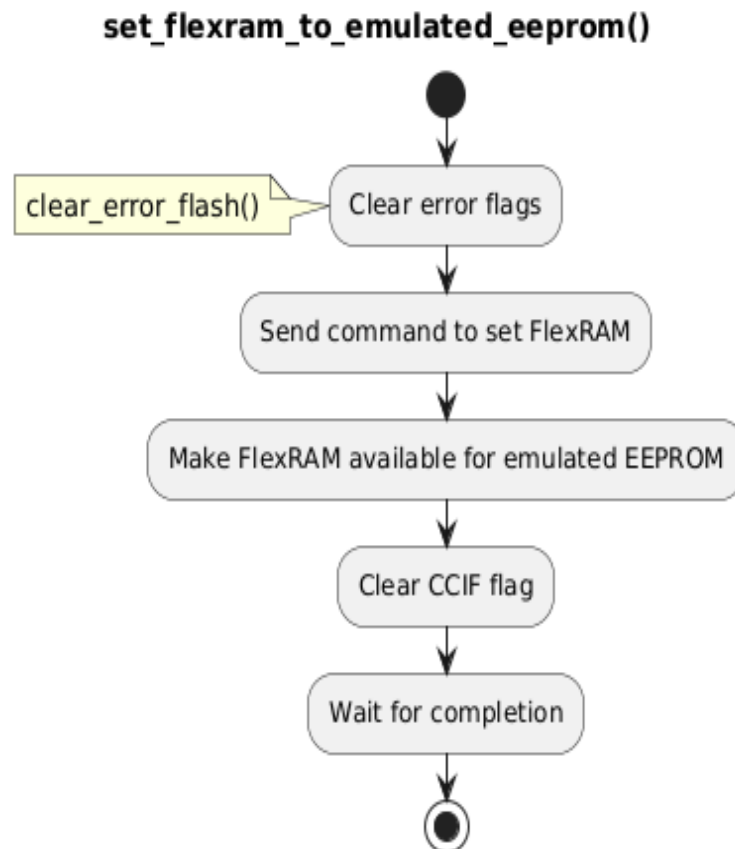


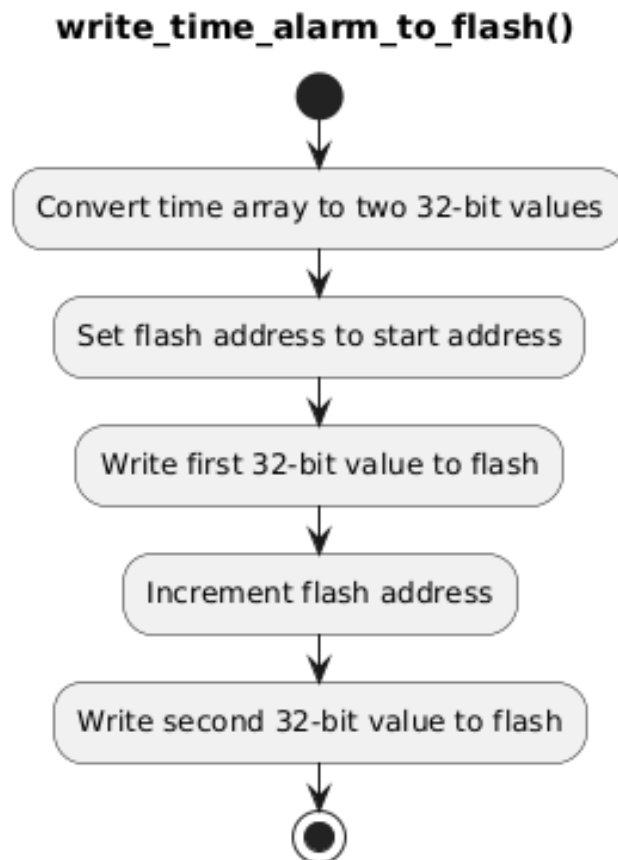
### send\_time\_via\_spi()

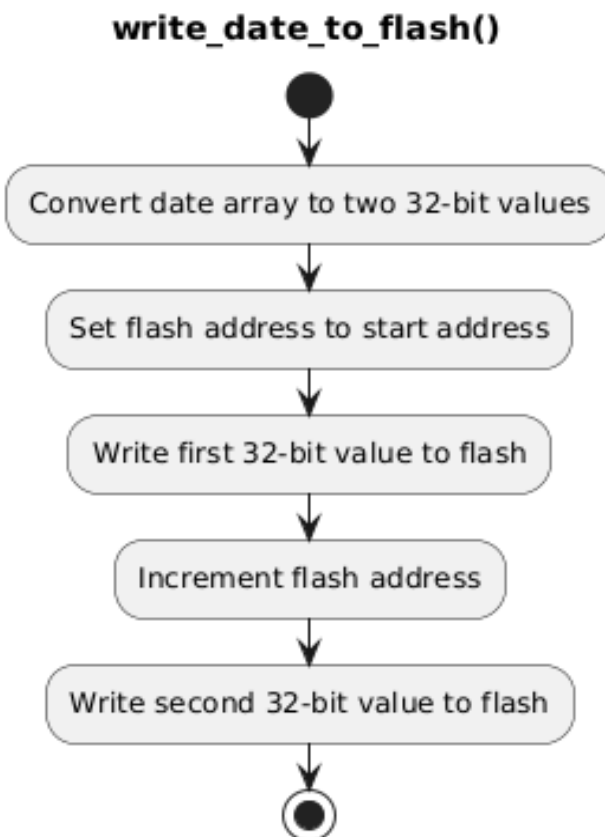
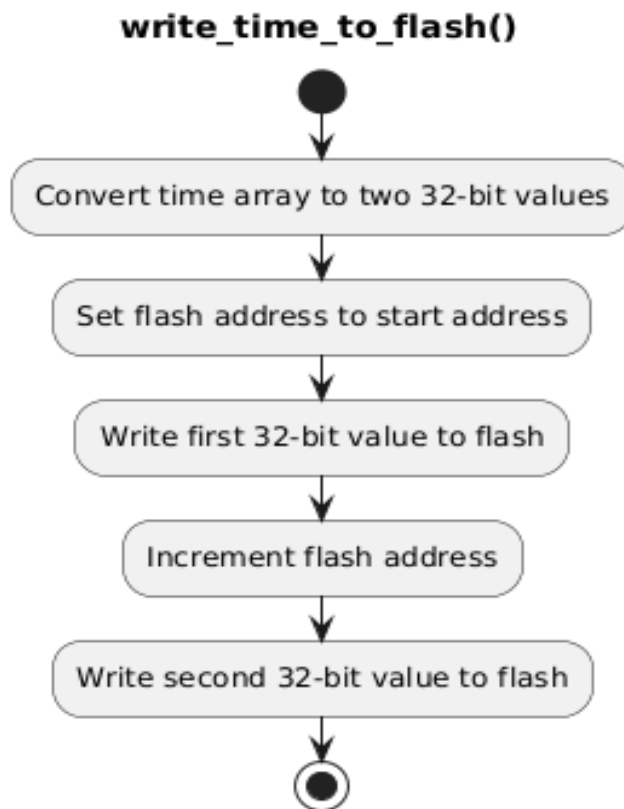




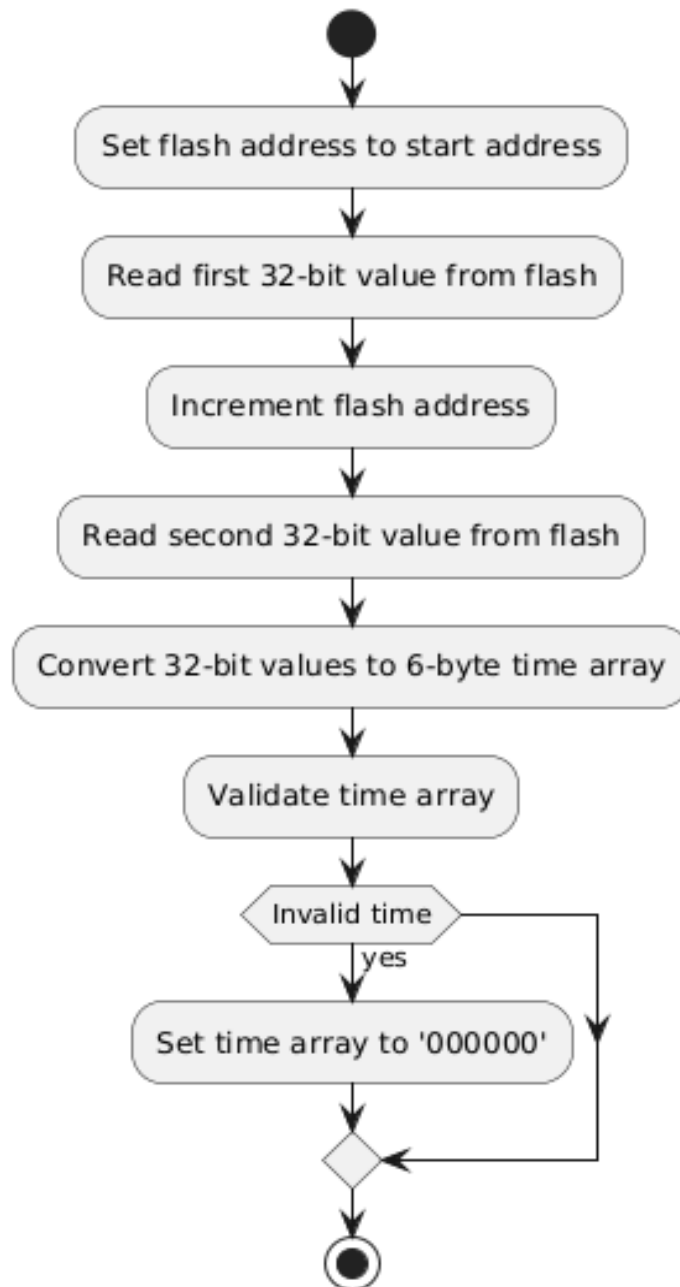




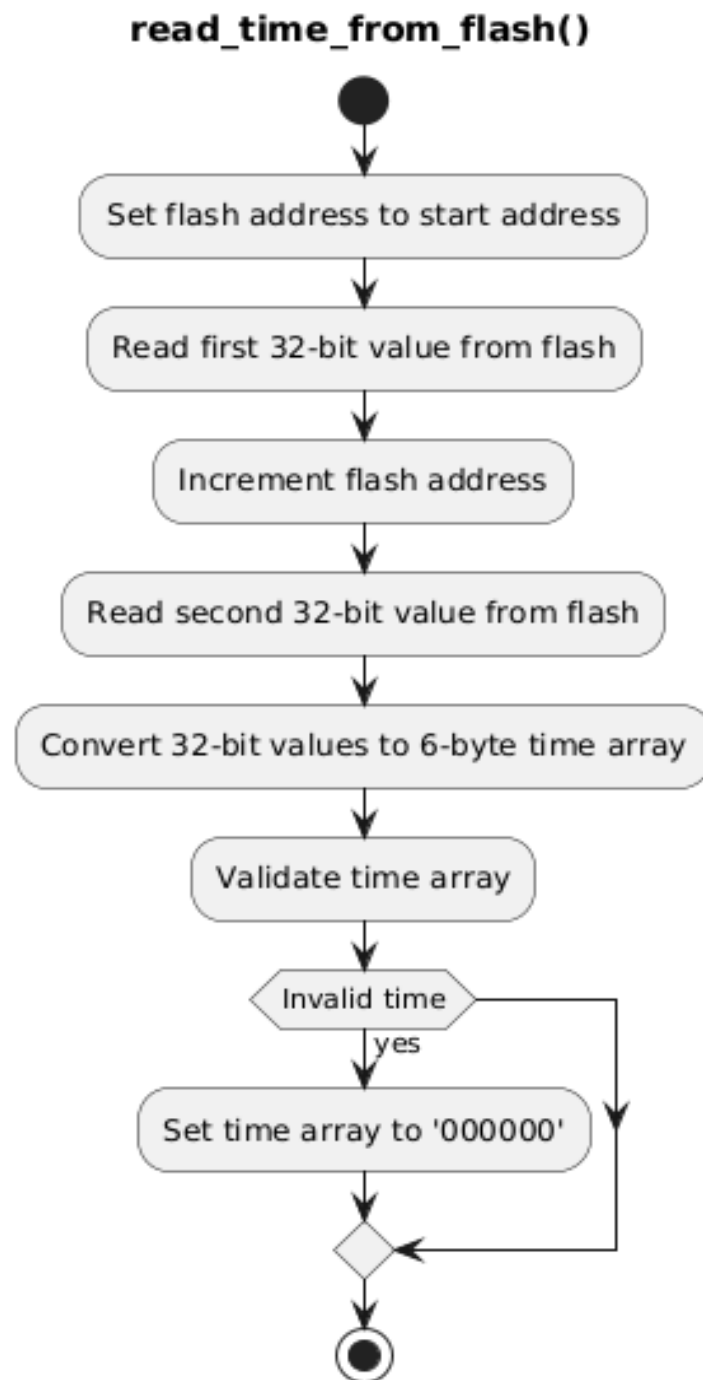


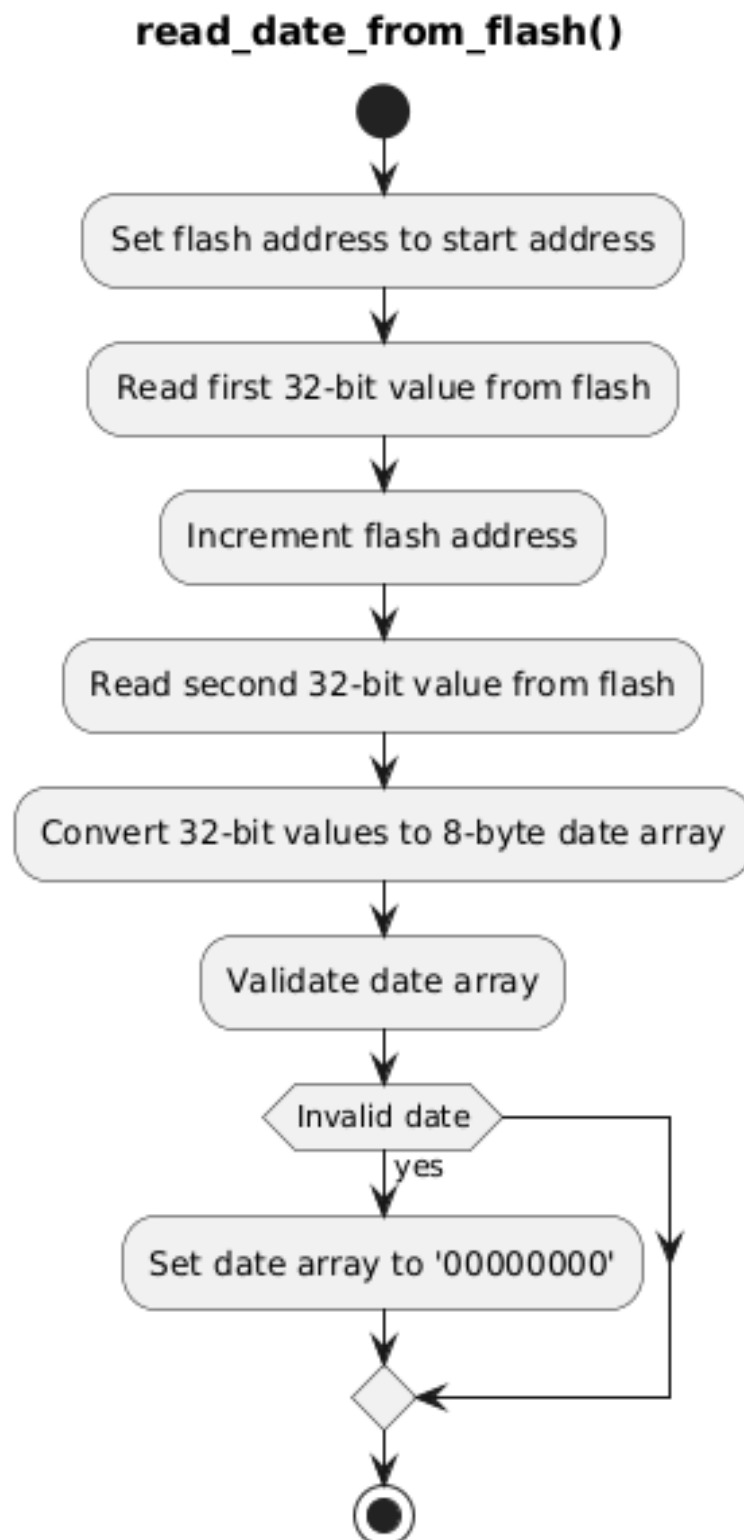


### read\_time\_alarm\_from\_flash()









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