PULP SOC JTAG debug module jtag_dti int_td jtag_dto ref_clk dmi_tap, dmi_jtag jtag_tap_top, lint_jtag_wrap SOC_CLK_RST_GEN s_lint_pulp_jtag_bus s_lint_riscv_jtag_bus TCDM Abiter soc_clk periph_clk s_lint_debug_bus s_lint_udma_rx/s_lint_udma_tx s_apb_perph_bus s_lint_fc_data_bus s_mem_l2_bus L2 RAM s_lint_fc_instr_bus s_lint_fc_data_bus s_mem_l2_pri_bus PERIPHERALS FC SUB_SYSTEM INTERCONNECT WRAP s_lint_fc_instr_bus apb_hwpe_bus M s_mem_rombus APU/FPU CORE **BOOT ROM** dm_top TCDM s_data_out_bus s_data_in_bus M: Master S: Slave AXI bus axi_slave_cdc axi_master_cdc CLUSTER **CLUSTER** bus BRIDGE CORE #0 CORE #1 CORE #2 CORE #3 CORE #4 CORE #5 CORE #6 CORE #7