# Nhan Le

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## Education

**Ho Chi Minh City University of Technology** — Ho Chi Minh, Vietnam
*B.Eng in Electrical and Electronics Engineering;* ***GPA: 3.19/4.00*** — *Sep 2015 – Oct 2019*

## Skills

**Languages:** C, Python, BashShell, MATLAB, Rush

**Technologies:** ARMv8-A, RISC-V, ACPI, IPMI, UEFI, SMBIOS, EDK2, BMC, Git, Docker, Jenkins

## Experience

**Ampere Computing** — Raleigh, North Carolina, The United States (Remotely)
*Firmware Engineer* — *April 2022 – Present, Full-time*

- Design, develop, test, and maintain firmware, bootloaders, device driver and manageablity solutions, features, and quality. Optimize firmware solutions. Upstream source code to open source communities and development partners
  - Bring-up EDK2 in AmpereOne™ Family
  - Apply industry standard: Standalone MM, MPAM, IPMI, ACPI, SMBIOS, ARM-SBSA
  - Enabling third-party drivers in Ampere EDK2: ASPEED GOP, ethernet card
  - Develop the signature Ampere's features
  - Enhance Ampere EDK2 to map with customers requirement: SPI performance, UEFI Menu.

**Ampere Computing** — Ho Chi Minh, Vietnam (Onsite)
*Platform Firmware Engineer* — *April 2020 – April 2022, Full-time*

- Support baseboard management controller (BMC) on Ampere's platform (Mt.Jade); focusing on MegaRAC firmware, and response for these features: dynamic fan control; sensor monitoring; manage CPLD (update firmware, get the user-code); PSU monitoring; log warning critical events from sensors, CPU, peripheral devices. Support FAE to verify the BMC features on a new platform; analyze issues that are reported by FAE team or ODM, maintain a robot system to find out the degraded bug
  - Modify source code for specific requests from customers: LED behavior for Ethernet port, IPMI command for Ampere platform
  - Fix unwilling issues on the Ampere platform
  - Work with industry standards likes IPMI 2.0, PmBus, etc which relates to sensor monitoring.

**Ho Chi Minh City University of Technology** — Ho Chi Minh, Vietnam (Onsite)
*Teaching assistant* — *June 2019 – March 2020, Full-time*

- Research the RISC-V architecture under the supervision of PhD. Tran Hoang Linh
- Response for laboratory's tasks under the supervision of Ms.Eng Bui Quoc Bao
- Co-operate with local companies to apply the technology on their products: L.E.D driver, Smart home devices

**2KLIC Holdings Inc** — Ho Chi Minh, Vietnam (Onsite)
*Platform Engineer* — *March 2018 – December 2018, Internship*

- Being familiar with Linux environment and shell scripts
- Being familiar with an embedded product manufacturing process
- Apply Long Range (LoRa) technology for smart agriculture
- Modify peripheral's driver for embedded computer RK328x SoC.

## Awards & Achievements

**Attend scientific research: Apply LoRa technology to manage the basic smart farm model** (Sep 2019)

**First Prize in the Design Contest Happy L.E.D 2016 for freshman at student research club (PIF club)**
Advisory Board: M.Eng Dang Anh Tung (January 24th 2016)

## Projects

**Thesis: Design LoRa Gateway**

- Add the LoRa management packages to OpenWrt OS on HLK7688 SoC
- Design board using HLK7688 (included UART, I2C, micro SD Card, Ethernet, SPI, USB)
- Grade: 9.33/10.

**Texas Instruments Innovation Challenge: Vietnam MCU Design Contest 2017**

- A member of GYRO team (One of eight teams went to final round)
- Leader: Mr. Phan Tai Toan