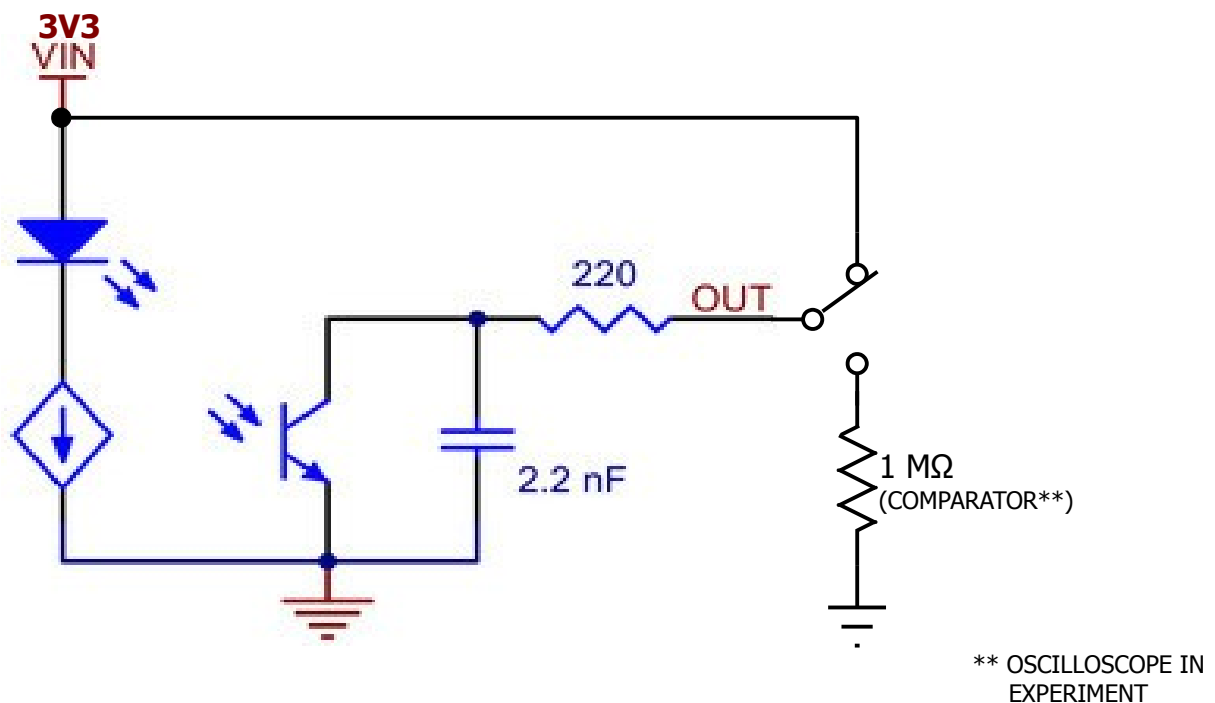


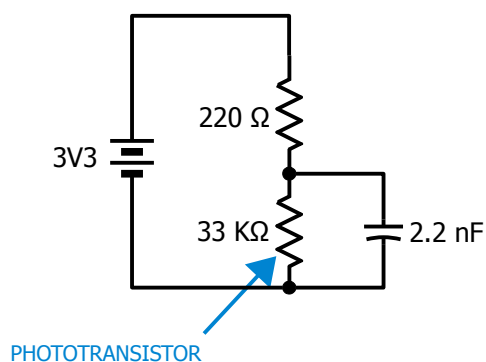
RSLK PATH DETECTION CIRCUIT

Two Phases in detection cycle, both with LED emitting.

Phase 1: Capacitor Charge-Up



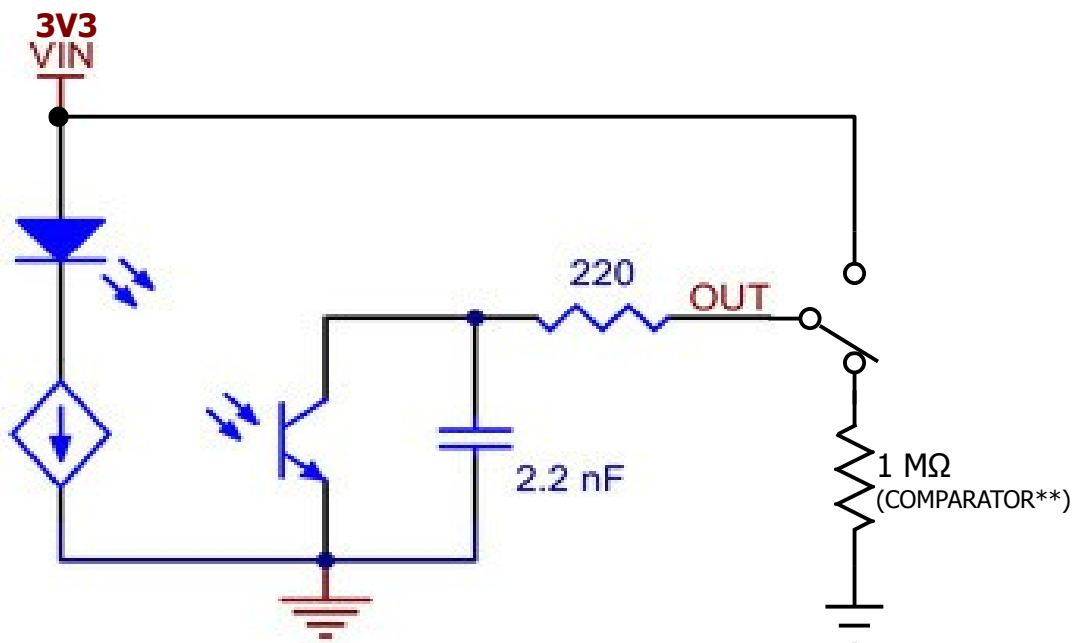
Voltage divider made with 220 Ω and phototransistor ($\sim 33 \text{ K}\Omega$)



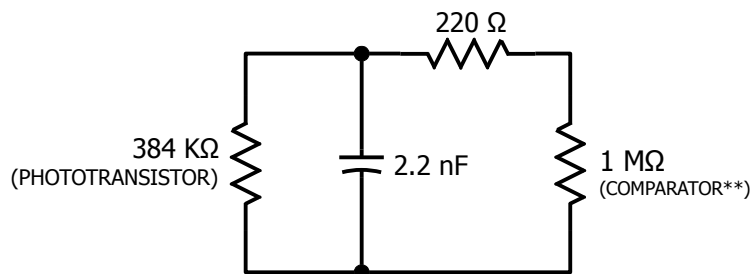
RSLK PATH DETECTION CIRCUIT

Two Phases in detection cycle, both with LED emitting.

Phase 2: Capacitor Discharge

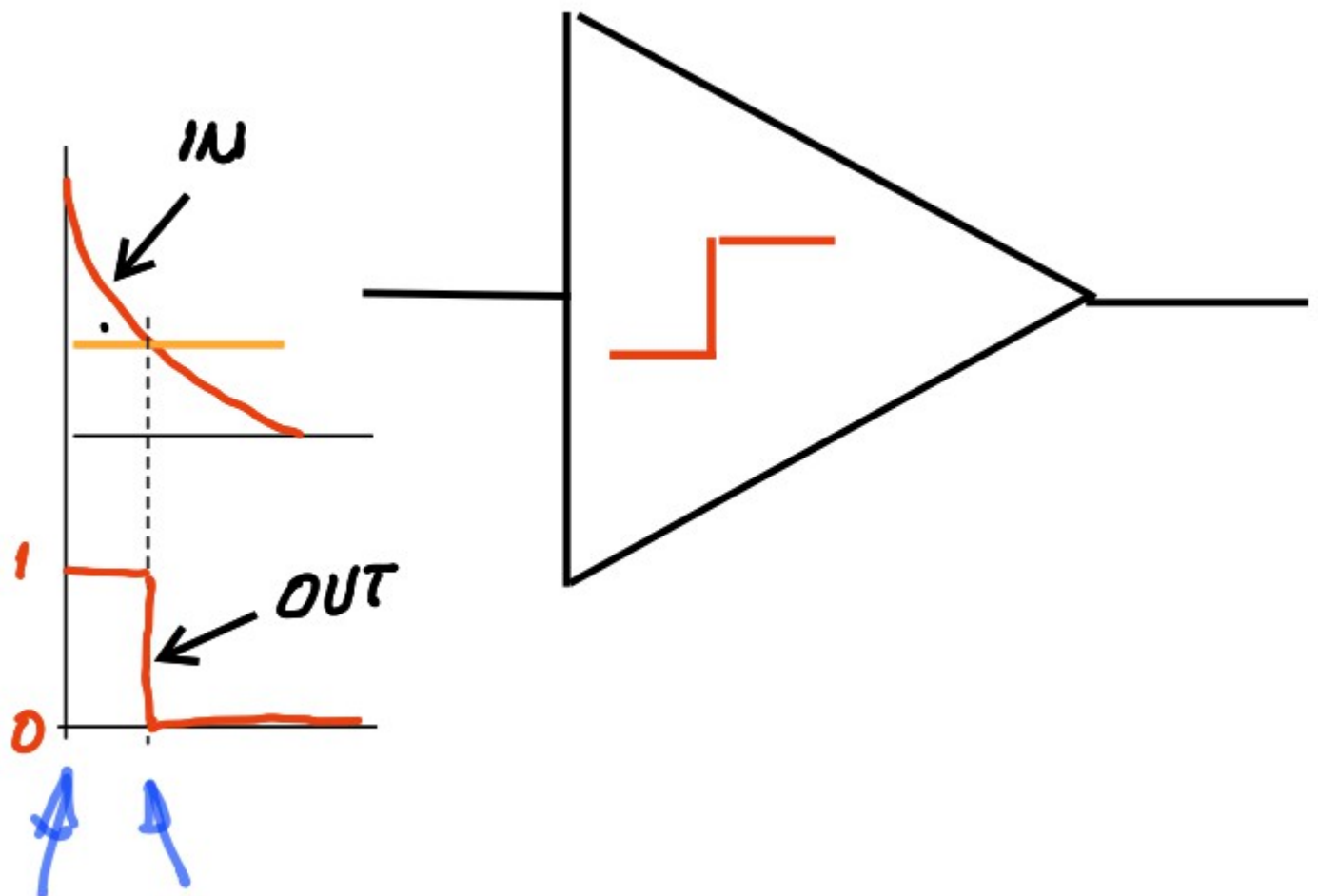
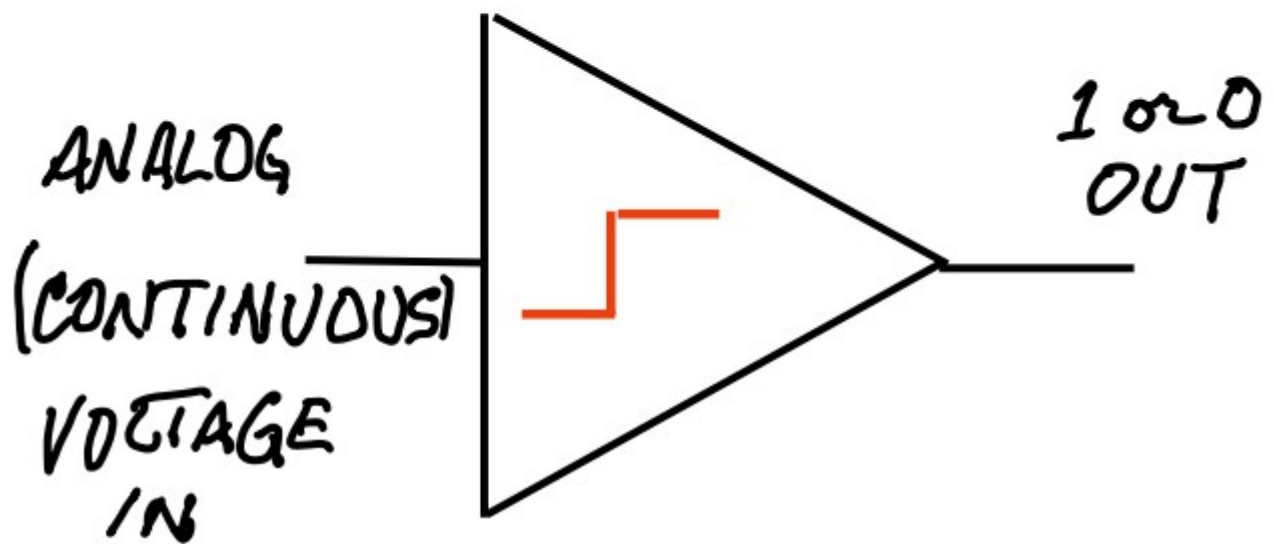


** OSCILLOSCOPE IN
EXPERIMENT



THE COMPARATOR

Think of it as a one-bit analog-to-digital converter (ADC).



CAPACITOR CHARGING & DISCHARGING

