

2. FPGA Bank. SchDoc

U1A	
	IO L1P HSWAPEN (144 FPGA_HSWAP
BANK 0	IO LIN VREF
Ė	IO 1 2P ★42 PIN142
A	
~	IO L3P S A2
	10_L3N_128 SA0
	10_L4P_ 127 S A 1
	IO_L4N S BS1
	IO_L34P_GCLK19_
	10_L34N_GCLK18
	10_L35P_GCLK17_132SCS
	IO_L35N_GCLK16
	IO_L36P_GCLI 10 L36P_GCLI 127 S CAS 126 S PAS
	IO_L36N_GCL_1 100 126 S RAS
	IO L37P GCLK1 0 224 S EDQW
	IO L37N GCLK12 S WE
	10_L62 121 S DQ6
	IO_L62N_VRE
	IO_L63P_SCP7
	IO_L63N_SCPC 118 S DQ5
	IO_L64P_SCPS OF 17 S DO2
	IO L64N SCP4 0 S DQ3
	IO L65P SCP3 0 S DQ1
	IO L65N SCP2 0 114 S DO0
	IO_L66P_SCP1_0 111 IOC SDA
	IO_L66N_SCP0_0 III IZC_SDA

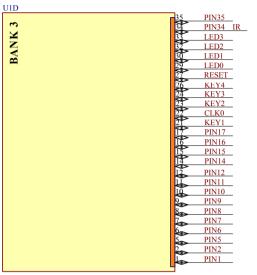
U1B		
	IO_L1P_	TXD
	10_E11_104	RXD
BANK	IO_LIN_VREF	PIN102
5	10_L32P_	PIN101
B	IO_L32N_	
	IO L33P IO L33N	PIN180
	IO L34P	PIN98
	IO L34N 97	PIN97
	IO_L40P_GCLK11_1	PIN95
	IO_L40N_GCLK10_03	PIN94
	IO L41P GCLK9 IRDY1	PIN93
	IO L41N GCLK8	PIN92
	IO L42P GCLK7	PIN88
	IO L42N GCLK6 TRDY1	PIN87
	IO_L43P_GCLK5_1	PIN85
	IO L43N GCLK4	PIN84
	IO L45P	PIN83
	IO_L45N_	PIN82
	IO L46P	PIN81
	IO L46N	PIN80
	IO_L47P_	PIN79
	IO_L47N_	PIN78
	IO_L74P_AWAKE_	PIN75
	IO L74N DOUT BUSY T	PIN74
	IO_L74N_DOUT_BUSY_	PIN74

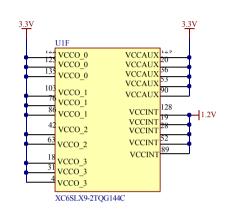
SPI CLK BANK

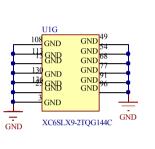
XC6SLX9-2TQG144C XC6SLX9-2TQG144C

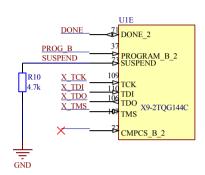
XC6SLX9-2TQG144C

3. FPGA Bank. SchDoc

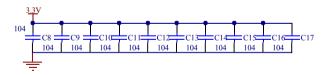


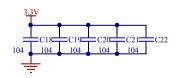


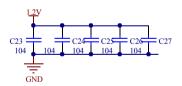




XC6SLX9-2TQG144C

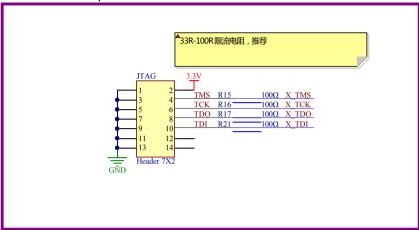




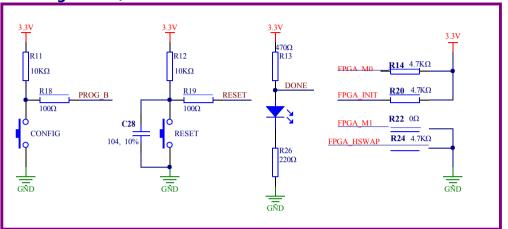


4. FPGA_Config_Jtag_Clk. SchDoc

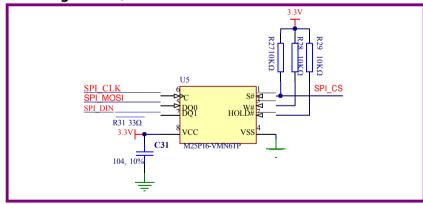
JTAG download port



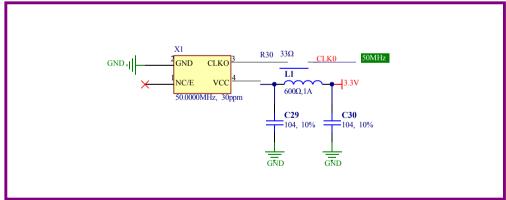
Reconfiguration, reset



Reconfiguration, reset

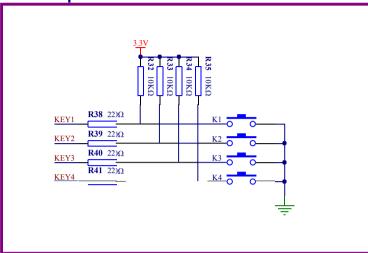


50M crystal oscillator

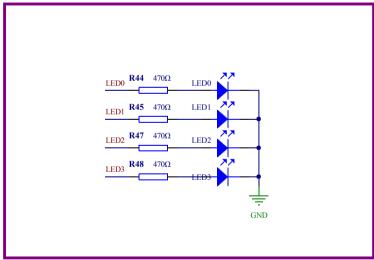


5. KEY_LED_IR_BEEP. SchDoc

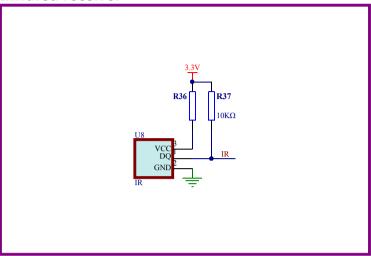
Four independent buttons



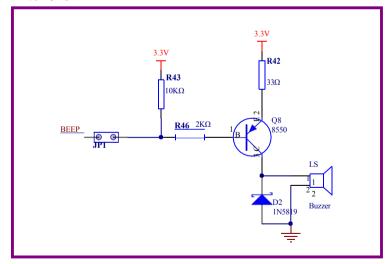
Four LED

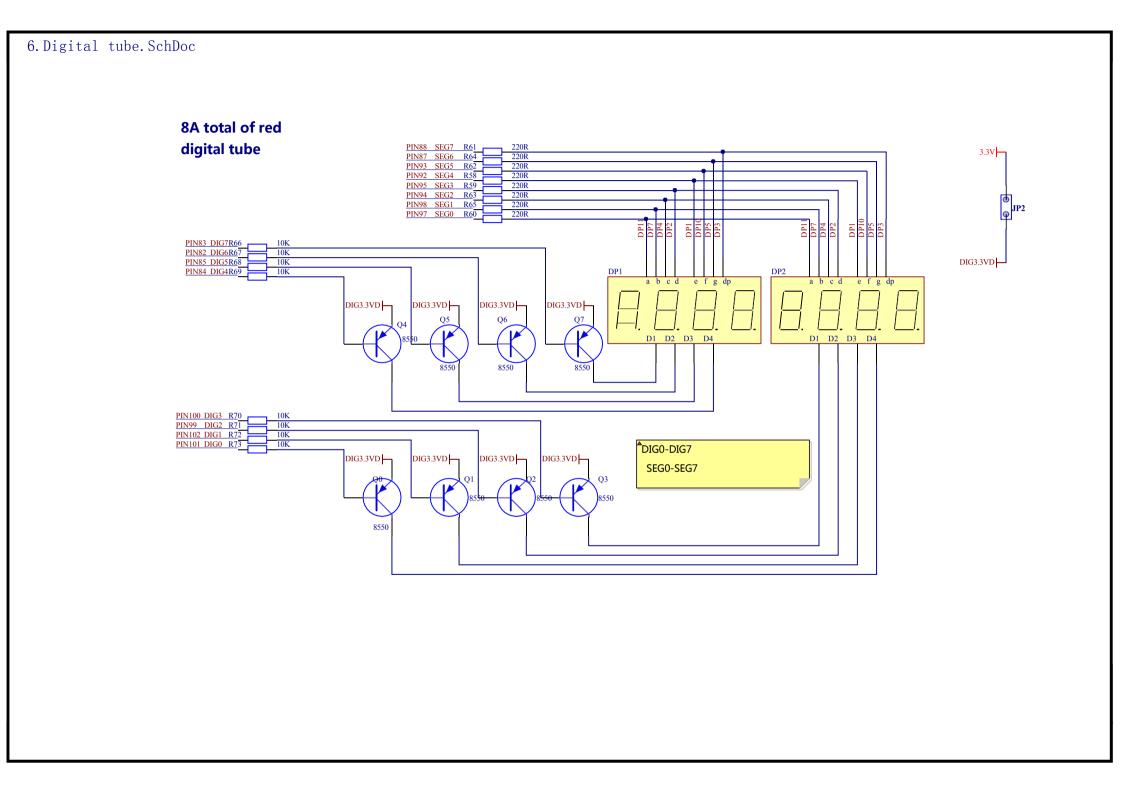


Infrared receiver



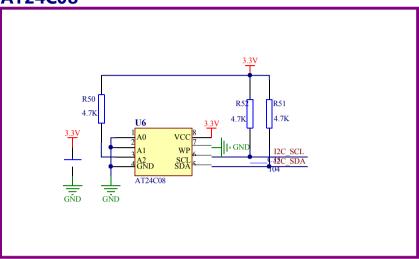
无源蜂鸣器



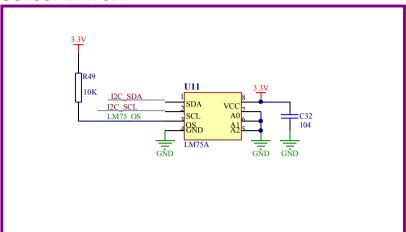


7. EEPROM_PCF8563_LM75A. SchDoc

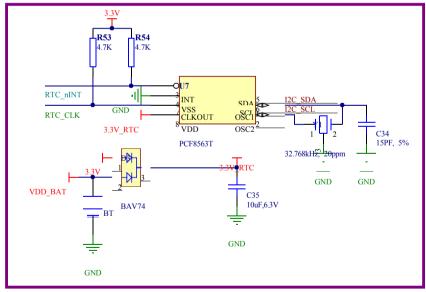
AT24C08



Sensor LM75A



Real time clock PCF8563

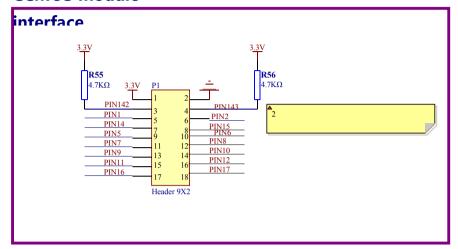


AExplanation: LThe M75A is more industrially practical than the DS18B2O, preferably the

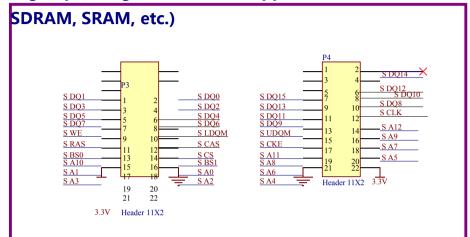
PCF8563 is an i2c interface with countdown, crossover output, interrupt and other functions, which is more accurate than DS1302! The DS1302 has a single function and is not a standard interface.

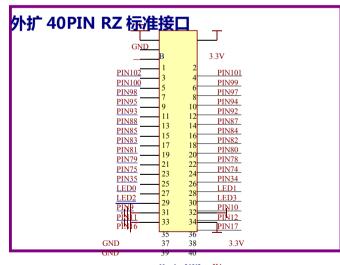


CCMOS module



High-speed signal interface (supports





Header 20X2 5V