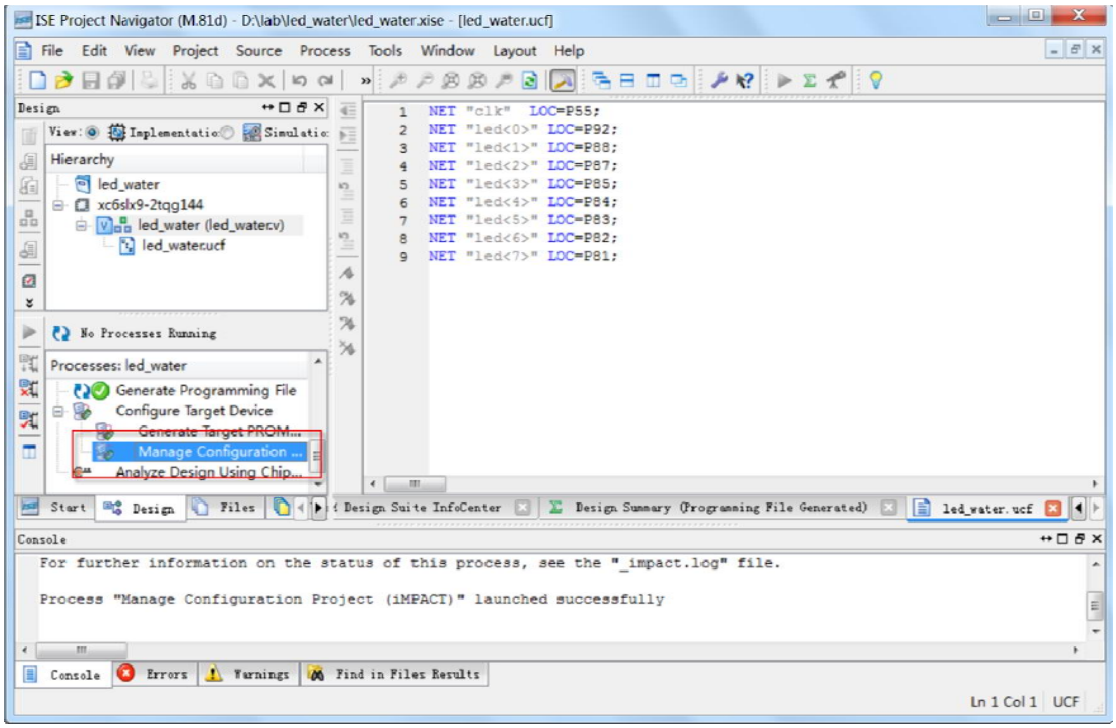
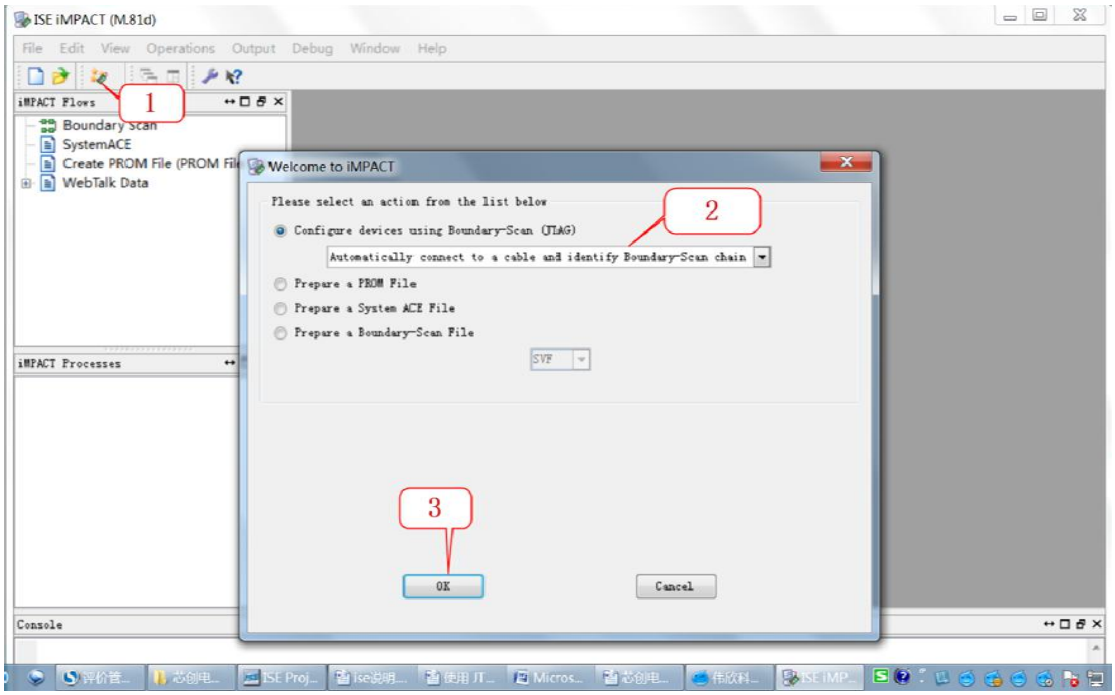
The newly created project interface appears after the previous step is completed.

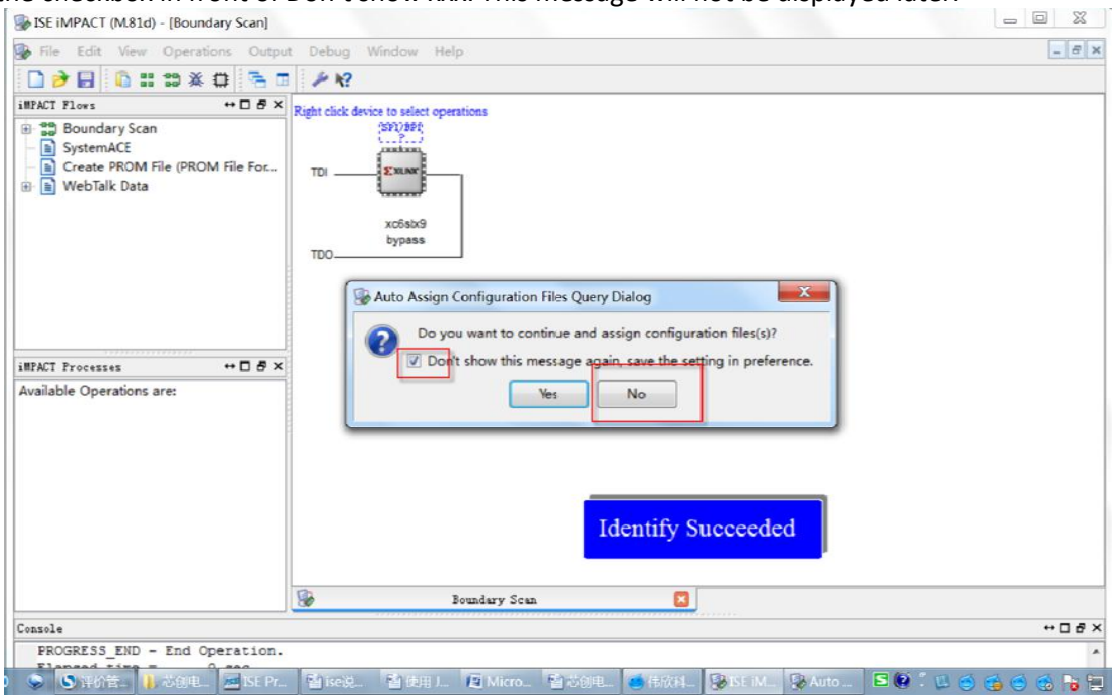
Connect the computer, development board and emulator. The power switch on the core board is turned on, powered on, and the power indicator is on. Double-click the ManageConfiguration Project in the project or find the IMPACT software in the Start menu and click Open.



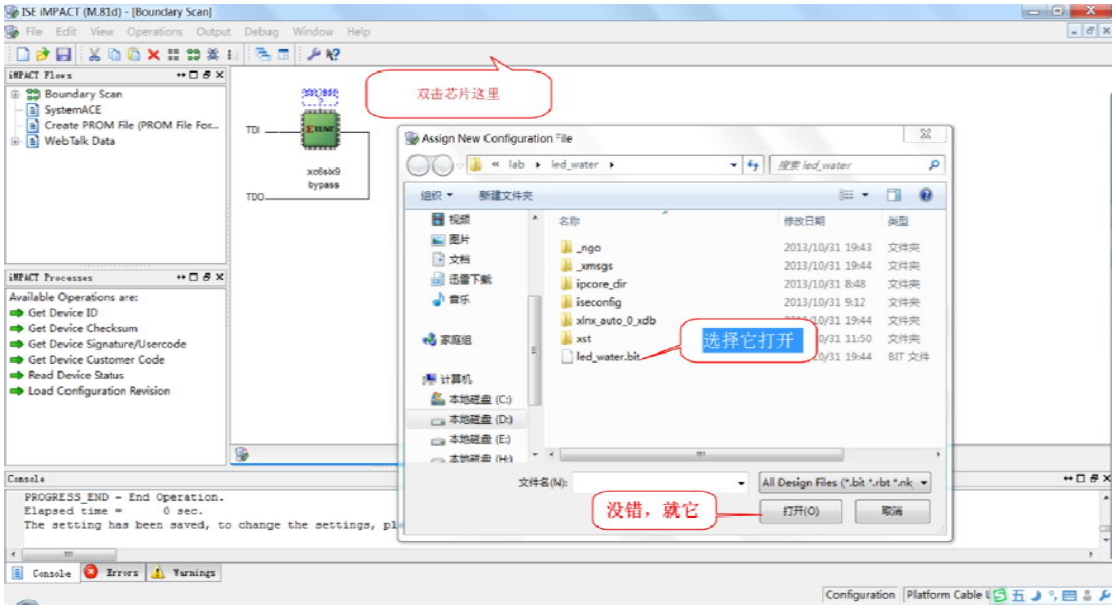
After entering the IMPACT software, we click on the icon 1 or in the File Select New Project, the option in the pop-up will pop up, because we are JTAG direct burning Bit
File, so select Configure devicesusing Boundary-Scan(JTAG) and click OK.



The selection box pops up in the figure below. Since we only have one FPGA device, select No and check the checkbox in front of Don't show xxx. This message will not be displayed later.

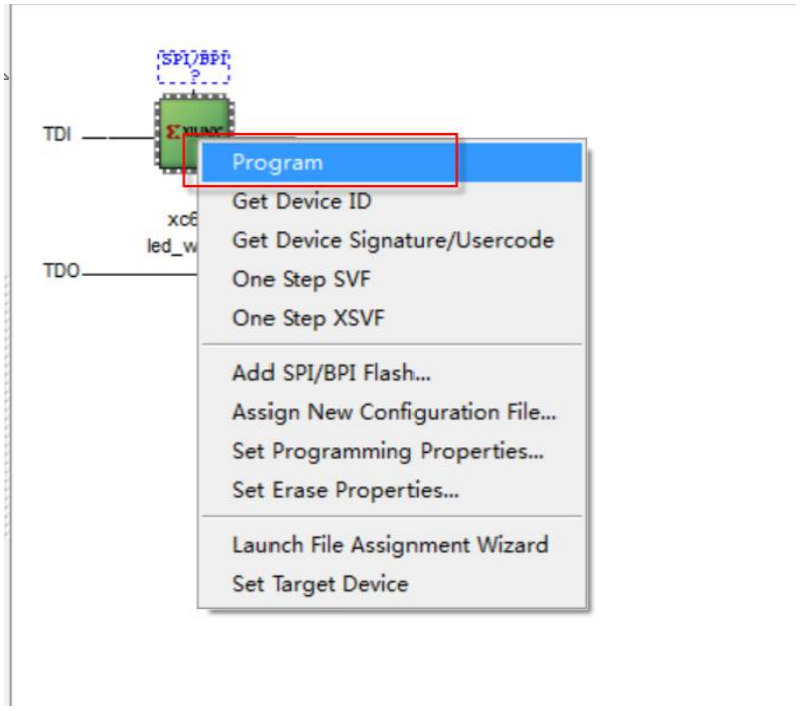


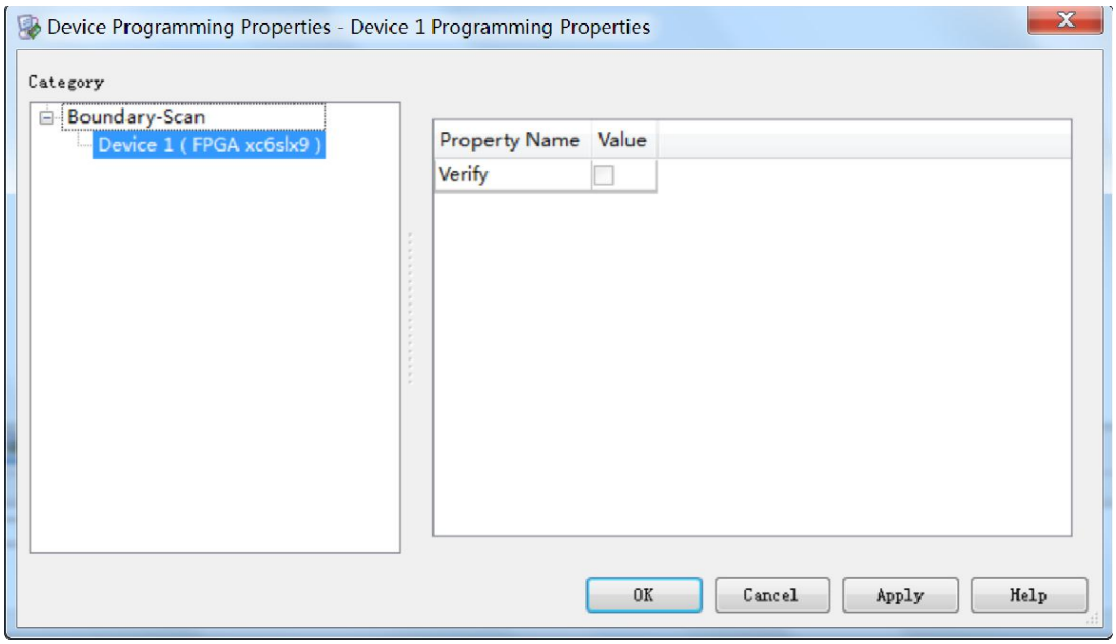
Double-click on the Xilinx chip on the diagram, a selection box will pop up. We select the generated bit file according to the path of the project and click Open.



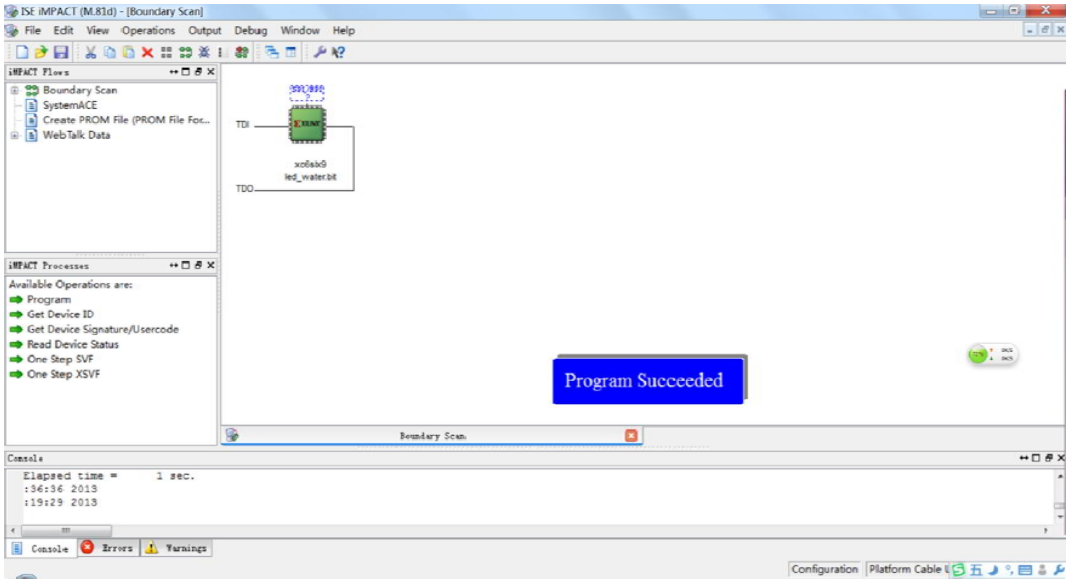
A prompt will pop up to add the SPI device. Since we have not yet generated the MCS file, we chose No. In the next section we will further explain the generation of MCS files and download methods.

Right click on the chip, select Program, download the program, and click OK to download.





After the download is complete, the IMPACT interface displays the following information, and the 8 LED lights of the development board run up.


ISE iMPACT (M.81d) - [Boundary Scan]
File Edit View Operations Output Debug Window Help
iMPACT Flows
Boundary Scan
SystemACE
Create PROM File (PROM File For...
WebTalk Data
iMPACT Processes
Available Operations are:
Program
Get Device ID
Get Device Signature/Usercode
Read Device Status
One Step SVF
One Step XSVF
TDI
TDO
xc6slx9
led_water.bit
Program Succeeded
Boundary Scan
Console
Elapsed time = 1 sec.
:36:36 2018
:19:29 2018
Console
Errors
Warnings
Configuration
Platform Cable