Nhuong M. Hoang

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⌨ github.com/nhuongmh

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📄 Summary

Software engineer with good knowledge in hardware, experienced in designing and developing multiple web projects. Currently focused on backend technologies, especially distributed and high-performance microservice applications.

🌱 Technical Skills

- **Languages**: Golang, Java, Python, C, C++, Shell/Bash Script
- **Frameworks & Tools**: Gin, Spring/Spring Boot
- **Database**: MySQL, SQLite3, PostgreSQL, MongoDB
- **Others**: Low level hardware/software knowledge, GNU Linux Kernel, Docker
- **English**: TOEIC Certificate 780 (Jun 2018)

🔭 Technical Experience

Ampere Computing
Senior Software Engineer

Dec, 2018 - *present*
HCMC, VN

Responsible for building test infrastructure through multiple projects to support testing ARM64-based server systems

- **Platform Validation Test Tool**
  - ‣ Microservices project to manage server system under test (SUTs), launching agents to connect and perform testing on SUTs
  - ‣ Stack: Java Spring Boot, Golang, VueJS, Websocket, MySQL, Bash/Powershell, Python, Docker, Apache ActiveMQ
  - ‣ Designed framework architecture, implemented and maintained core features
  - ‣ Continuously optimizing framework to help it scale horizontally as number of SUTs increased
  - ‣ Build up CI/CD pipelines to automatically build and test for changes, generate package releases for customers. (Uses Gitlab CI, Earthly, Docker)
  - ‣ Take part in resolve open sources libraries and 3rd parties components licenses in order to ship package to customers
  - ‣ Gain knowledge on server systems hardware and software, debugs issue on Linux
- **Virtual Systems Managing System**
  - ‣ Web applications to generate and manage virtual systems for testing; each virtual system can mimic behavior of real system through consoles based on user configuration
  - ‣ Stack: Go with Gin, VueJS with Vuetify, MongoDB
  - ‣ Designed framework and was the main developer of the project
  - ‣ Used this tool to collaborate and help internal teams boost up development/testing progress
- **Test Data Analytics**
  - ‣ Web application to collect and analyze all past test data, virtualizing results on a dashboard for monitoring
  - ‣ Stack: Elastic Search, Apache Airflow, Grafana
  - ‣ Developed tools to collect test data, setup Apache Airflow to schedule and monitor overall workflows

Arrive Technologies
Logic Design Engineer

Jun, 2018 - Dec, 2018
HCMC, VN

- Joined design and implementation logic circuit targeted in telecommunication application (SONET/SDH) using Verilog
- Implemented firmware for telecommunication chips on FPGA-based embedded processors using C++

🎓 Education

Electronics & Telecommunications Engineering

Ho Chi Minh University of Technology

BEng

2014 - 2018