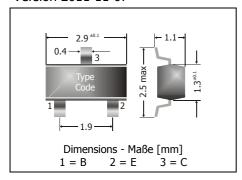


### BC856 ... BC860

# PNP Surface Mount General Purpose Si-Epi-Planar Transistors Si-Epi-Planar Universaltransistoren für die Oberflächenmontage

Version 2011-11-07



Power dissipation – Verlustleistung 250 mW

Plastic case SOT-23

Kunststoffgehäuse (TO-236)

Weight approx. – Gewicht ca. 0.01 g

Plastic material has UL classification 94V-0 Gehäusematerial UL94V-0 klassifiziert

Standard packaging taped and reeled Standard Lieferform gegurtet auf Rolle



**PNP** 

## Maximum ratings $(T_A = 25^{\circ}C)$

Grenzwerte ( $T_A = 25$ °C)

			BC856	BC857 BC860	BC858 BC859	
Collector-Emitter-volt. – Kollektor-Emitter-Spannung	B open	- V <sub>CEO</sub>	65 V	45 V	30 V	
Collector-Base-voltage – Kollektor-Basis-Spannung	E open	- V <sub>CBO</sub>	80 V	50 V	30 V	
Emitter-Base-voltage – Emitter-Basis-Spannung	C open	- V <sub>EBO</sub>	5 V			
Power dissipation – Verlustleistung		P <sub>tot</sub>	250 mW <sup>1</sup> )			
Collector current – Kollektorstrom (dc)		- I <sub>C</sub>	100 mA			
Peak Collector current – Kollektor-Spitzenstrom		- I <sub>CM</sub>	200 mA			
Junction temperature – Sperrschichttemperatur Storage temperature – Lagerungstemperatur		$T_{j}$ $T_{S}$	-55+150°C -55+150°C			

# Characteristics $(T_j = 25^{\circ}C)$

### Kennwerte ( $T_j = 25$ °C)

			Min.	Тур.	Max.
DC current gain – Kollektor-Basis-Stromverhältnis					
- $V_{CE}$ = 5 $V_{r}$ - $I_{C}$ = 10 $\mu A$	Group A Group B Group C	H <sub>FE</sub> h <sub>FE</sub> h <sub>FE</sub>	- - -	90 150 270	- - -
$- V_{CE} = 5 V, - I_{C} = 2 mA$	Group A Group B Group C	H <sub>FE</sub> h <sub>FE</sub>	125 220 420	180 290 520	250 475 800
Collector-Emitter saturation voltage – Kollektor-Sa					
$I_{\text{C}}=10$ mA, $I_{\text{B}}=0.5$ mA $I_{\text{C}}=100$ mA, $I_{\text{B}}=5$ mA		- V <sub>CEsat</sub>	_ _	- -	300 mV 650 mV
Base-Emitter saturation voltage – Basis-Sättigungsspannung <sup>2</sup> )					
$I_{\text{C}}=10$ mA, $I_{\text{B}}=0.5$ mA $I_{\text{C}}=100$ mA, $I_{\text{B}}=5$ mA		- V <sub>BEsat</sub> - V <sub>BEsat</sub>	1 1	700 mV 900 mV	- -

Mounted on P.C. board with 3 mm² copper pad at each terminal Montage auf Leiterplatte mit 3 mm² Kupferbelag (Lötpad) an jedem Anschluss

<sup>2</sup> Tested with pulses  $t_p = 300~\mu s$ , duty cycle  $\leq 2\%$  — Gemessen mit Impulsen  $t_p = 300~\mu s$ , Schaltverhältnis  $\leq 2\%$ 



Characteristics ( $T_j = 25^{\circ}C$ )			Kennwerte ( $T_j = 25$ °C)			
			Min.	Ту	/p.	Max.
Base-Emitter-voltage – Basis-Emitter-Spannung <sup>2</sup> )						
- $V_{CE}$ = 5 V, $I_{C}$ = - 2 mA - $V_{CE}$ = 5 V, $I_{C}$ = - 10 mA		- V <sub>BE</sub> - V <sub>BE</sub>	600 mV –			750 mV 720 mV
Collector-Base cutoff current – Kollektor-Basis-Reststrom						
- $V_{CB} = 30 \text{ V, (E open)}$ - $V_{CE} = 30 \text{ V, T}_{j} = 125^{\circ}\text{C, (E open)}$		- I <sub>CBO</sub> - I <sub>CBO</sub>	_ _	_ _		15 nA 4 μA
Emitter-Base cutoff current						
$-V_{EB} = 5 V$ , (C open)		- I <sub>EBO</sub>			_	100 nA
Gain-Bandwidth Product – Transitfrequenz						
- $V_{CE}$ = 5 V, - $I_{C}$ = 10 mA, f = 100 MHz		$f_{T}$	100 MHz	_		ı
Collector-Base Capacitance – Kollektor-Basis-Kapazität						
- $V_{CB}$ = 10 $V$ , $I_E$ = $i_e$ = 0, $f$ = 1 MHz	- $V_{CB}$ = 10 V, $I_E$ = $i_e$ = 0, f = 1 MHz		_	-		4.5 pF
Emitter-Base Capacitance – Emitter-Basis-Kapazität						
$- V_{EB} = 0.5 \text{ V}, I_{C} = i_{c} = 0, f = 1 \text{ MHz}$		$C_{\text{EB0}}$	_	9 pF		-
Noise figure – Rauschzahl						
$ \begin{array}{ll} \text{- $V_{CE} = 5$ V, $-$ I_{C} = 200$ $\mu$A} & \text{BC856} \dots \\ R_{G} = 2 \ k\Omega, \ f = 1 \ \text{kHz}, \ \Delta f = 200 \ \text{Hz} & \text{BC859} \dots \end{array} $		F	- -		dB ! dB	10 dB 4 dB
Thermal resistance junction to ambient air Wärmewiderstand Sperrschicht – umgebende Luft		$R_{\text{thA}}$	< 420 K/W <sup>1</sup> )		)	
Recommended complementary NPN transistors Empfohlene komplementäre NPN-Transistoren			BC846 BC850			
Stempelung der lieferbaren	BC856A BC856B BC856C	= 3B	BC857A = BC857B = BC857C =	3F	BC85	8A = 3E 8B = 3F 8C = 3G
			BC860B = BC860C = 3G or 4G		BC85	9B = 3F 9C or 4C

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Tested with pulses t<sub>p</sub> = 300 μs, duty cycle ≤ 2% - Gemessen mit Impulsen t<sub>p</sub> = 300 μs, Schaltverhältnis ≤ 2%
 Mounted on P.C. board with 3 mm² copper pad at each terminal Montage auf Leiterplatte mit 3 mm² Kupferbelag (Lötpad) an jedem Anschluss