NPN 100mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

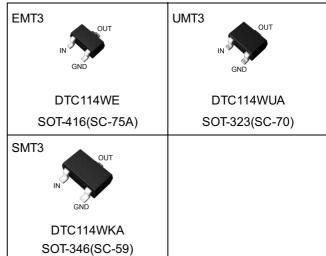
Datasheet

Parameter	Value
V <sub>CC</sub>	50V
I <sub>C(MAX.)</sub>	100mA
R <sub>1</sub>	10kΩ
R <sub>2</sub>	4.7kΩ

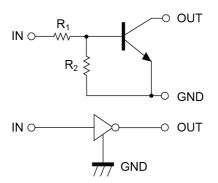
#### Features

- 1) Built-In Biasing Resistors
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary PNP Types: DTA114W series
- 6) Lead Free/RoHS Compliant.

## Outline



### •Inner circuit



### Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

### Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC114WE	EMT3	1616	TL	180	8	3000	84
DTC114WUA	UMT3	2021	T106	180	8	3000	84
DTC114WKA	SMT3	2928	T146	180	8	3000	84

# • Absolute maximum ratings ( $T_a = 25$ °C)

Parameter			Values	Unit
Supply voltage			50	V
Input voltage		V <sub>IN</sub>	-10 to 30	V
Output current			100	mA
Collector current	Collector current			mA
	DTC114WE		150	
Power dissipation	DTC114WUA	P <sub>D</sub> *2	200	mW
	DTC114WKA		200	
Junction temperature		T <sub>j</sub>	150	°C
Range of storage temperature		T <sub>stg</sub>	-55 to +150	°C

## • Electrical characteristics $(T_a = 25^{\circ}C)$

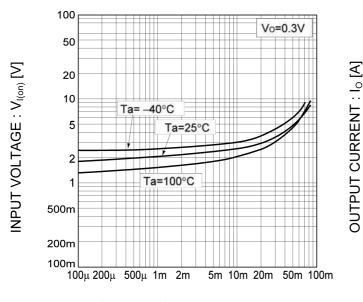
Downwater	Values		Unit				
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
lanut valtaga	$V_{l(off)}$	V <sub>CC</sub> = 5V, I <sub>O</sub> = 100μA	-	-	0.8	V	
Input voltage	V <sub>I(on)</sub>	$V_O = 0.3V$ , $I_O = 2mA$	3	-	-		
Output voltage	V <sub>O(on)</sub>	I <sub>O</sub> /I <sub>I</sub> = 10mA / 0.5mA	-	0.1	0.3	V	
Input current	I <sub>I</sub>	V <sub>I</sub> = 5V	-	-	0.88	mA	
Output current	I <sub>O(off)</sub>	V <sub>CC</sub> = 50V, V <sub>I</sub> = 0V	-	-	0.5	μA	
DC current gain	G <sub>I</sub>	V <sub>O</sub> = 5V, I <sub>O</sub> = 10mA	24	-	-	-	
Input resistance	R <sub>1</sub>	-	7	10	13	kΩ	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	0.37	0.47	0.57	-	
Transition frequency	f <sub>T</sub> *1	V <sub>CE</sub> = 10V, I <sub>E</sub> = -5mA, f = 100MHz	-	250	-	MHz	

<sup>\*1</sup> Characteristics of built-in transistor

<sup>\*2</sup> Each terminal mounted on a reference footprint

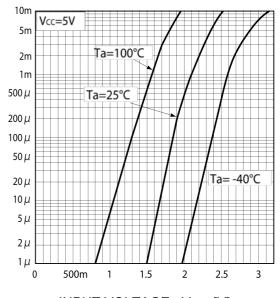
## ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)



OUTPUT CURRENT : Io [A]

Fig.2 Output current vs. input voltage (OFF characteristics)



INPUT VOLTAGE :  $V_{I(off)}[V]$ 

Fig.3 Output current vs. output voltage

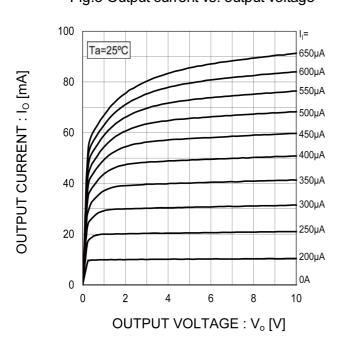
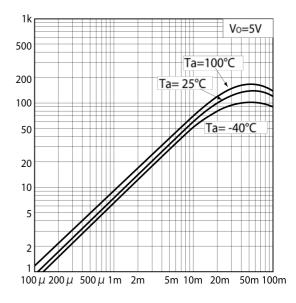


Fig.4 DC current gain vs. output current

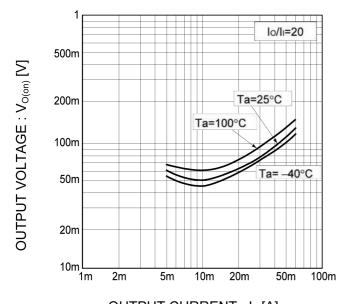


OUTPUT CURRENT: Io [A]

OC CURRENT GAIN: G

# ● Electrical characteristic curves (T<sub>a</sub> =25°C)

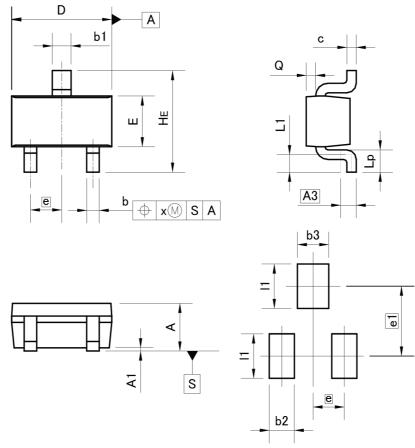
Fig.5 Output voltage vs. output current



OUTPUT CURRENT : Io [A]

### Dimensions

EMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	0.60	0.80	0.024	0.031	
A1	0.00	0.10	0.000	0.004	
A3	0.	25	0.0	10	
b	0.15	0.30	0.006	0.012	
b1	0.25	0.40	0.010	0.016	
С	0.10	0.20	0.004	0.008	
D	1.50	1.70	0.059	0.067	
E	0.70	0.90	0.028	0.035	
е	0.	50	0.0	20	
HE	1.40	1.80	0.055	0.071	
L1	0.10		0.004	-	
Lp	0.15	<b>5</b> 1	0.006	THE	
Q	0.05	0.25	0.002	0.010	
x	20	0.10	=	0.004	

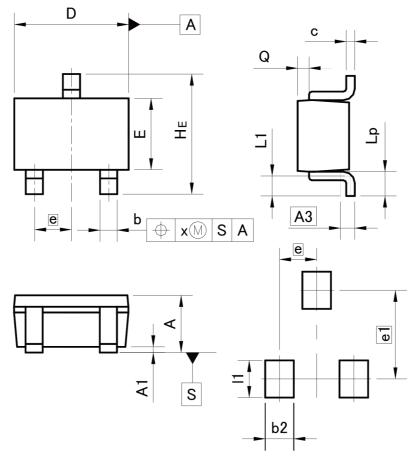
DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2	777//	0.40	54	0.016
b3	<del></del>	0.50	-	0.020
e1	1,	1.10		043
11	<b>#</b> 0	0.70	=	0.028

Dimension in mm/inches



### Dimensions

UMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	0.80	1.00	0.031	0.039
A1	0.00	0.10	0.000	0.004
A3	0.3	25	0.0	10
b	0.15	0.30	0.006	0.012
С	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.0	65	0.026	
HE	2.00	2.20	0.079	0.087
L1	0.20	0.50	0.008	0.020
Lp	0.25	0.55	0.010	0.022
Q	0.10	0.30	0.004	0.012
x	-	0.10	450	0.004

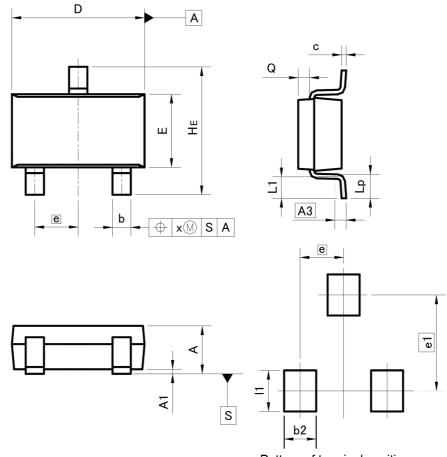
DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2	-	0.50	-	0.020
e1	1.55		0.0	061
11	-	0.65	_	0.026

Dimension in mm/inches



### Dimensions

SMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	1.00	1.30	0.039	0.051
A1	0.00	0.10	0.000	0.004
A3	0.	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.09	0.25	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.95		0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
x		0.10	200	0.004
У	<b>(2)</b>	0.10	1	0.004
[	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b2	<u>=</u>	0.60	-	0.024
e1	2 10		0.0	83

Dimension in mm/inches

11



0.035

0.90

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