

LL\_PLL\_ConfigSystemClock\_HSE

LL\_PLL\_ConfigSystemClock\_HSI

UTILS\_PLL\_IsBusy

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graph LR; A[LL_PLL_ConfigSystemClock_HSE] --> C[UTILS_PLL_IsBusy]; B[LL_PLL_ConfigSystemClock_HSI] --> C;
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The diagram illustrates a dependency or data flow. On the left, two white rectangular boxes are stacked vertically. The top box contains the text 'LL\_PLL\_ConfigSystemClock\_HSE' and the bottom box contains 'LL\_PLL\_ConfigSystemClock\_HSI'. On the right, there is a gray rectangular box containing the text 'UTILS\_PLL\_IsBusy'. Two blue arrows originate from the right side of the top and bottom white boxes, respectively, and point towards the left side of the gray box, indicating that both functions on the left interact with or depend on the variable 'UTILS\_PLL\_IsBusy'.