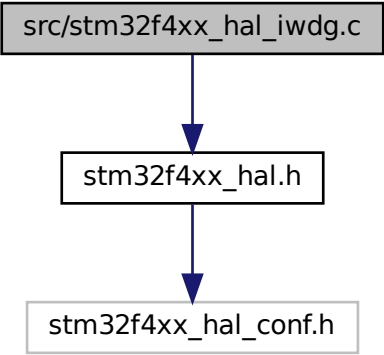


src/stm32f4xx\_hal\_iwdg.c



```
graph TD; A[src/stm32f4xx_hal_iwdg.c] --> B[stm32f4xx_hal.h]; B --> C[stm32f4xx_hal_conf.h];
```

stm32f4xx\_hal.h

stm32f4xx\_hal\_conf.h