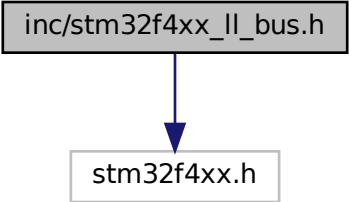


inc/stm32f4xx_ll_bus.h



```
graph TD; A[inc/stm32f4xx_ll_bus.h] --> B[stm32f4xx.h]
```

stm32f4xx.h