

LL_PLL_ConfigSystemClock_HSE

LL_PLL_ConfigSystemClock_HSI

UTILS_EnablePLLAndSwitch
System

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graph LR; A[LL_PLL_ConfigSystemClock_HSE] --> C[UTILS_EnablePLLAndSwitch System]; B[LL_PLL_ConfigSystemClock_HSI] --> C;
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The diagram illustrates a functional flow where two separate configuration functions, 'LL_PLL_ConfigSystemClock_HSE' and 'LL_PLL_ConfigSystemClock_HSI', serve as inputs to a single utility function, 'UTILS_EnablePLLAndSwitch System'. The input boxes are white with black borders, while the output box is shaded gray with a black border. Blue arrows indicate the direction of data flow from the inputs to the output.