TOTAL:/1	0
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ECE 543: Introduction to Digital Systems

Homework #3

Due: Friday, October 1st, 2021 (6 P.M.)

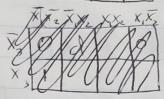
Student Name:
 Note: Please use this sheet as a cover page. Your work must be hand-written (no typing please). Homework must be submitted electronically through Canvas in a PDF format. Do the following problems from "Fundamentals of Digital Logic with Verilog Design" by Brown & Vranesic (3rd Edition)
Solve the following problems:
Problems from Chapter 2: 2.38, 2.40

ECE 543

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1	20
do	10

	X,	X	X3	[F]
	0	0	0	0
	0	0)	1
1	0	1	0	d
1	0	1	1	0
1	1	0	0	ť
1	1	0	1	d
1	4:	1	0	0
	1	1	1	1

	XzX3	XX3	X2×3	X2X2
x,	0	1	0\$	d
X	F	d	D	0
	-		-	



 $(\overline{\chi}_1 + \overline{\chi}_2) (\overline{\chi}_1 + \overline{\chi}_3) (\overline{\chi}_2 + \overline{\chi}_3)$

		-			
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11/1/2	.0	10	0	0	1
	0	0	0	1	d
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	0	9	1	I L	10
	0	1	0	0	0
1 (0	-11	0	1	0
	0	1	1	0	d
	.0	7	1	1	d
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	.1	0	0	1	1
4812	=1	0	1	0	
2003	16	0	11/1	1	0
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	11	1	0	1)	0
	, (1	1	0	()

	~ _	1			
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X, Xz	1	1	d	1	
$\bar{\chi}_{1} \chi_{2}$	0	0	(d)	d	
X, X2	0	0	1	0	
X, XZ	1.	1	0	1	
		1		. /	