TOTAL:/10	
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ECE 543: Introduction to Digital Systems

Homework #2

Due: Friday, September 24th, 2021 (6 P.M.)

Student Name:
Note: - Please use this sheet as a cover page Your work must be hand-written (no typing please) Homework must be submitted electronically through Canvas in a PDF format.
Do the following problems from "Fundamentals of Digital Logic with Verilog Design" by Brown & Vranesic (3^{rd} Edition)
Solve the following problems:
Problems from Chapter 2:

2.37, 2.54, 2.55



