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CSE 160 Introduction to Parallel Computing
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Task 1 Writeup

      I ran my job on TSCC on February 7, 2018 at around 4:30pm on node tscc-1-61. The number of flops is inversely proportional to the matrix size. As the matrix size increases, more time is spent retrieving the matrix data from memory and then performing the multiplication of data, instead of storing the smaller matrices in cached memory, which allows for more multiplications per second.

Time and Flops to Multiply N x N Matrices